

Advanced Product / Process Change Notice

No.: Z200-APCN-DM201501-01-P Date: 01/05/2015 Change Title: 1Gb (128Mb x8 and 64Mb x16) DDR2 technology migration from 65 to 46nm Change item : ☑ Design ☐ Raw Material ☐ Wafer FAB ☐ Package Assembly ☐ Testing ☐ Others Affected Product(s): W971GG6JB-18, W971GG6JB18A, W971GG6JB-25, W971GG6JB25A, W971GG6JB25I, W971GG6JB25K, W971GG6JB25L, W971GG6JB25T, W971GG6JB25W, W971GG6JB-3, W971GG6JB-3A, W971GG8JB-18, W971GG8JB-25, W971GG8JB25A, W971GG8JB25I, W971GG8JB25K, W971GG8JB-3, W971GG8JB-3A, W971GG8JB-3I Description of Change(s): Technology migration (65nm to 46nm) for 1Gb DDR2 Reason for Change(s): According to Winbond product roadmap, launch new 1Gb DDR2 with 46nm technology Impact of Change(s): (positive & negative) Form: No Change Fit: No Change Function: No Concern (Compatible between 65nm and 46nm) Reliability: On-going Hazardous Substances: No Concern (Pleae refer to attachment I) Qualification Plan/ Results: The new sample has passed ESD 2KV, latch-up 200mA and the full qualification is in progress. The result is expected out on March /6 / 2015. Implementation Plan: The follow-up disposition of 65nm 1Gb DDR2: 1) The date of Last-buy orders for the 65nm 1Gb DDR2: April. 30, 2015. 2) The last shipment date of the 65nm 1Gb DDR2: July ,31, 2015. ■ Date Code: onward ☐ Lot No: onward ☑ Proposed first ship date: 01/30/2015 Approval: (QA Originator: (QA Approval: Yu-Sung, cheng ytt Char (QRA Director) Sec. Manager) Dept. Manager)

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Table No.: 1110-0001-08-A



Contact for Questions & Concerns

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Customer Comments:

Note: Please sign this notice, and return to Winbond contact within **30** days. If no response is received within **30** days, this Change Request will be assumed to meet your approval.

■ Approval	☐ Disapproval	
Comment:		
Date:		
Dept. name:		
Person in charge:		