



Customer Information Notification

201901020I

Issue Date: 08-Feb-2019

Effective Date: 09-Feb-2019

Here's your personalized quality information concerning products Digi-Key purchased from NXP. For detailed information we invite you to view this notification online

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QUALITY

Change Category

- | | | | | |
|--|---|--|---|--|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

i.MX6SLL Data
Sheet Rev1.0
Updates

Description

NXP Semiconductors announces a data sheet update for the i.MX6SLL to revision 1.0. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

Data Sheet Changes:

IMX6SLLCEC: Changes include Items 1 to 8.

IMX6SLLIEC: Changes include Items 1 to 9.

1. Added the SD Host Controller information in the Table 2, "i.MX 6SLL Modules List".
2. Updated the input voltages for non-VBUS USB signals in the Table 7, "Absolute Maximum Ratings".
3. Added Vin/Vout for non-DDR pins in the Table 7, "Absolute Maximum Ratings".
4. Added a note in the Section 4.1.2, Thermal Resistance.
5. Updated the Table 19, "XTALI and RTC_XTALI DC Parameters".
6. Updated the SD2 min and max values in the Table 40, "eMMC4.4/4.41 Interface Timing Specification".
7. Updated the remark of GPANAIO in the Table 62, "13 x 13 mm Supplies Contact Assignment"/ "14 x 14 mm Supplies Contact Assignment".
8. Updated the IO and value of DRAM_SDCLK_0 in the Table 63, "13 x 13 mm Functional Contact

Assignments"/ "14 x 14 mm Functional Contact Assignments".

9. Changed K5 to K6 for NVCC_DRAM and K6 to K5 for NVCC_DRAM_2P5 in the Table 63, "14 x 14mm Supplies Contact Assignment".

The i.MX6SLL data sheet revision 1.0 is attached to this notice, and can be found at:

https://www.nxp.com/products/microcontrollers-and-processors/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-6-processors/i.mx-6sll-processors-single-core-processor-with-arm-cortex-a9-core:i.MX6SLL?tab=Documentation_Tab&linkline=Data-Sheet

Reason

The datasheet has been updated to correct errors and / or provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Grace Wang
Position Product Engineer
e-mail address grace.wang@nxp.com

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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NXP Semiconductors
High Tech Campus, 5656 AG Eindhoven, The Netherlands

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Changed Orderable Part#	Changed Part 12NC	Changed Part Number	Changed Part Description	Package Outline	Package Name	Status	Product Line
MCIMX6V7DVN10AB	935345954557	MCIMX6V7DVN10AB	i.MX6SLL	SOT1556-1	TFBGA432	RFS	Apps Processors
MCIMX6V2CVM08AB	935346039557	MCIMX6V2CVM08AB	i.MX6SLL	SOT1559-1	LFBGA400	RFS	Apps Processors