

# **Final Product Change Notification**

Issue Date: 09-Aug-2016 Effective Date: 21-Nov-2016

Here's your personalized quality information concerning products Digi-Key purchased from NXP. For detailed information we invite you to view this notification online

# 201607009F01



### **Management Summary**

Utilize ASEN as a Final Test Location for PCA9511ADP in order to enable full turn-key processing.

**Change Category** 

Change Calego	א וע y			
[] Wafer Fab	[] Assembly	[] Product Marking	[X] Test	[] Design
Process	Process		Location	
[] Wafer Fab	[] Assembly	[] Mechanical Specification	[]Test Process	[] Errata
Materials	Materials			
[] Wafer Fab	[] Assembly	[]	[] Test	[] Electrical spec./Test
Location	Location	Packing/Shipping/Labeling	Equipment	coverage

Add ASEN as an additional Final Test Location for PCA9511ADP

## **Details of this Change**

Enable ASEN as an ATE (test) location of the PCA9511ADP to complete turn-key processing.

#### Why do we Implement this Change

As a JV and strategic partner, ASEN, which is located in Suzhou, China has been supporting the assembly

test of numerous BL SIP products in both assembly only and full turn-key formats. Adding/Implementing the ATE

step allows for NXP and the BL to optimize assembly and test capacity by improving flexibility for production planning.

Assembly was previously qualified but enabling ATE-FT step will allow for full turn-key processing.

#### **Identification of Affected Products**

Products tested in ASEN are identified via NXP internal traceability. And while the product identification does not change, an improvement is under consideration to provide additional traceability.

## **Product Availability**

# Sample Information

Samples are available upon request

Production

Planned first shipment 10-Oct-2016

#### **Impact**

no impact to the product's functionality anticipated.

#### **Data Sheet Revision**

No impact to existing datasheet

# **Disposition of Old Products**

Existing inventory will be shipped until depleted

#### **Timing and Logistics**

Your acknowledgement of this change, conform JEDEC JESD46 D, is expected till 08-Sep-2016.

# **Contact and Support**

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Jose Arauz

Position Quality Engineer BL SIP e-mail address jose.d.arauz@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they

reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

#### **About NXP Semiconductors**

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

View Notification	Subscription	Support
-------------------	--------------	---------

NXP | Privacy Policy | Terms of Use

**NXP Semiconductors** 

High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006-2010 NXP Semiconductors. All rights reserved.

#### **Affected Part Number**

PCA9511ADP,118