Product Change Notification - SYST-31YNCZ069

Date:

04 Jun 2019

Product Category:

32-bit Microcontrollers

Affected CPNs:

Notification subject:

Data Sheet - PIC32MZ Graphics (DA) Family Data sheet

Notification text:

SYST-31YNCZ069

Microchip has released a new DeviceDoc for the PIC32MZ Graphics (DA) Family Data sheet of devices. If you are using one of these read the document located at PIC32MZ Graphics (DA) Family .

Notification Status: Final

Description of Change:

This revision includes the following major changes, which are referenced by their respective chapter in the following table. In addition, minor updates to text and formatting were incorporated throughout the document.

Section Name	Update Description
Table TABLE 2:169-pin LFBGA PIC32MZ DA Features	Package information was added
Table Table 1-2:Oscillator Pinout I/O Descriptions	SOSCO description was added
Table Table 1-6:PORTA through PORTK Pinout I/O Descriptions	Pin Number for 176-pin LQFP was corrected
Table Table 1-13:EBI Pinout I/O Descriptions	Pin Number for 176-pin LQFP was corrected
Table Table 4-5:Timing Parameters	Timing parameters
Register 8-6 PBxDIV: Peripheral Bus ‘x' Clock Divisor Control Register (‘x' = 1-7)	Added Note 2
Register 11-9 USBIENCSR1: USB Indexed Endpoint Control Status Register 1 (Endpoint 1-7)	Note added
Register 11-13 USBOTG: USB OTG Control/Status Register	4-3 was corrected
Register 11-25 USBDPBFD: USB Double Packet Buffer Disable Register	Register were corrected
Register 23-1 I2CxCON: I2C Control Register	In bit16 and bit 18 the name SCKREL is corrected to read SCLREL
Register 25-2 PMMODE: Parallel Port Mode Register	Note added for 14-13 IRQM<1:0>
Register 25-7 PMSTAT: Parallel Port Status Register (Slave modes only)	• Note added for bit 14 IBOV • Note added for bit 6 OBUF
28.0 “Random Number Generator (RNG)”	LSFR was corrected to read LFSR
Register 29-3 ADCCON3: ADC Control Register 3	15-13 bits definition was corrected
Register 29-16 ADCFLTRx: ADC Digital	Note added for bit 31 AFEN

Filter ‘x' Register (‘x' = 1 through 6)	
39.0 “Secure Digital Host Controller (SDHC)”	Added Note 2.
Register 41-5 DEVCFG2/ADEVCFG2: Device Configuration Word 2	Note added for bit 28 FDSEN
44.0 “Electrical Characteristics”	• Maximum current rating added for VDDCORE and VDDR1V8 • ESD Qualification added
Table Table 44-2:Thermal Operating Conditions	Formula added DDR2 version
Table Table 44-5:Electrical Characteristics: RESETS	Parameter numbers RST10 and RST11 corrected and Note added for RST11
Table Table 44-17:DC Characteristics: DDR2 SDRAM Memory	Maximum current rating reduced for some parameters
Table Table 44-18:Comparator Specifications	CMRR and VICM values corrected

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 04 Jun 2019

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A
Attachment(s):
PIC32MZ Graphics (DA) Family Data sheet

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

PIC32MZ1025DAA169-I/HF
PIC32MZ1025DAA169T-I/HF
PIC32MZ1025DAA176-I/2J
PIC32MZ1025DAA176T-I/2J
PIC32MZ1025DAA288-I/4J
PIC32MZ1025DAA288T-I/4J
PIC32MZ1025DAB169-I/HF
PIC32MZ1025DAB169T-I/HF
PIC32MZ1025DAB176-I/2J
PIC32MZ1025DAB176T-I/2J
PIC32MZ1025DAB288-I/4J
PIC32MZ1025DAB288T-I/4J
PIC32MZ1025DAG169-I/6J
PIC32MZ1025DAG169T-I/6J
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PIC32MZ1025DAG176T-I/2J
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