

Product Change Notification - SYST-28GIIG219

Date:

01 Oct 2018

Product Category:

32-bit Microcontrollers

Affected CPNs:

Notification subject:

Data Sheet - PIC32MZ Graphics (DA) Family Datasheet

Notification text:

SYST-28GIIG219

Microchip has released a new DeviceDoc for the PIC32MZ Graphics (DA) Family Datasheet of devices. If you are using one of these devices please read the document located at [PIC32MZ Graphics \(DA\) Family Datasheet](#).

Notification Status: Final

Description of Change:

- 1) Minor updates to text and formatting were incorporated throughout the document.
- 2) This revision includes the following major changes, which are referenced by their respective chapter in the following table.

| Section Name | Update Description |
|---|---|
| TABLE 2: &ldquo;169-pin LFBGA PIC32MZ DA Features&rdquo; , TABLE 3: &ldquo;176-pin LQFP PIC32MZ DA Features&rdquo; , and TABLE 4: &ldquo;288-pin LFBGA PIC32MZ DA Features&rdquo; ; | Updated three tables with package information |
| 4.0 &ldquo;Memory Organization&rdquo; ; | The PIC32MZ DA Family Memory Map was updated (see Figure 4-1). |
| Register 6-3: &ldquo;RNMICON: Non-Maskable Interrupt (NMI) Control Register&rdquo; ; | Added attributes and new note. |
| TABLE 12-1: &ldquo;Input Pin Selection&rdquo; ; | Corrected peripheral pin name. |
| Register 17-3: &ldquo;DMTCLR: Deadman Timer Clear Register&rdquo; ; and Register 17-4: &ldquo;DMTSTAT: Deadman Timer Status Register&rdquo; ; | Corrected and added description of bits. |
| Register 28-4: &ldquo;RNGNUMGENx: Random Number Generator Register &lsquo;x' (&lsquo;x' = 1 or 2)&rdquo; ; and Register 28-5: &ldquo;RNGSEEDx: True Random Number Generator Seed Register &lsquo;x' (&lsquo;x' = 1 or 2)&rdquo; ; | Added Note to both registers. |
| Register 34-1: &ldquo;HLVDCON: High/Low-Voltage Detect Control Register&rdquo; ; | Corrected Pin Description for HLVDL bits. |
| FIGURE 39-1: &ldquo;Secure Digital Host Controller (SDHC) Block Diagram&rdquo; ; | Pin name corrected to SDDATA3. |
| FIGURE 40-1: &ldquo;XLP Device Block Diagram&rdquo; ; | Corrected text in figure. |
| Register 40-1: &ldquo;DSCON: Deep Sleep | Corrected Bit 1 DSBOR description |



| | |
|--|---|
| Control Register | |
| Table 44-2: Thermal Operating Conditions | Corrected the Formula for PINT and PI/O |
| TABLE 44-6: Low-Voltage Detect Characteristics | Removed bitfields 1100, 1101, and 1110 as they are reserved. |
| Communication Interfaces | Corrected Lin support from LIN1.2 to LIN2.1 |
| Table 1-6: PORTA through PORTK Pinout I/O Descriptions | RB14 pin for LQFP176 corrected from 175 to 159 |
| Table TABLE 4-3: Boot Flash 1 Sequence and Configuration Words Summary | Table Note Updated |
| Table TABLE 4-4: Boot Flash 2 Sequence and Configuration Words Summary | Table Note Updated |
| Table Register 8-4: REFOxCON: Reference Oscillator Control Register (x' = 1-4) | Bit 30-16 explained in detail |
| Table Register 21-1: SPIx CON: SPI Control Register | Bit 29 Description Corrected |
| Table Register 26-1: EBICSx: External Bus Interface Chip Select Register (x' = 0-3) | Note added |
| Table Register 36-16: GLCDSTAT: Graphics LCD Controller Status Register | Note Added for bit 3 and bit 2 |
| Table Register 41-13: CFGCON2: Configuration Control Register 2 | Bit 0 definition corrected |
| Table Table 44-23: External Clock Timing Requirements | OS42 detail added |
| Table Table 44-53: Ethernet Module Specifications | Corrected parameter ET13 name from setup and Hold to output Delay |
| 46.1 Package Marking Information | Updated package marking information |

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 01 Oct 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[PIC32MZ Graphics \(DA\) Family Datasheet](#)

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Affected Catalog Part Numbers (CPN)

PIC32MZ1025DAA169-I/HF
PIC32MZ1025DAA169T-I/HF
PIC32MZ1025DAA176-I/2J
PIC32MZ1025DAA176T-I/2J
PIC32MZ1025DAA288-I/4J
PIC32MZ1025DAA288T-I/4J
PIC32MZ1025DAB169-I/HF
PIC32MZ1025DAB169T-I/HF
PIC32MZ1025DAB176-I/2J
PIC32MZ1025DAB176T-I/2J
PIC32MZ1025DAB288-I/4J
PIC32MZ1025DAB288T-I/4J
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