

Product Change Notification - SYST-18KAOU710

Date:

22 Jul 2019

Product Category:

32-bit Microcontrollers

Affected CPNs:

Notification subject:

Data Sheet - SAM D5X/E5X Family Datasheet Data Sheet Document Revision

Notification text:

SYST-18KAOU710

 Microchip has released a new DeviceDoc for the SAM D5X/E5X Family Datasheet of devices. If you are using one of these read the document located at [SAM D5X/E5X Family Datasheet](#).

Notification Status: Final

Description of Change: This revision includes the following changes

Section Name	Update Description
Introduction	Updated sections: <ul style="list-style-type: none"> &bull; Updated Features &bull; Updated the Configuration Summary
Processor and Architecture	Updated Interrupt Line Mapping
CMCC	Updated sections: <ul style="list-style-type: none"> &bull; Data Cache Disable &bull; Instruction Cache Disable
GCLK	Updated the PCHCTRLm register
PM	Updated Backup Mode
RTC	Count32 Registers updated: <ul style="list-style-type: none"> &bull; CTRLA &bull; EVCTRL &bull; INTENCLR &bull; INTENSET &bull; INTFLAG &bull; SYNCBUSY &bull; Count16 Registers updated:

	<ul style="list-style-type: none"> &bull; CTRLA &bull; EVCTRL &bull; INTENCLR &bull; INTENSET &bull; INTFLAG &bull; SYNCBUSY &bull; <p>The following Clock registers were updated:</p> <ul style="list-style-type: none"> &bull; CTRLA &bull; EVCTRL &bull; INTENCLR &bull; INTENSET &bull; INTFLAG &bull; SYNCBUSY &bull;
DMAC	<p>The following topics were updated:</p> <ul style="list-style-type: none"> &bull; Initialization &bull; Enabling, Disabling and Resetting &bull; Transfer Descriptors <p>The following registers were updated:</p> <ul style="list-style-type: none"> &bull; CTRL &bull; SWTRIGCTRL &bull; INTSTATUS &bull; BUSYCH
GMAC	<p>The following topics were updated:</p> <ul style="list-style-type: none"> &bull; Features

	<ul style="list-style-type: none"> &bull; Pause Frame Reception
OSCCTRL	<p>The following topics were updated:</p> <ul style="list-style-type: none"> &bull; Digital Phase Locked Loop (DPLL) Operation
SERCOM-SPI	<p>Updated the following topics:</p> <ul style="list-style-type: none"> &bull; Clock Generation
QSPI	<p>Updated the following topics:</p> <ul style="list-style-type: none"> &bull; Continuous Read Mode
SDMMC	<p>Updated the following registers:</p> <ul style="list-style-type: none"> &bull; PSR &bull; PCR &bull; NISTR &bull; NISTER &bull; HC2R EMMC &bull; HC2R SDIO &bull; CA0R &bull; CA1R &bull; MCCAR &bull; ASAR &bull; &bull; MC1R &bull; ACR
ADC	Updated the INPUTCTRL register
AC	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; Signal Description
TC	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; Counter Mode
TCC	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; Capture Operations <p>Updated the following registers:</p> <ul style="list-style-type: none"> &bull; WAVE
I2S	Updated the following sections:

	<ul style="list-style-type: none"> &bull; DMA Operation <p>Updated the following registers:</p> <ul style="list-style-type: none"> &bull; CTRLA &bull; INTENCLR &bull; INTENSET &bull; INTFLAG &bull; SYNCBUSY &bull; TXCTRL &bull; RXCTRL
<p>Electrical Characteristics at 85&deg;C</p>	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; General Operating Ratings &bull; Injection Current &bull; Power Consumption &bull; Analog-to-Digital Characteristics &bull; Digital-to-Analog Converter Characteristics <p>Added in :</p> <ul style="list-style-type: none"> &bull; PTC Characteristics
<p>Electrical Characteristics at 105&deg;C</p>	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; Power Consumption &bull; Digital-to-Analog Converter Characteristics &bull; Analog-to-Digital Characteristics <p>Added new section:</p> <ul style="list-style-type: none"> &bull; PTC Characteristics
<p>Electrical Characteristics at 125&deg;C</p>	<p>Updated the following sections:</p> <ul style="list-style-type: none"> &bull; Power Consumption &bull; Analog-To-Digital Characteristics &bull; Digital-to-Analog Characteristics

	Added new section:
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	• PTC Characteristics
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Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 22 Jul 2019

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[SAM D5X/E5X Family Datasheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATSAMD51G18A-MF
ATSAMD51G18A-MFT
ATSAMD51G18A-MU
ATSAMD51G18A-MU-EFP
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