Product Change Notification - SYST-14OCHA716

Date:

17 Dec 2018

Product Category:

Microprocessors

Affected CPNs:

Notification subject:

Data Sheet - SAMA5D2 System-In-Package (SIP) MPU with up to 1 Gbit DDR2 SDRAM

Notification text:

SYST-14OCHA716

Microchip has released a new DeviceDoc for the SAMA5D2 System-In-Package (SIP) MPU with up to 1 Gbit DDR2 SDRAM of devices. If you are using one of these devices please read the document located at SAMA5D2 System-In-Package (SIP) MPU with up to 1 Gbit DDR2 SDRAM.

Notification Status: Final

Description of Change:

1) Added 1 Gbit and 2 Gbit LPDDR2 memory options. Added 361-ball TFBGA package option and mechanical drawing.
2) Pinout: added PTC signals.
3) Added section Electrical Characteristics

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 01 Nov 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

SAMA5D2 System-In-Package (SIP) MPU with up to 1 Gbit DDR2 SDRAM

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATSAMA5D225C-D1M-CU
ATSAMA5D225C-D1M-CUR
ATSAMA5D27C-CN
ATSAMA5D27C-CNR
ATSAMA5D27C-CNRVAO
ATSAMA5D27C-CNVAO
ATSAMA5D27C-CU
ATSAMA5D27C-CUR
ATSAMA5D27C-D1G-CU
ATSAMA5D27C-D1G-CUR
ATSAMA5D27C-D5M-CU
ATSAMA5D27C-D5M-CUR
ATSAMA5D28C-CN
ATSAMA5D28C-CNR
ATSAMA5D28C-CU
ATSAMA5D28C-CUR
ATSAMA5D28C-D1G-CU
ATSAMA5D28C-D1G-CUR