

Product Change Notification - SYST-07NUDB571

Date:	12 Dec 2017
Product Category:	32-bit PIC Microcontrollers
Affected CPNs:	 
Notification subject:	Data Sheet - SAM D20 Datasheet Data Sheet Document Revision
Notification text:	SYST-07NUDB571

Microchip has released a new DeviceDoc for the SAM D20 Datasheet of devices. If you are using one of these devices please read the document located at [SAM D20 Datasheet](#).

Notification Status: Final

Description of Change:

- 1) General update: This revision contains updates to restore content that was omitted in the previous version of the document, and to remove content that is not applicable to this device family.
- 2) Restored Column F in Table 6-1, Power Domain Overview, Power Supply Connection (see Figure 7-1), PAC2 Write Protect Clear register bits (see WPCLR), PAC2 Write Protect Set register bits (see WPSET), and DSU Block Diagram (see Figure 12-1)
- 3) Removed DMAC and DMA references were removed throughout the document, RWW and RWWEE references were removed throughout the Non-Volatile Memory Controller chapter and elsewhere in the document
- 4) NVM User Row Mapping The NVM User Row Mapping table was updated (see Table 9-3).
- 5) Electrical Characteristics at 85°C The Bandgap (Internal 1.1V reference) Characteristics were updated (see Table 32-34).

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 12 Dec 2017

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [SAM D20 Datasheet](#)

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Affected Catalog Part Numbers (CPN)

ATSAMD20G14A-MUTA4
ATSAMD20G14A-MUTA2
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