

# $PIC16C54A \rightarrow PIC16C54C \ Migration$

## **DEVICE MIGRATIONS**

This document is intended to describe the functional differences and the electrical specification differences that are present when migrating from one device to the next.

**Note:** This device has been designed to perform to the parameters of its data sheet. It has been tested to an electrical specification designed to determine its conformance with these parameters. Due to process differences in the manufacture of this device, this device may have different performance characteristics than its earlier version. These differences may cause this device to perform differently in your application than the earlier version of this device.

Table 1 shows the considerations that must be taken into account when migrating from the PIC16C54A to the PIC16C54C.

## TABLE 1: PIC16C54A $\rightarrow$ PIC16C54C DIFFERENCES

Functional Differences									
No.	Difference	H/W	S/W	Prog.					
1	Master Clear Filter added, PIC16C54C. See Electrical Specification #30	~	—	—					
2	Code protection change, PIC16C54C now reads as 0 when code protect enabled	—	_	~					
3	Programming algorithm change, PIC16C54C uses a new programming algorithm	_		~					

Parm. No.	Sym.	Characteristic	PIC16C54A Data Sheet		PIC16C54C Data Sheet			11.1.160	Conditions	
			Min	Тур	Max	Min	Тур	Max	Units	Conditions
	Vdd	Supply Voltage								
		XT, RC and LP Options	3.0	—	6.25	3.0	—	5.5	V	
		LP Option	2.5	_	6.25	2.5	_	5.5	V	
		HS option	4.5	—	5.5	4.5	—	5.5	V	
	IDD	Supply Current								Fosc=32kHz
		LP Option, Commercial	—	14	29	_	14	32	μA	VDD=3.0V
		LP Option, Industrial	—	17	37	_	17	40	μA	WDT Disabled
		LP Option Industrial	—	11	35	—	14	35	μΑ	Note 1
	IPD	Power Down Current								VDD=3.0V
		Industrial	—	5.0	14	—	4.0	14	μΑ	WDT Enabled
			—	0.3	5.0	—	0.25	5.0	μΑ	WDT Disabled VDD=3.5V
		Extended	—	5.0	22	_	4.5	22	μΑ	WDT Enabled
			—	0.8	18	—	0.3	18	μA	WDT Disabled
	VIL	Input Low Voltage								
		I/O Ports	Vss	_	0.8					4.0V <vdd≤5.5< td=""></vdd≤5.5<>
			Vss	—	0.2 Vdd					For all VDD
						Vss	—	0.8	V	4.5V <vdd≤5.5< td=""></vdd≤5.5<>
						Vss	_	0.15 Vdd	V	Otherwise
	Vін	Input High Voltage								
		I/O Ports	2.0	_	Vdd					4.0V <vdd≤5.5< td=""></vdd≤5.5<>
			0.2 Vdd+1V	_	Vdd					For all VDD
						2.0	—	Vdd	V	4.5V <vdd≤5.5< td=""></vdd≤5.5<>
					1	0.25Vdd+.8V	—	Vdd	V	Otherwise

Note 1: Fosc=32kHz, VDD=3.0V, WDT disabled

**Note:** The user should verify that the device oscillator starts and performs as expected. Adjusting the loading capacitor values and /or the oscillator mode may be required.

### Note the following details of the code protection feature on PICmicro<sup>®</sup> MCUs.

- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable".
- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

If you have any further questions about this matter, please contact the local sales office nearest to you.

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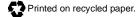
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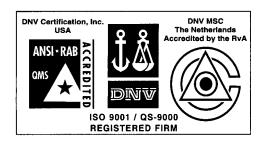
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