

PCN Number: RC140304B (Revised 03/31/14) Notification Date*: February 19, 2014 See Below Correction and Addendum in Blue Text

Title: Migration to Marketing Revision Level B of the AT91SAM7X512								
Product Identification:								
	Current Part Number		New Part Number					
	AT91SAM7X512-AU		AT91SAM7X512B-AU					
	AT91SAM7X512-AU-999		AT91SAM7X512B-AUR					
	AT91SAM7X512-CU		AT91SAM7X512B-CU					
Reason for Change:		Material / Comp		oosition	🛛 Manı	ufacturing Location		
		🛛 Processing / Ma		nufacturing 🛛 🗌 Quality		ity / Reliability		
		🛛 Design / Firmwa		are	🛛 Logis	stics		
		🛛 Datasheet			Other			
Change Description: A complete list of differences between the marketing revision level (MRL) A and B can be found in chapter 41.2 of the AT91SAM7X series datasheet with Atmel document reference 6120K. The ChipID has been incremented to: AT91SAM7X512 Revision B chip ID is 0x275C 0A41 The Flash memory access time has been increased as per the below table: Flash Wait State Read Operations Maximum Operating Frequency (MHz) 0 1 cycle 1 2 cycles								
2		3 cycles		48				
3		4 cycles		55				
The rev B is manufactured at UMC. Samples are available in Atmel Sample Center: https://samples.atmel.com/scripts/samplecenter.dll?atmel?cmd=menu								
Identification Method to Distinguish Change:								
Marketing Revision Level (MRL) has been incremented on the top side marking to B.								
Qu Dat		🛾 Available		☐ Will be availa (mm/dd/yr):	able	□ Not Applicable		
Sar	mples:			☐ Will be availa (mm/dd/yr):	able	□ Not Applicable		

Quantifiable Impact on Quality & Reliability: None

Forecasted Availability Date: February 20, 2014

Last Time Buy Date: May 20, 2014

Last Ship Date: February 20, 2015

*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).

Atmel Contact: Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include the PCN number in subject line).

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