

PCN / EOL Notification

Product Change Notification Number: RC121502 Notification Date*: May 10, 2012

Title: Migration to Marketing Revision Level B of the AT91SAM7SE32							
Product Identification:							
Current Part Num	ber N	New Part Number					
AT91SAM7SE32-AU	AT91S	AT91SAM7SE32B-AU					
AT91SAM7SE32-AU-	999 AT91S	SAM7SE32B-AUR					
AT91SAM7SE32-CU	AT91S	91SAM7SE32B-CU					
AT91SAM7SE32-CU-	999 AT91S	1SAM7SE32B-CUR					
Reason for	terial / Composit	ion 🖂	Desi	gn / Firmware	× N	Manufacturing Location	
Change:	cessing / Manuf		Logis			Quality / Reliability	
		- 🖂				tuanty / Tronability	
Change Description:	b -t t		اما اما	ial (MDL) A and	D	he formal in abouter 10	
A complete list of differences between the marketing revision level (MRL) A and B can be found in chapter 43 of the datasheet of the AT91SAM7SE-series with Atmel document reference 6222H.							
of the datasheet of the 7110 107 thin 62 defined with 7 thine decament reference decement							
Multiple errata have been corrected on the ADC, PWM, RTT, SDRAM controller, SPI, SSC, TWI and USART.							
The pull-up resistor range has changed slightly as per table 40.2							
The ChipID has been incremented to: 0x2728 0341							
The product is manufactured in 0.15 micron embedded Elech technology in LIMC Taiwan							
The product is manufactured in 0.15micron embedded Flash technology in UMC Taiwan.							
Identification Method to Distinguish Change:							
Marketing Revision Level (MRL) has been incremented on the top side marking to B.							
Qualification Data:		☐ Will be availab	ole (m	m/dd/yr):			
Samples:		☐ Will be availab	ole (m	m/dd/yr):		☐ Not Applicable	
Quantifiable Impact on Quality & Reliability:							
None							
Forecasted Availability Date: Now							
Last Time Buy Date: November 15, 2012							
Last Ship Date: May 15, 2013							
*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).							
Atmel Contact: pcnadm@atmel.com							

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