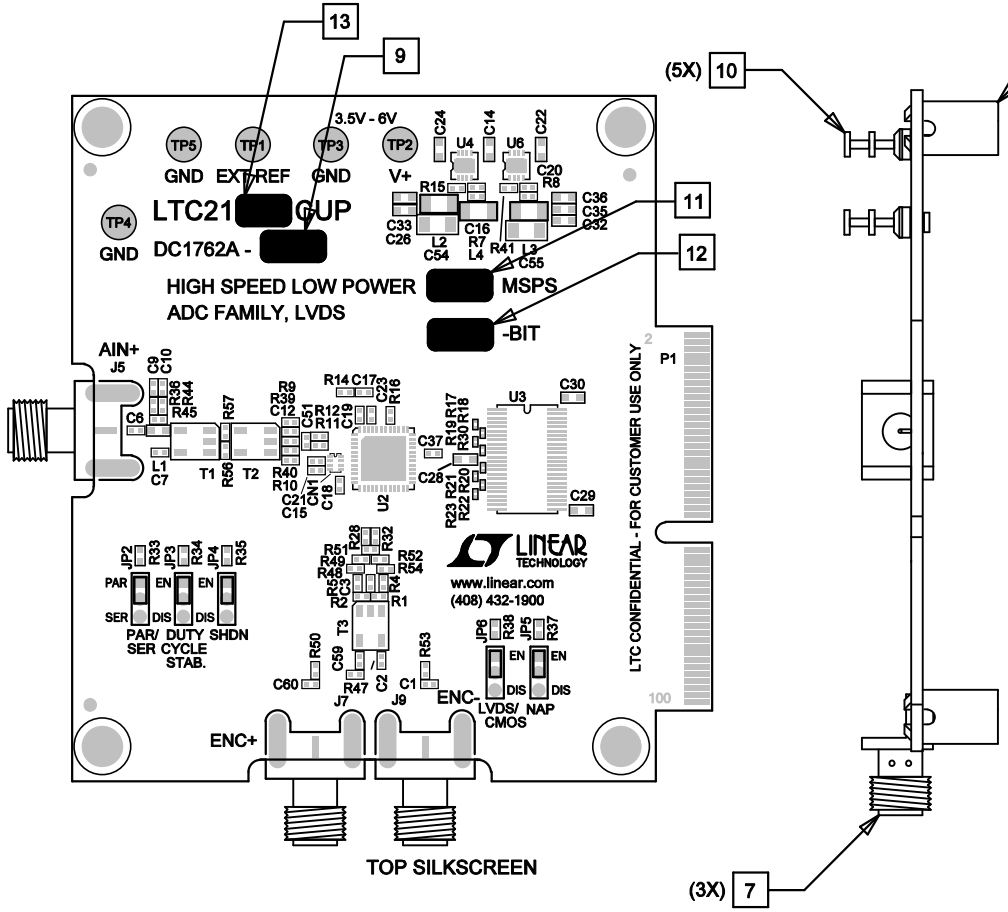

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVAL	DATE
	1	FIRST PROTOTYPE	CLARENCE M.	12/10/10

NOTES: UNLESS OTHERWISE SPECIFIED,

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL CONNECTOR P/N 142-0701-846 AS SHOWN;
8. INSTALL 4 STANDOFFS AT 4 CORNERS AS SHOWN;
9. MARK APPROPRIATE ASEEMBLY LETTER.
10. INSTALL TURRETS AS SHOWN.
11. MARK APPROPRIATE MSPS
12. MARK APPROPRIATE BIT
13. MARK APPROPRIATE IC. NUMBER

ASSY	U2	SAMPLE RATE	No. of BITS	FREQUENCY R
-A	LTC2165CUP	125Msps	16	5< AIN < 170
-B	LTC2164CUP	105Msps	16	5< AIN < 170
-C	LTC2163CUP	80Msps	16	5< AIN < 170
-D	LTC2162CUP	65Msps	16	5< AIN < 170
-E	LTC2161CUP	40Msps	16	5< AIN < 170
-F	LTC2160CUP	25Msps	16	5< AIN < 170

APPROVALS	
PCB DES.	MI
APP. ENG.	CLARENCE M.

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LTC CONFIDENTIAL-
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TITLE: TOP ASSEMBLY DRAWING,
HIGH SPEED LOW POWER
ADC FAMILY, LVDS

SIZE	IC NO	REV
N/A	LTC21XXCUP FAMILY DEMO CIRCUIT 1762A	1

SCALE: NONE	FILENAME: 1762A-1.PCB	SHT 1 of 2