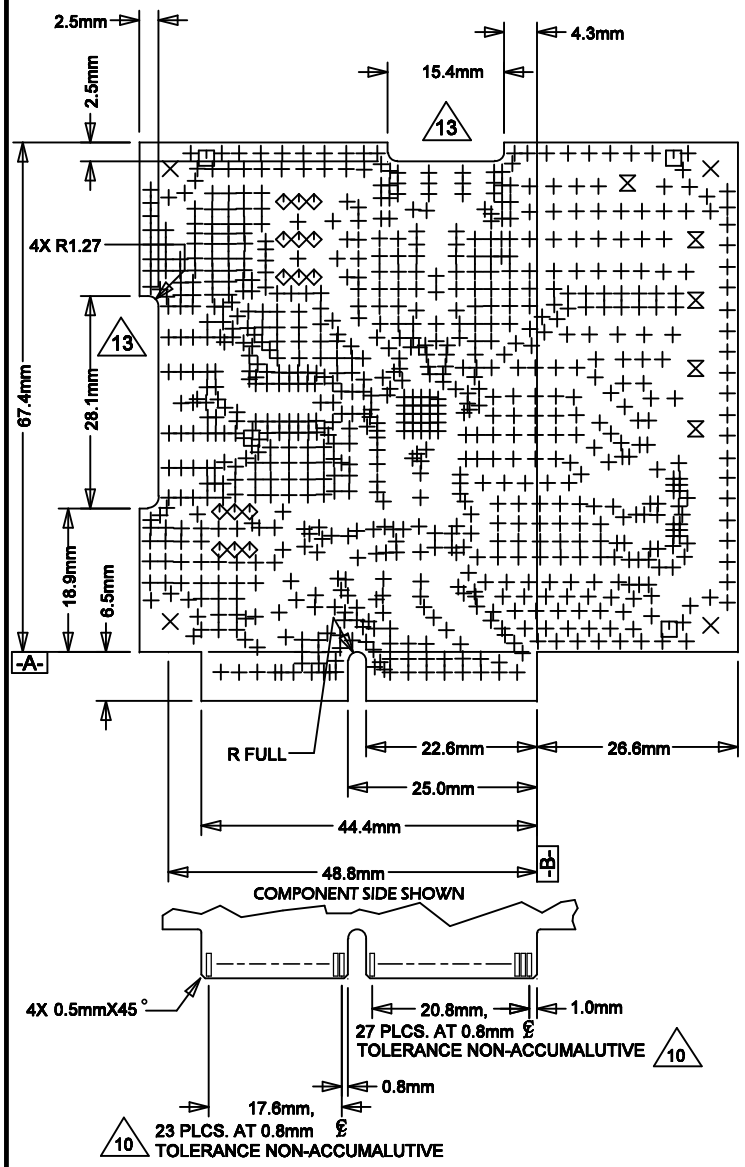
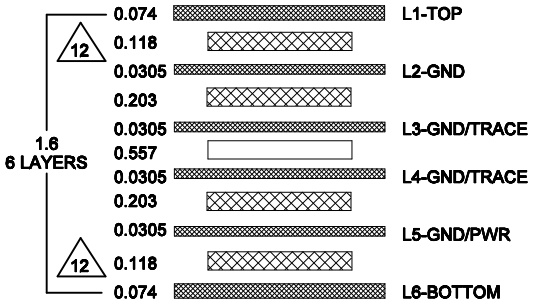
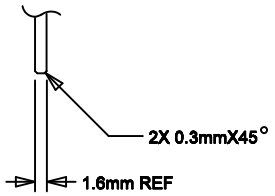
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVAL	DATE
	1	FIRST PROTOTYPE	CLARENCE M.	12/10/10



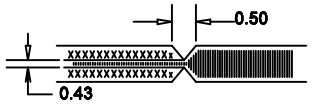
LAYER STRUCTURE





NOTES: UNLESS OTHERWISE SPECIFIED,

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, ISOLA FR-370HR
   -FINISHED THICKNESS TO BE 1.6 +/-0.1
   -TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
   -FLAMMABILITY RATING: 94 V-O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
4. DRILLING : -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   -HOLE LOCATION TOLERANCES ARE +/-0.08 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   -GOLD IMMERSION BOTH SIDES.
   -FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.74 PAD, 0.64 SOLDER MASK, AND 0.61 SOLDER PASTE. DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

10 FINGER AREA: GOLD PLATE TO MIN. OF 0.00008. PER MIL-G-45204, TYP II, CL1, GRADE C, OVER 0.005 LOW STRESS NICKEL PER QQ-N-290, CL2.

11. CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYERS 1-3 USING 0.762 TRACE.

12 SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

13 INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).

SIZE	QTY	SYM	PLATED	TOL
0.25	857	+	YES	+/-0.08
0.8	15	◇	YES	+/-0.08
1.8	3	□	NO	+/-0.13
2.4	5	⊠	YES	+/-0.13
4.7	4	×	NO	+/-0.13

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS
TOLERANCES ON ANGLE ±1°
.XX ± 0.254
.XXX ± 0.127
INTERPRET DIM AND TOL PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

DO NOT SCALE DRAWING

APPROVALS	
PCB DES.	MI
APP. ENG.	CLARENCE M.

SCALE: NONE

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FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING,
HIGH SPEED LOW POWER
ADC FAMILY, LVDS

SIZE	IC NO	REV
N/A	LTC21XXCUP FAMILY DEMO CIRCUIT 1762A	1

FILENAME: 1762A-1.PCB

SHT 1 of 1