User's Guide
SLOU286–December 2009

TAS5611/13PHD2EVM

This user's guide describes the operation of the evaluation module for the TAS5613 150W (TAS5611 125W) Stereo Feedback Analog-Input Digital Amplifiers from Texas Instruments. The user's guide also provides measurement data and design information including the schematic, BOM, and PCB layout.

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1 Overview

The TAS5611/13PHD2EVM PurePath™ Premier Pro customer evaluation module demonstrates the integrated circuit TAS5611 or TAS5613PHD from Texas Instruments (TI).

The TAS5611 and TAS5613PHD is high-performance, integrated Stereo Feedback Analog-Input Digital Amplifier Power Stages designed to drive 4Ω speakers at up to 150W per channel for TAS5613PHD and 125W per channel for TAS5611PHD. This amplifier requires only a simple passive demodulation filter to deliver high-quality, high-efficiency audio amplification.

This EVM is configured with 2 BTL channels and the possibility to apply either a single ended or a differential analog input signal. It is also possible to configure the two BTL channels into one parallel BTL (PBTL) channel.

The OPA1632 is a High Performance Fully Differential Audio Op Amp designed to allow operation with single ended or differential input signals to the EVM.

This EVM stuffed with either TAS5611PHD or TAS5613PHD is a complete stereo analog input power amplifier ready for evaluation and great music.

Table 1. TAS5611/13PHD2EVM Specification

Key Parameters	
TAS5613 Output stage supply voltage	18 V – 36V
TAS5611 Output stage supply voltage	16V - 32.5V
Number of channels	2 × BTL or 1 x PBTL
Load impedance BTL	4–8 Ω
Load impedance PBTL	2–3 Ω
TAS5613 Output power BTL	150 W / 4 Ω 10% THD
TAS5613 Output power PBTL	300 W / 2Ω 10% THD
TAS5611 Output power BTL	125 W / 4Ω / 10% THD
TAS5611 Output power PBTL	250 W / 2Ω / 10% THD
DNR	>100 dB(A)
Frontend	OPA1632
Output stage	TAS5611PHD, TAS5613PHD
Other features	+15 V on-board switcher from PVDD supply

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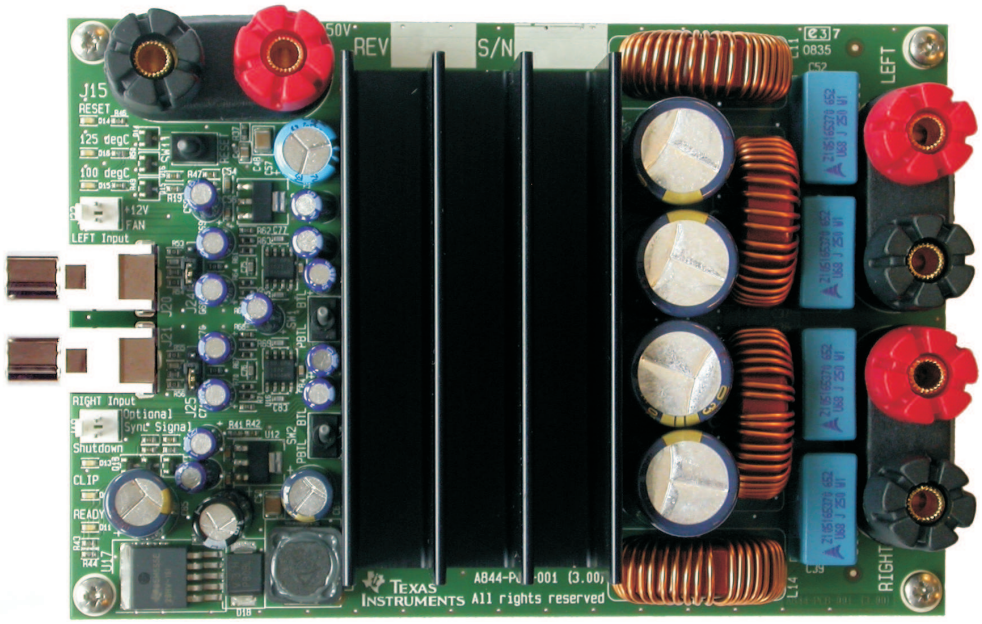
This document covers EVM specifications, audio performance and power efficiency measurements graphs, and design documentation that includes schematics, parts list, layout, and mechanical design.



1.1 TAS5611/13PHD2EVM Features

- Stereo PurePath™ Premier Pro evaluation module.
- Self-contained protection system (short circuit and thermal).
- Standard 1VRMS single ended line input or differential input.
- Double-sided, plated-through PCB layout.

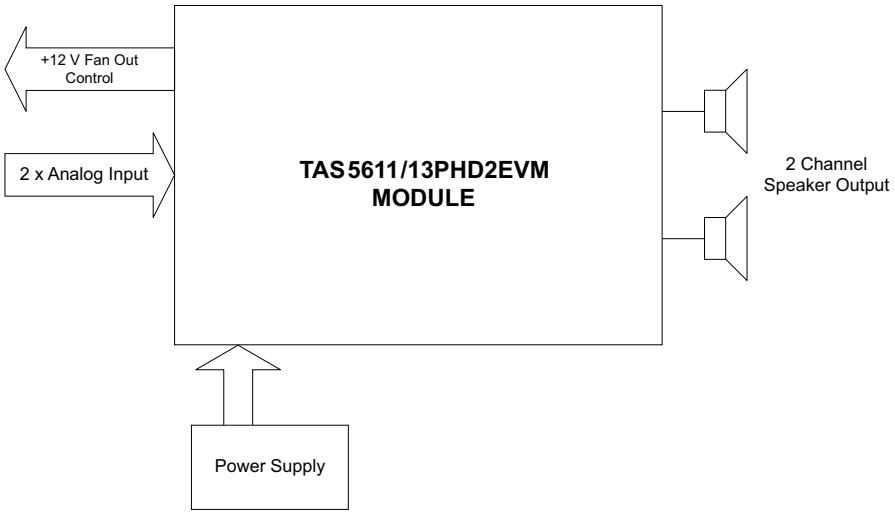


Figure 1. Integrated PurePath™ Digital Amplifier System

1.2 PCB Key Map

Physical structure for the TAS5611/13PHD2EVM is illustrated in Figure 2.

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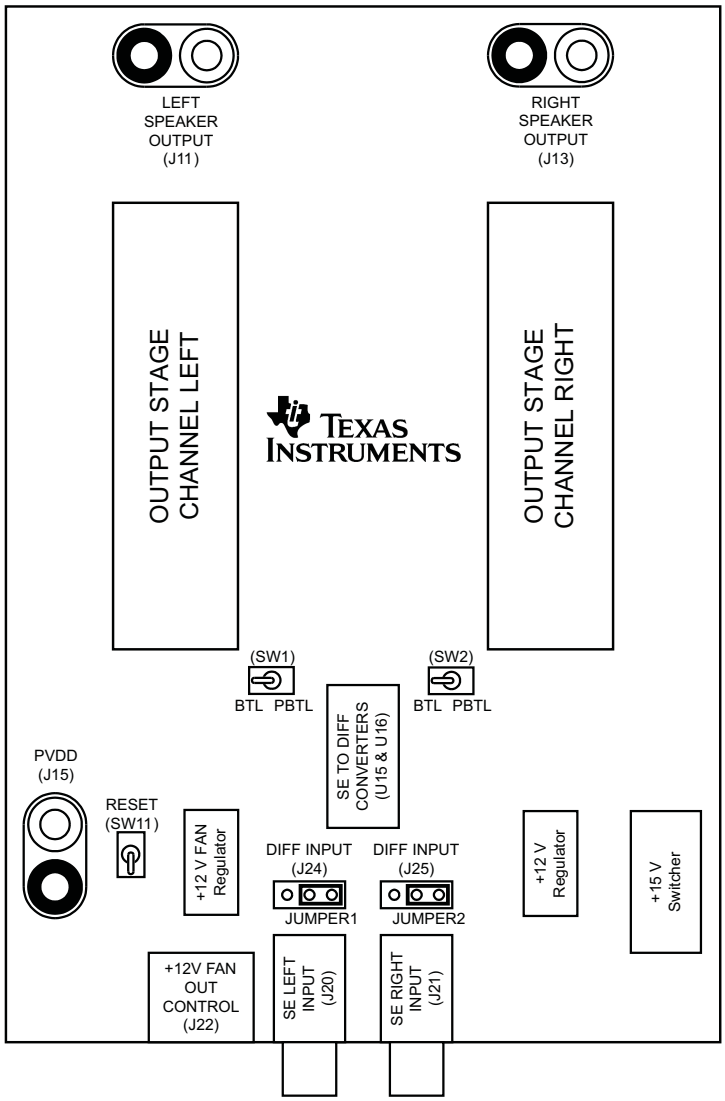

Figure 2. Physical Structure for the TAS53630PHDEVM (Approximate Layout)

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2 Quick Setup Guide

This chapter describes the TAS5611/13PHD2EVM board in regards to power supply and system interfaces. The chapter provides information regarding handling and unpacking, absolute operating conditions, and a description of the factory default switch and jumper configuration.

This section provides a step–by–step guide to configuring the TAS5611/13PHD2EVM for device evaluation

2.1 Electrostatic Discharge Warning

Many of the components on the TAS5611/13PHD2EVM are susceptible to damage by electrostatic discharge (ESD). Customers are advised to observe proper ESD handling precautions when unpacking and handling the EVM, including the use of a grounded wrist strap at an approved ESD workstation.

CAUTION

Failure to observe ESD handling procedures may result in damage to EVM components.

2.2 Unpacking the EVM

On opening the TAS5611/13PHD2EVM package, ensure that the following items are included:

- 1 pc. TAS5611/13PHD2EVM board using one TAS5611PHD or one TAS5613PHD.

If any of the items are missing, contact the Texas Instruments Product Information Center nearest you to inquire about a replacement.

2.3 Power Supply Setup

To power up the EVM, one power supply are needed. An onboard switched voltage regulator is supplying system power, logic and gate-drive. Power supply is connected to the EVM using connector J15.

NOTE: While powering up set switch SW11 to the RESET position.

Table 2. Recommended Supply Voltages

Description	Voltage Limitations	Current Requirement	Cable
TAS5613 Output stage power supply	18V - 36V	16 A	J15 (marked PVDD)
TAS5611 Output stage power supply	16V - 32.5V	16A	J15 (marked PVDD)

CAUTION

Applying voltages above the limitations given in Table 2 may cause permanent damage to your hardware

NOTE: The length of power supply cable must be minimized. Increasing length of PSU cable is equal to increasing the distortion for the amplifier at high output levels and low frequencies.

2.4 Applying Input Signal

It is possible to apply either a single ended input signal to J20 and J21 or a differential input signal to J24 and J25.

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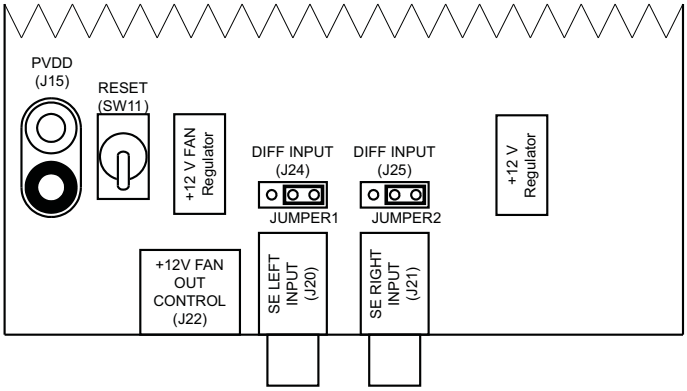
NOTE: If a single ended input signal is applied please insert jumpers in the header J24 and J25.



2.5 Speaker Connection

CAUTION

Both positive and negative speaker outputs are floating and may not be connected to ground (e.g., through an oscilloscope).

2.6 Output configuration BTL and PBTL

When changing mode e.g. from BTL to PBTL make sure that RESET switch (SW11) is activated before changing the state of mode switches SW1 and SW2. Switch SW1 and SW2 has to be synchronized in state BTL or PBTL.

Input signal to RCA connector J20 when operating PBTL mode. J21 is disabled.

In PBTL mode, the load has to be connected according to Figure 3:

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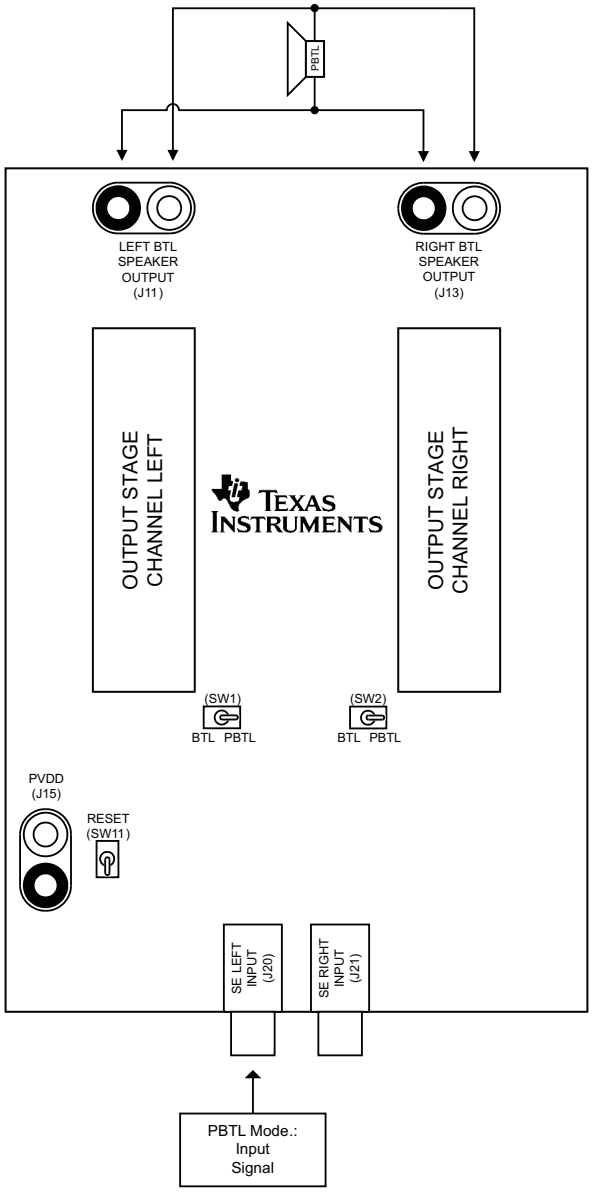

Figure 3. Figure 3. PBTL Mode Configuration

3 Protection

This section describes the short-circuit protection and fault-reporting circuitry of the TAS5611 and TAS5613 devices.

3.1 Short-Circuit Protection and Fault-Reporting Circuitry

The TAS5611 and TAS5613 is self-protecting devices that provides fault reporting (including high-temperature protection and short-circuit protection). TAS5611 and TAS5613 is configured in back-end auto-recovery mode, and therefore; resets automatically after all errors (M1, M2, and M3 is set low); see the data sheet (SLAS681) (SLAS676) for further explanation. This mean that the device restart itself after an error occasion and report through the $\overline{SD}$ error signal.

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3.2 Fault Reporting

The $\overline{\text{OTW}}$ and $\overline{\text{SD}}$ outputs from TAS5611/13 indicate fault conditions. See the TAS5611PHD/TAS5613PHD data manual for a description of these pins.

Table 3. TAS5611/13 Warning/Error Signal Decoding

$\overline{\text{SD}}$	$\overline{\text{OTW1}}$	$\overline{\text{OTW2}}$	Device Condition
0	0	0	High-temperature error and/or high-current error
0	0	1	Undervoltage lockout or high current error. 100°C temperature warning.
0	1	1	Undervoltage lockout or high-current error
1	0	0	125°C temperature warning
1	0	1	100°C temperature warning
1	1	1	Normal operation, no errors/warnings

The shutdown signals together with the temperature warning signal give chip-state information as described in the Table 3. device fault-reporting outputs are open-drain outputs.

4 Related Documentation from Texas Instruments

Table 4 contains a list of data manuals that have detailed descriptions of the integrated circuits used in the design of the TAS5611/13PHD2EVM. The data manuals can be obtained at the URL http://www.ti.com.

Table 4. Related Documentation from Texas Instruments

Part Number	Literature Number
TAS5611	SLAS681
TAS5613	SLAS676
OPA1632D	SBOS286
LM317M	SLVS297
TL2575HV-15I	SLVS638

4.1 Additional Documentation

1. *System Design Considerations for True Digital Audio Power Amplifiers* application report (SLAA117)
2. *Digital Audio Measurements* application report (SLAA114)
3. *PSRR for PurePath Digital™ Audio Amplifiers* application report (SLEA049)
4. *Power Rating in Audio Amplifiers* application report (SLEA047)
5. *PurePath Digital™ AM Interference Avoidance* application report (SLEA040)
6. *Click and Pop Measurements Technique* application report (SLEA044)
7. *Power Supply Recommendations for DVD-Receivers* application report (SLEA027)
8. *Implementation of Power Supply Volume Control* application report (SLEA038)

Appendix A Design Documents

This appendix comprises design documents pertaining to the TAS5611/13PHD2EVM evaluation module. The documents are presented in the following order.

- Schematic (4 pages)
- Parts List (1 pages)
- PCB Specification (1 page)
- PCB Layers (6 pages)
- Heat-Sink Drawing (1 page)
- Inductor (1 page)

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EVM Warnings and Restrictions

It is important to operate this EVM within the input voltage range of 0 V to 32.5 V for the TAS5611; 0 V to 36 V for the TAS5613 and the output voltage range of 0 V to 32.5 V for the TAS5611; 0 V to 36 V for the TAS5613.

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 90°C. The EVM is designed to operate properly with certain components above 125°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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Design Name:	**TAS5613PHD2EVM**
Type:	Mass Market EVM
File Name:	A858-SCH-001.DSN
Version:	1.00
Date:	24.Oct. 2009
Design Engineer:	Jonas Holm
Audio Configuration:	PurePath Premire Pro Digital Amplifier Design 1 x TAS5613PHD
Interfaces:	J20-J21: Single Ended Analog Audio Input J11, J13: Banana Bindingposts For Speakers J15: Banana Bindingpost For H-Bridge Supply
Setup:	4 Ohm (BTL) Speaker Loads +36 V H-Bridge Supply Voltage
Performance:	2 x 150 W / 4 Ohm (BTL) 10% THD+N > 102 dB Dynamic Range

Page

1/4: Front Page and Schematic Disclaimer
2/4: TAS5613 Amplifier
3/4: Input Stage
4/4: Mechanics

NOTE1

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Page Title: Disclaimer	Size: A3
File Name: A858-SCH-001.DSN	Engineer: Jonas L. Holm
Date: Monday, October 26, 2009	Page: 1 of 4

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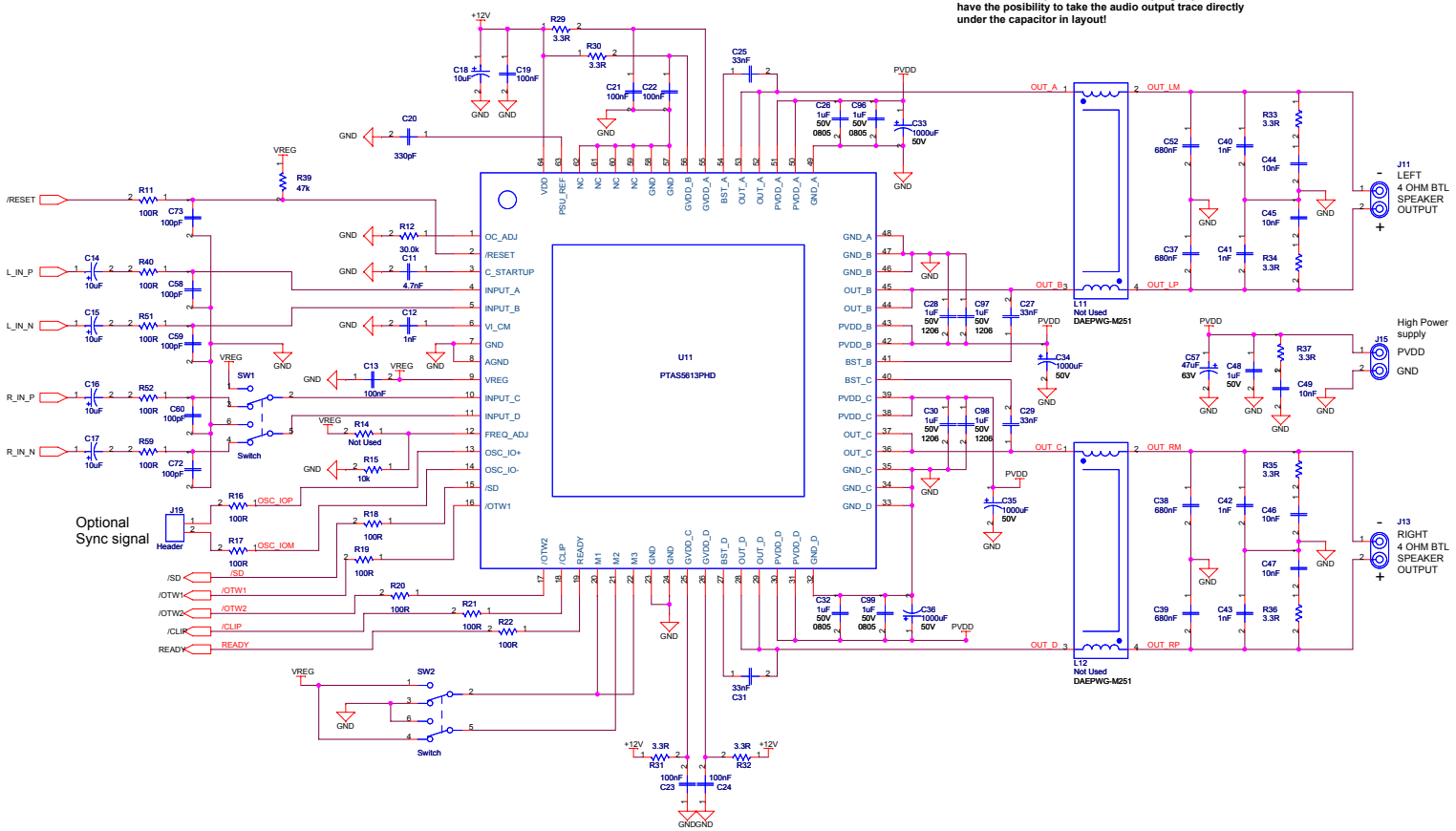
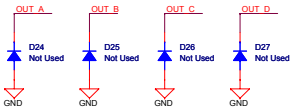
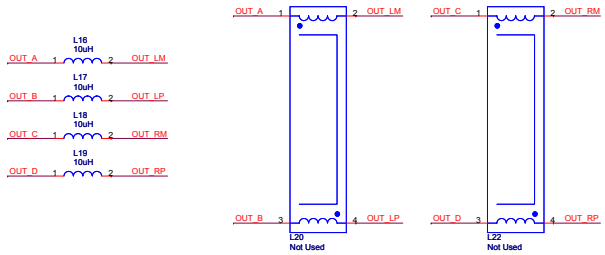


3 OHM LOAD OPERATION:

1) Mount Schottky diodes (D24, D25, D26 and D27)
2) Change ROC = 22kOhm (R12)



OUTPUT INDUCTOR STUFFING OPTION:



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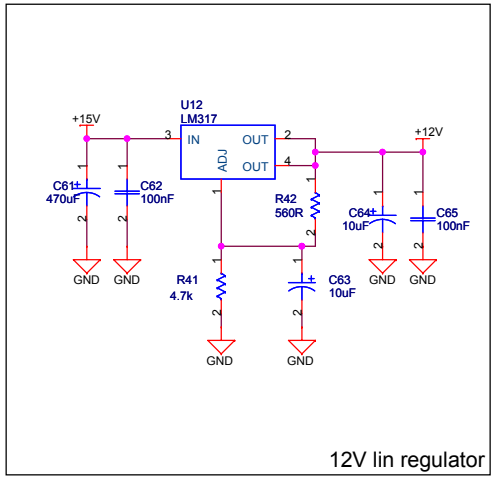
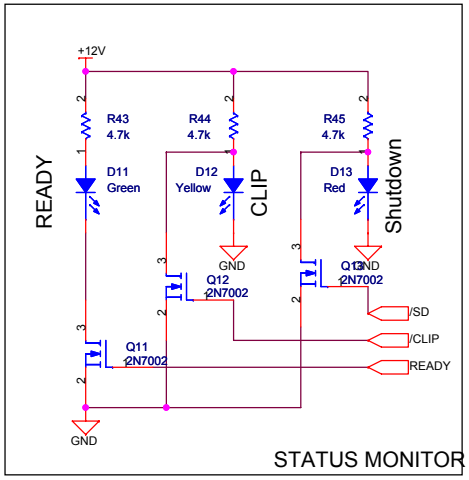
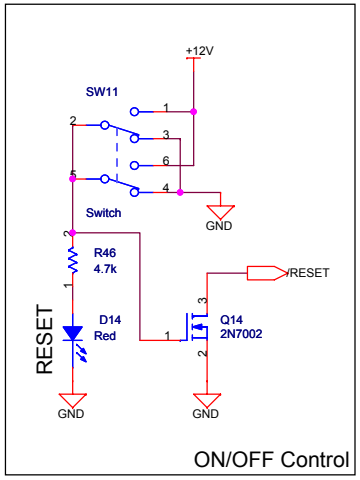
File Name: A858-SCH-001.DSN	Engineer: Jonas L. Holm
Date: Thursday, October 29, 2009	Page: 2 of 4



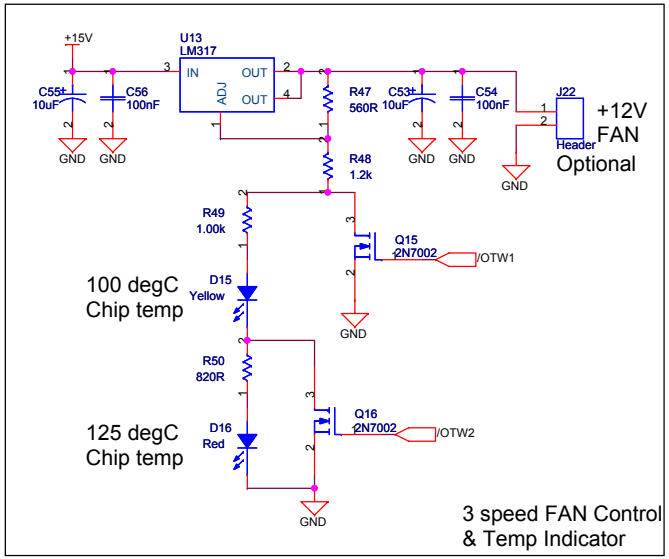
12V lin regulator



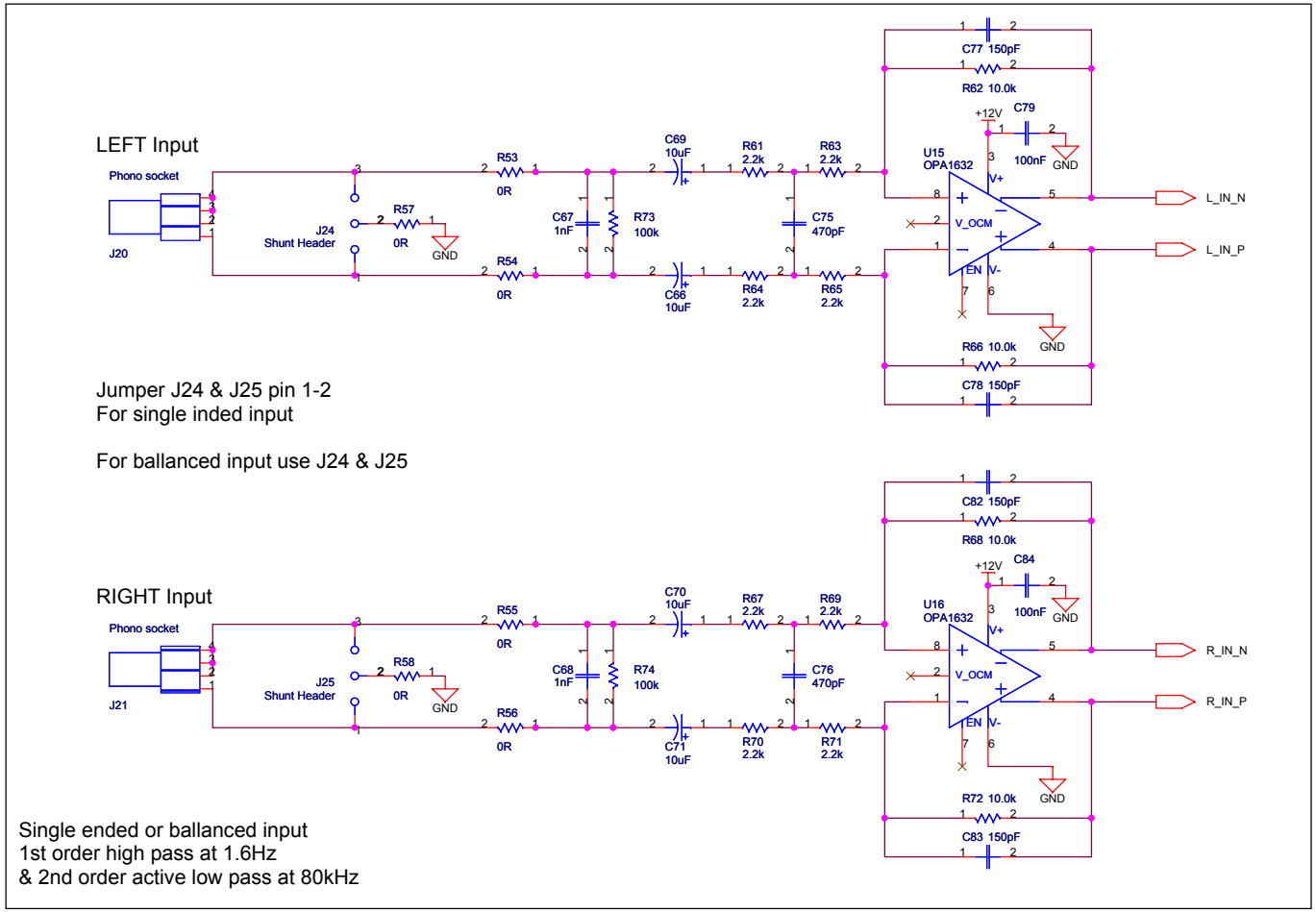
STATUS MONITOR

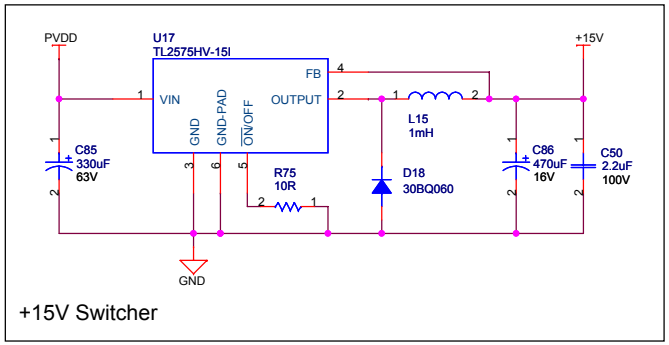


ON/OFF Control



3 speed FAN Control & Temp Indicator





+15V Switcher

TAS5613 Parts list 1.00

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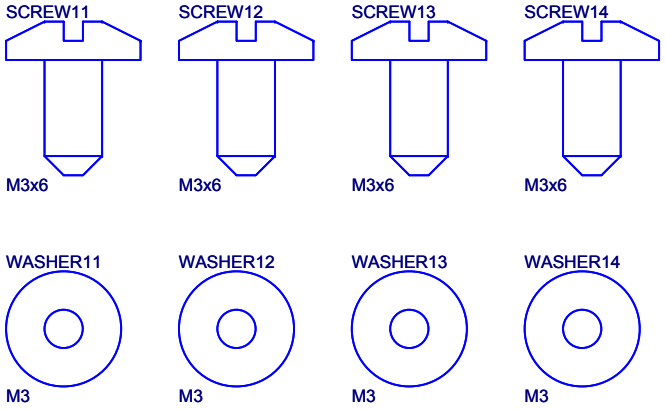
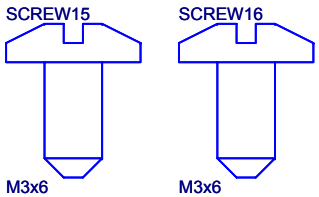
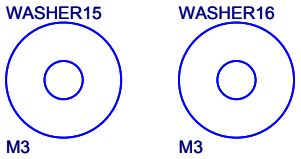
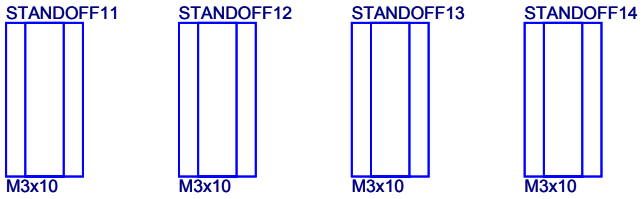
File Name: A858-SCH-001.DSN	Engineer: Jonas L. Holm
Date: Monday, October 26, 2009	Page: 3 of 4

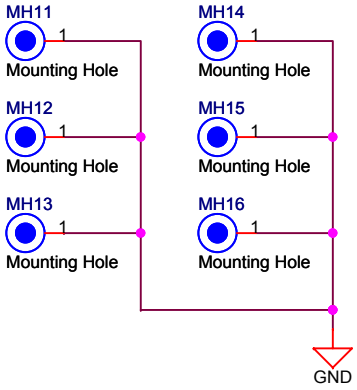
MECHANICS

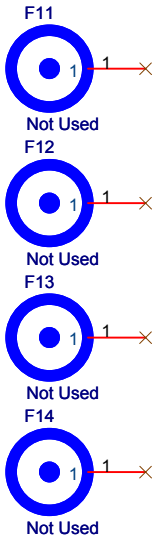


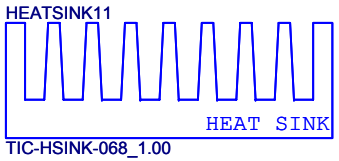


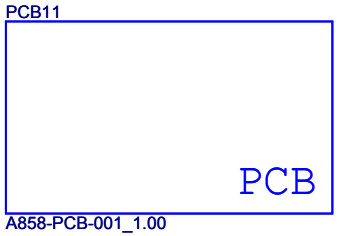


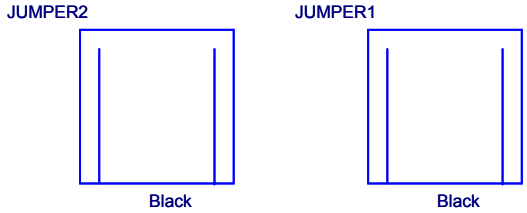












TAS5613 Parts list

TEXAS INSTRUMENTS	AUDIO/IMAGING GROUP Home Audio Amplifiers ALL RIGHTS RESERVED TEXAS INSTRUMENTS INCORPORATED
Project: TAS5613PHD2EVM	Rev: 1.00
Page Title: Mechanics	Size: A4
File Name: A858-SCH-001.DSN	Engineer: Jonas L. Holm
Date: Wednesday, October 28, 2009	Page: 4 of 4

TAS5611_13PHD2EVM Parts List (1.00).xls

Qty	Part Reference	Description	Manufacture	First Mfr P/N
6	R53 R54 R55 R56 R57 R58	0R / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-070RL
12	R40 R51 R52 R59	100R / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-07100RL
1	R49	1.00k / 100mW / 1% / 0603 Thick Film Resistor	Yageo	RC0603FR-071KL
1	R15	10k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-0710KL
4	R62 R66 R68 R72	10.0k / 100mW / 1% / 0603 Thick Film Resistor	Yageo	RC0603FR-0710KL
2	R73 R74	100k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-07100KL
1	R75	10R / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-0710RL
1	R48	1.2k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-071K2L
8	R61 R63 R64 R65 R67 R69 R70 R71	2.2k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-072K2L
1	R12	30.0k / 100mW / 1% / 0603 Thick Film Resistor	Yageo	RC0603FR-0730KL
9	R37	3.3R / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-073R3L
5	R41 R43 R44 R45 R46	4.7k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-074K7L
1	R39	47k / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-0747KL
2	R42 R47	560R / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-07560RL
1	R50	820R / 100mW / 5% / 0603 Thick Film Resistor	Yageo	RC0603JR-07820RL
5	C44 C45 C46 C47 C49	Ceramic 10nF / 100V / 20% X7R 0805 Capacitor	BC Components	0805B103M101NT
5	C26 C32 C48 C96 C99	Ceramic 1uF / 50V / 10% X7R 0805 Capacitor	Murata	GRM21BR71H105KA12L
1	C11	Ceramic 4.7nF / 50V / 10% X7R 0805 Capacitor	BC Components	0805B472K500NT
4	C40 C41 C42 C43	Ceramic 1nF / 100V / 10% NP0 1206 Capacitor	BC Components	1206N102K101NT
4	C28 C30 C97 C98	Ceramic 1uF / 50V / 10% X7R 1206 Capacitor	TDK	C3216X7R1H105K
1	C50	Ceramic 2.2uF / 100V / 20% X7R 1210 Capacitor	Murata	GRM32ER72A225KA35L
1	C12	Ceramic 1nF / 50V / 10% NP0 0805 Capacitor	BC Components	0805N102K500NT
12	C13 C19 C21 C22 C23 C24 C54 C56 C62 C65 C79 C84	Ceramic 100nF / 16V / 20% X7R 0603 Capacitor	Vishay	VJ0603Y104MXJ
4	C25 C27 C29 C31	Ceramic 33nF / 25V / 20% X7R 0603 Capacitor	BC Components	0603B333M250NT
5	C58 C59 C60 C72 C73	Ceramic 100pF / 50V / 10% NP0 0603 Capacitor	BC Components	0603N101K500NT
2	C67 C68	Ceramic 1nF / 50V / 10% NP0 0603 Capacitor	BC Components	0603N102K500NT
4	C77 C78 C82 C83	Ceramic 150pF / 50V / 10% NP0 0603 Capacitor	BC Components	0603N151K500NT
1	C20	Ceramic 330pF / 50V / 10% NP0 0603 Capacitor	BC Components	0603N331K500NT
2	C75 C76	Ceramic 470pF / 50V / 10% NP0 0603 Capacitor	BC Components	0603N471K500NT
4	C37 C38 C39 C52	Metal Film 680nF / 250V / 20% Polypropylene 15mm (W:8mm L:18mm) Capacitor	Wima	MKP 4 0.68uF/20%/250Vdc PCM15
13	C14 C15 C16 C17 C18 C53 C55 C63 C64 C66 C69 C70 C71	Electrolytic 10uF / 16V / 20% Aluminium 2mm ø5mm M Series - General Purpose Capacitor	Panasonic	ECA1CM100
4	C33 C34 C35 C36	Electrolytic 1000uF / 50V / 20% Aluminium 7.5mm ø16mm FC Series - Low Impedance Capacitor	Panasonic	EEUFC1H102
1	C85	Electrolytic 330uF / 63V / 20% Aluminium 5mm ø10mm FC Series - Low Impedance Capacitor	Panasonic	EEUFC1J331L
1	C57	Electrolytic 47uF / 63V / 20% Aluminium 5mm ø10mm Capacitor	BC Components	2222 136 68479
1	C86	Electrolytic 470uF / 16V / 20% Aluminium 3.5mm ø8mm Low ESR Capacitor	Rubycon	16ZL470M8x16
1	C61	3.5mm ø8mm FC Series - Low Impedance	Panasonic	EEUFC1E471L
1	L15	1mH / 0.55A 20% (1.68R) Ferrite Inductor (12.8x12.8x8.0)	Epcos	B82477G4105M000
4	L16 L17 L18 L19	10uH / Ferrite Inductor	Toko	C3B-A0336
1	D18	3A / 60V Schottky 30BQ060 Diode (SMC)	Int. Rectifier	30BQ060PBF
3	D13 D14 D16	Light Emitting Red Red LED (0603)	Toshiba	TLSU1008
1	D11	Light Emitting Green Green LED (0603)	Toshiba	TLGU1008
2	D12 D15	Light Emitting Yellow Yellow LED (0603)	Toshiba	TLYU1008
6	Q11 Q12 Q13 Q14 Q15 Q16	0.115A / 60V N-ch Power 2N7002 Mosfet (SOT-23)	Fairchild	2N7002
1	U11	TAS5611PHD or TAS5613PHD / Stereo Analog Audio PWM Power Output Stage (PHD64)	Texas Instruments	TAS5611PHD or TAS5613PHD
2	U15 U16	OPA1632 / High-Performance, Fully-Differential Audio Opamp (SO8)	Texas Instruments	OPA1632D
2	U12 U13	LM317 / 0.5A Positive Adjustable Regulator (DCY)	Texas Instruments	LM317MDCY
1	U17	TL2575HV-15I / 15V/1-A SIMPLE STEP-DOWN SWITCHING VOLTAGE REGULATORS (KTT5)	Texas Instruments	TL2575HV-15IKTTR

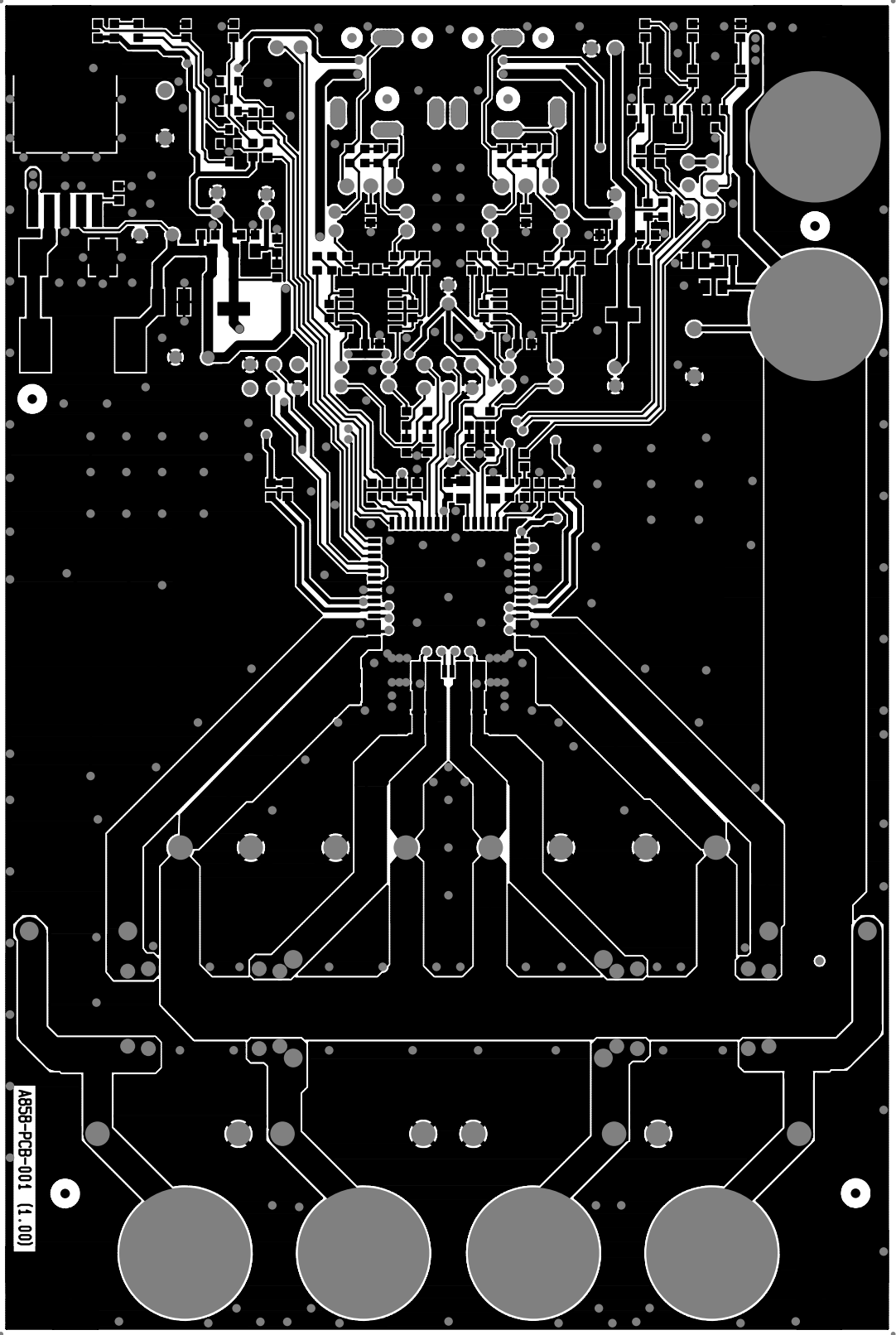
6	SCREW11 SCREW12 SCREW13 SCREW14 SCREW15 SCREW16	M3x6 Pan Head, Pozidriv, A2 Screw	Bossard	BN 81882 M3x6
6	WASHER11 WASHER12 WASHER13 WASHER14 WASHER15 WASHER16	M3 Stainless Steel Spring Washer	Bossard	BN 760 M3
4	STANDOFF11 STANDOFF12 STANDOFF13 STANDOFF14	M3x10 Aluminium Stand-off	Ettinger	05.03.108
2	J19 J22	2 pins / 1 row / 2.54mm Pitch Vertical Male Friction lock Pin header Header	Molex	22-27-2021
2	JUMPER1 JUMPER2	2 pins / 1 row / 2.54mm Pitch Horizontal Female Black Shunt Black	Molex	15-29-1024
2	J20 J21	Horizontal Female w. Switch Coax Phono socket	Chunfeng	RJ843-4W
3	J11 J13 J15	2 pins / Vertical Female Banana Red and black banana socket	Cliff	TPP-3CT
2	J24 J25	3 pins / 1 row / 2.54mm Pitch Vertical Male Shunt Header Shunt Header	Samtec	TSW-107-07-T-T
3	SW1 SW2 SW11	Switch DPDT PCB Mount Switch	NKK-Nikkai	G-22-AP
1	PCB11	A858-PCB-001_1.00 / TAS5613PHD2EVM2 Printed Circuit Board (ver. 1.00)	Elcon	A858-PCB-001(1.00)
1	HEATSINK11	TIC-HSINK-068_1.00 / Heatsink for 1 PHD package, length 78 mm	Phonotech	TIC-HSINK-068(1.00)

Jonas Holm

TAS5613PHD2EVM
PCB SPECIFICATION
Version 1.00

BOARD IDENTIFICATION:	A858-PCB-001(1.00)
BOARD TYPE:	DOUBLE-SIDED PLATED-THROUGH BOARD
LAMINATE TYPE:	FR4
LAMINATE THICKNESS:	1.6mm
TOP LAYER COPPER THICKNESS:	70µm (INCL. PLATING EXTERIOR LAYER)
BOTTOM LAYER COPPER THICKNESS:	70µm (INCL. PLATING EXTERIOR LAYER)
COPPER PLATING OF HOLES:	>25µm
MINIMUM HOLE DIAMETER	0.3 mm
SILKSCREEN COMPONENT SIDE:	WHITE - REMOVE SILKSCREEN FROM SOLDER AREA & PRE-TINNED AREAS
SILKSCREEN SOLDER SIDE:	None
SOLDER MASK COMPONENT SIDE:	GREEN
SOLDER MASK SOLDER SIDE:	GREEN
PROTECTIVE COATING:	SOLDER COATING AND CHEMICAL SILVER ON FREE COPPER
ELECTRICAL TEST:	PCB MUST BE ELECTRICAL TESTED
MANUFACTURED TO:	PERFAG 2E (www.perfag.dk)
APERTURE TABLE:	PERFAG 10A (www.perfag.dk)
BOARD SIZE:	95 x 142 mm
Aprox. Number of holes	468
COMMENTS:	SEE DRILL INFORMATION FILE (A858-PCB-001(1.00).pdf)

COMPONENT SIDE	DpS 5398 091029
TI Denmark A858-PCB-001 (1.00)	



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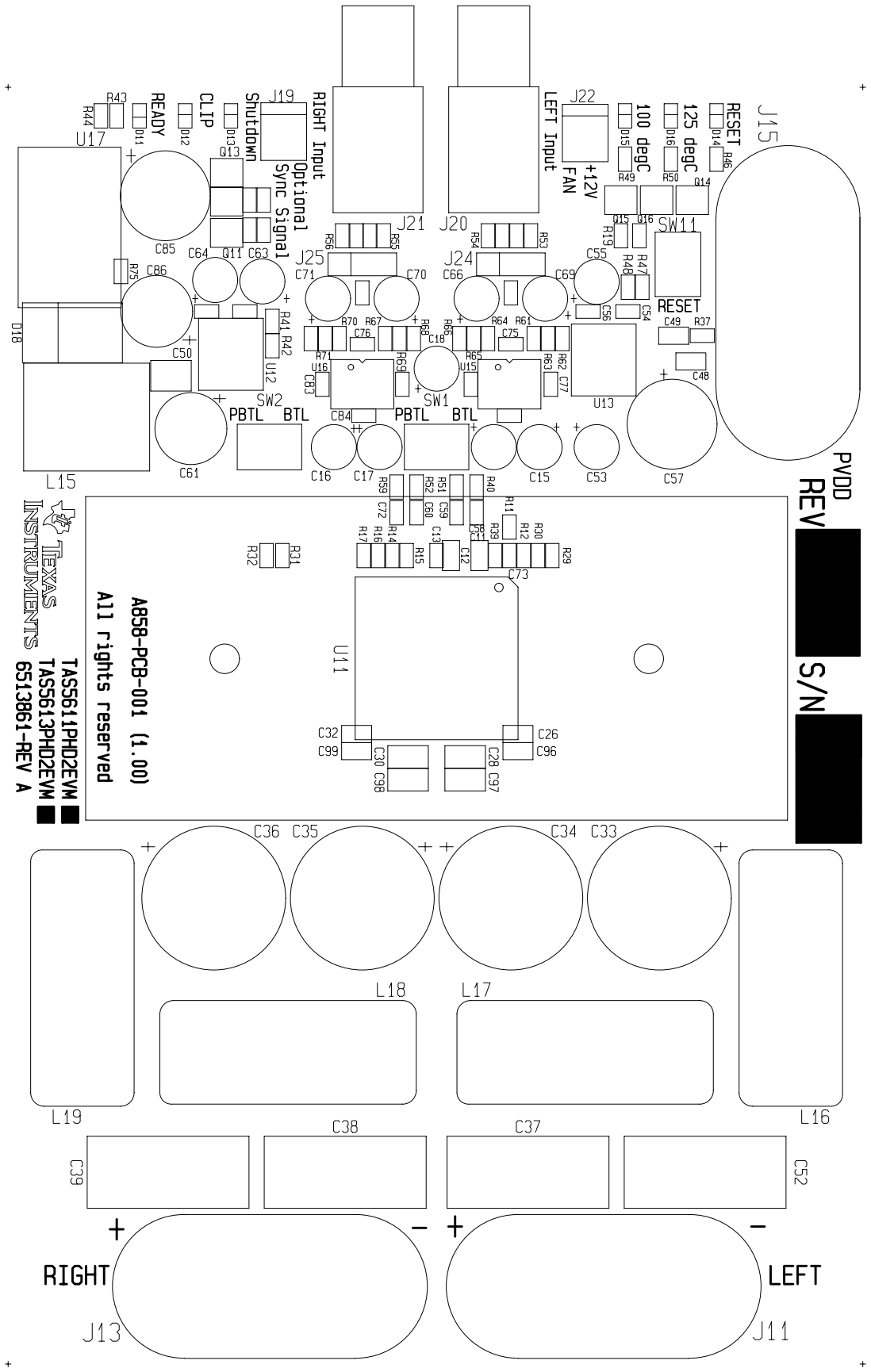

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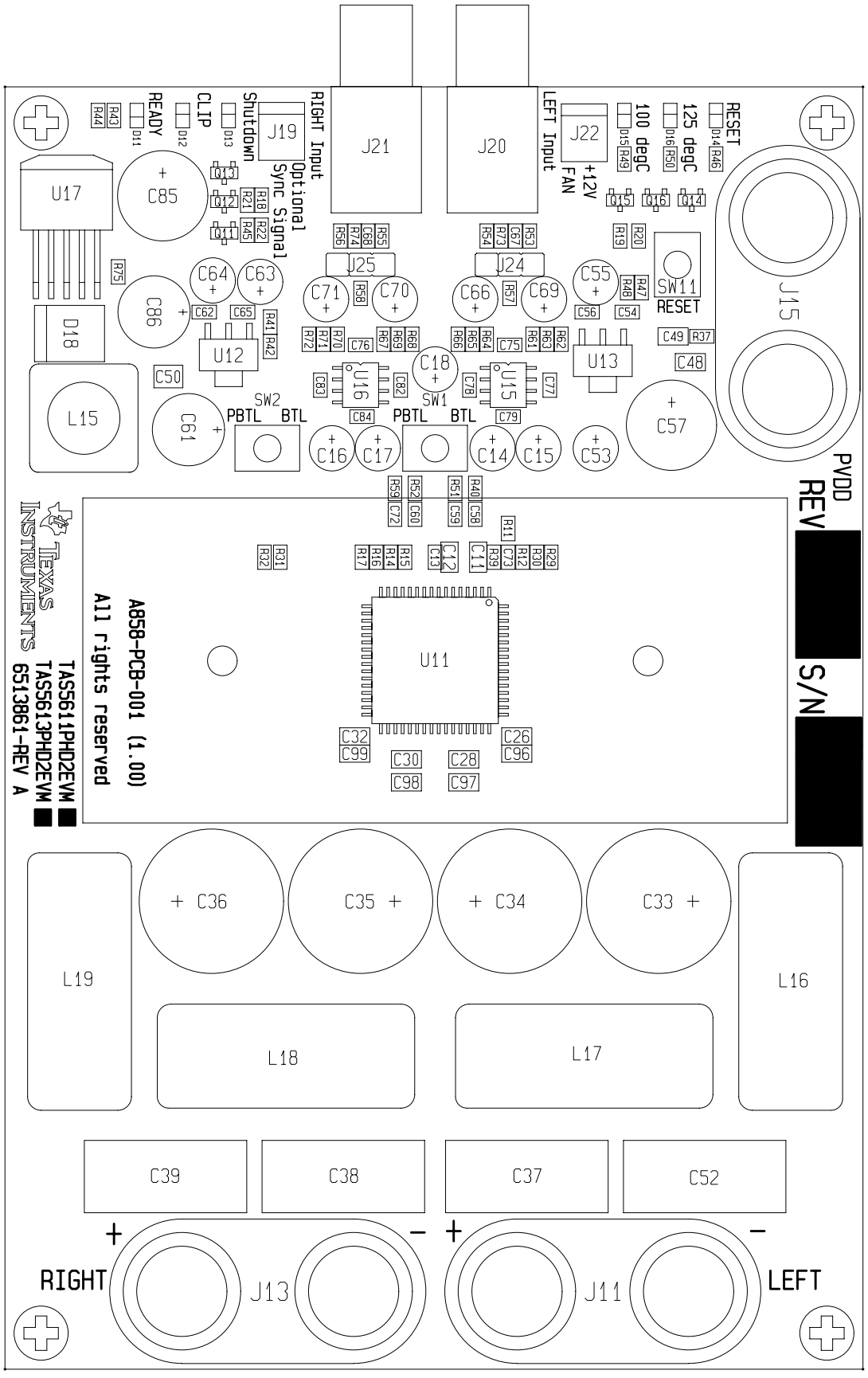

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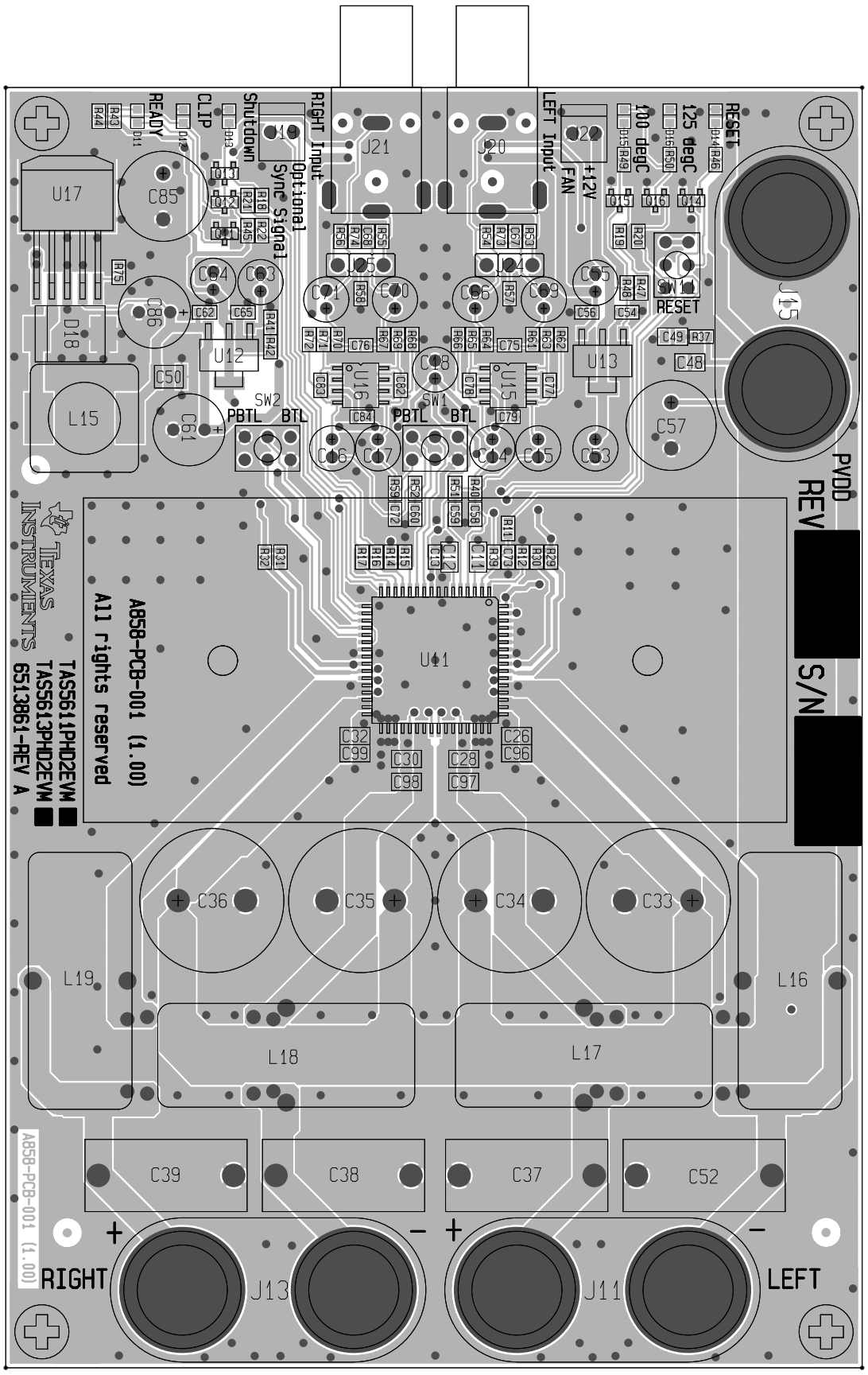

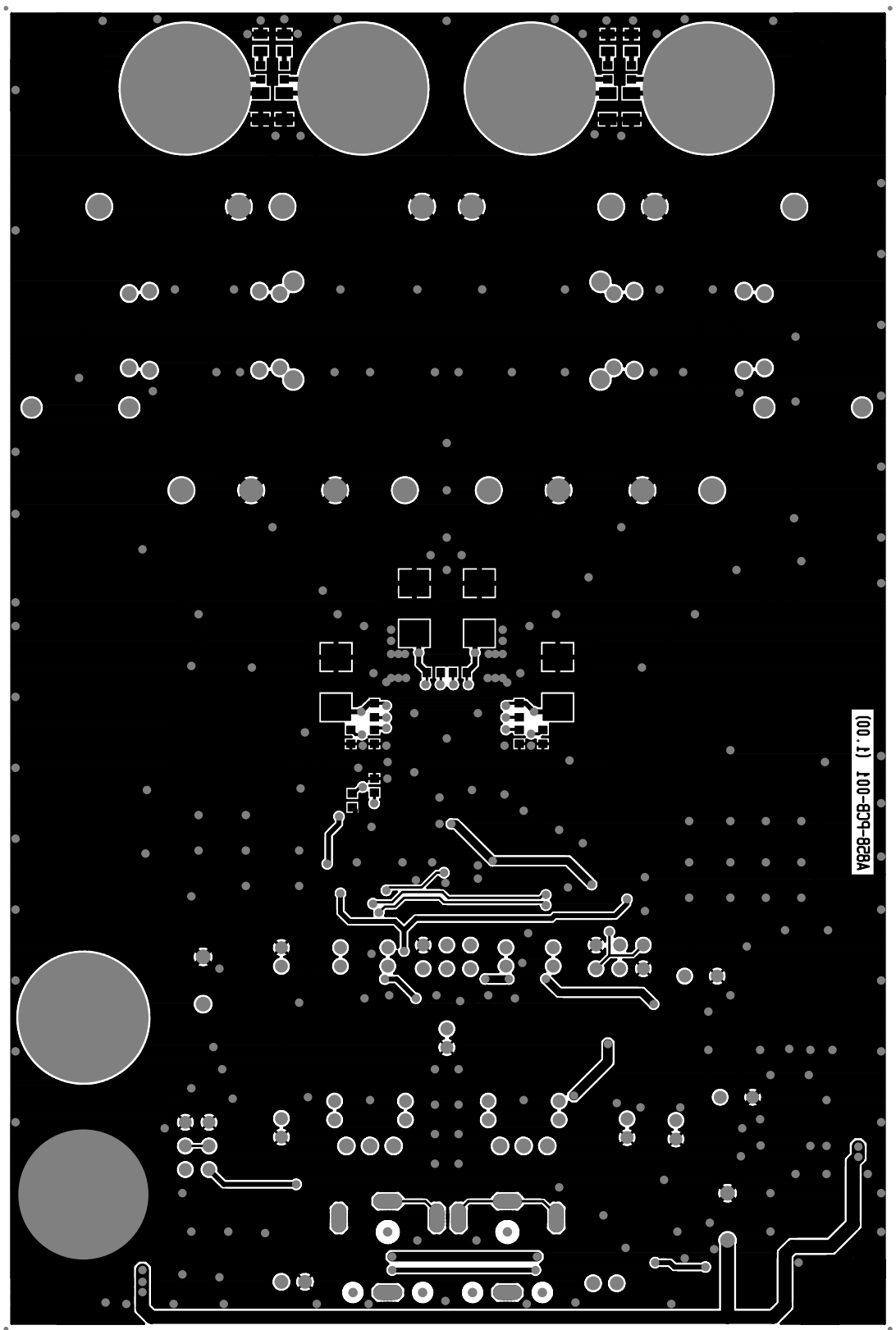

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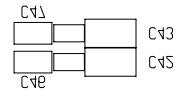
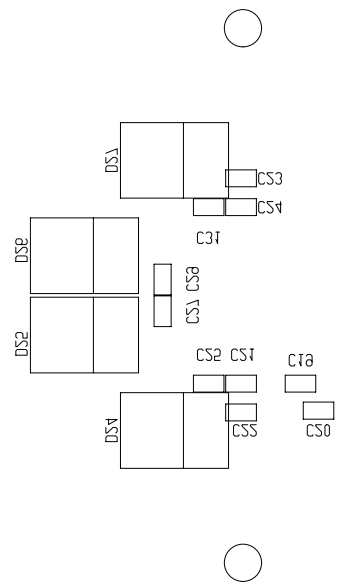
SOLDER SIDE		Page 2388 of 2520





TI Confidential	Page 2338 of 2358
SILKSCREEN BOARD A828-bCB-001 (J.00)	

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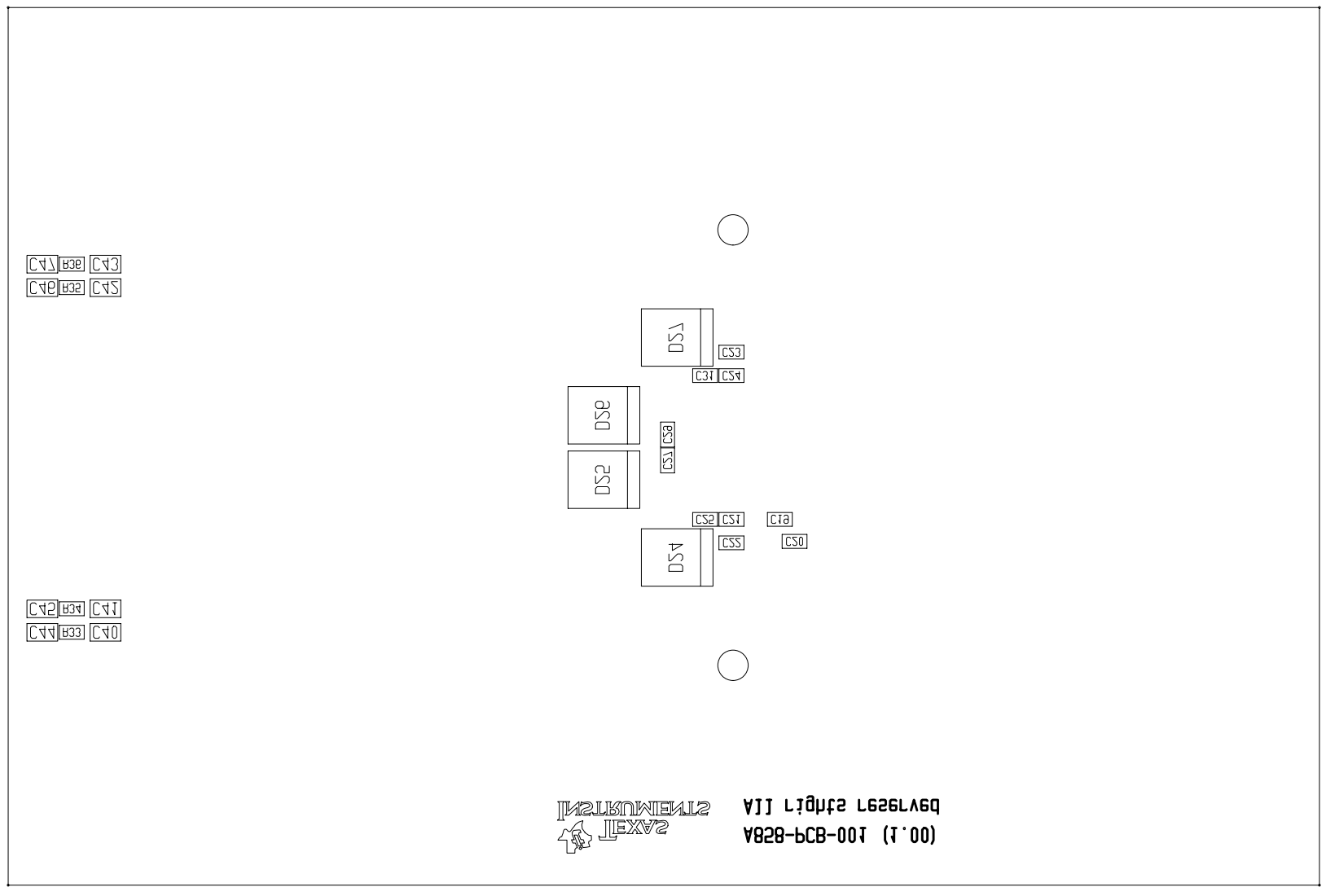
A828-bCB-001 (J.00)
INSTRUMENTS TEXAS



A828 Klemmbrett (1.00)
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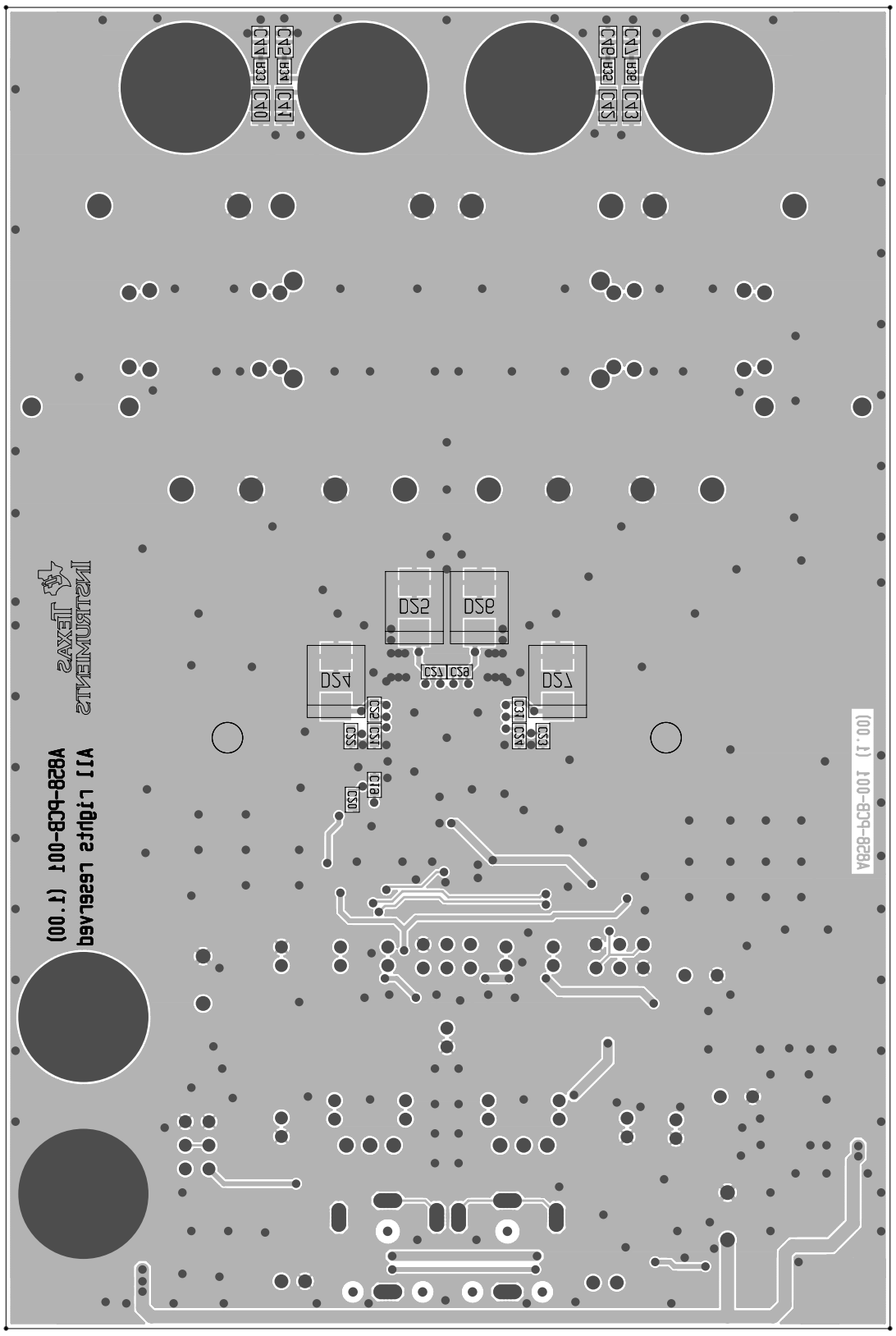


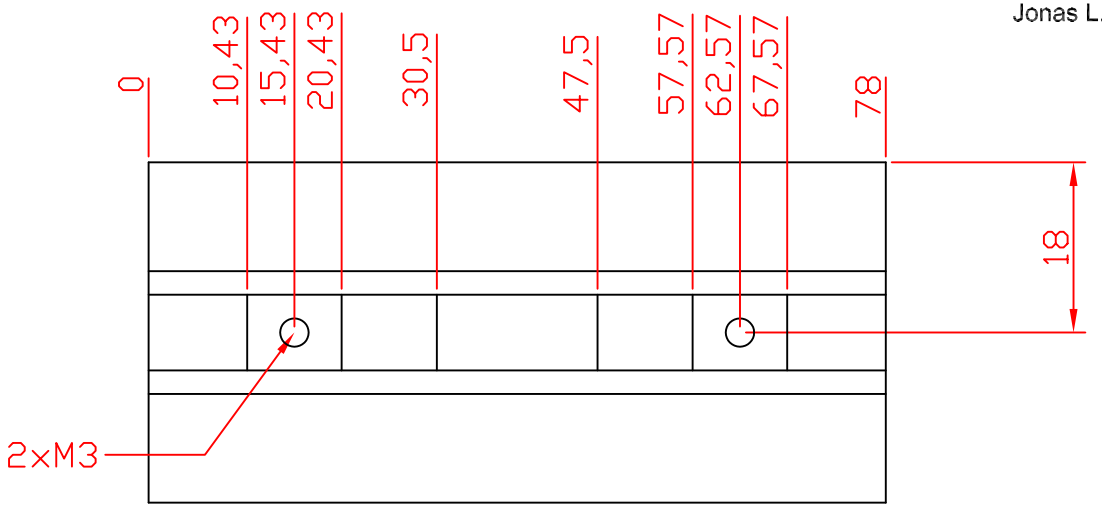
Page 2 of 2	COMP. LAYOUT 2 OF 2	TI Confidential AB58-PCB-001 (1.00)

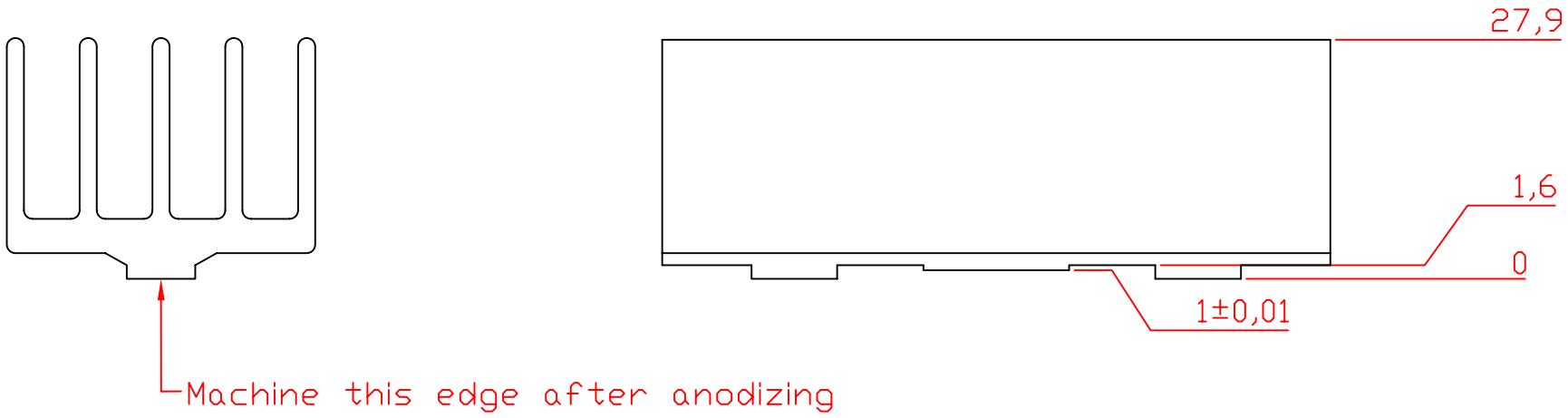
TIC-HSINK-068(2.00)

Heat sink for 1 PHD package

27.Oct.2009
TIC-HSINK-068(2.00).dwg

Jonas L. Holm





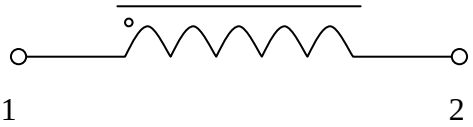
APPROX. SCALE: 1.25:1
DIMENSIONS: mm
MATERIAL: Profile TIC-HSINK-042(1.00), ALUMINUM
SURFACE: FREE OF SHARP EDGES
SURFACE TREATMENT: BLACK ANODIZED
TOLERANCES: +/- 0.1 mm

Company Confidential
Inductor Specification

DWG no.: TIC-INDC-020(1.00)

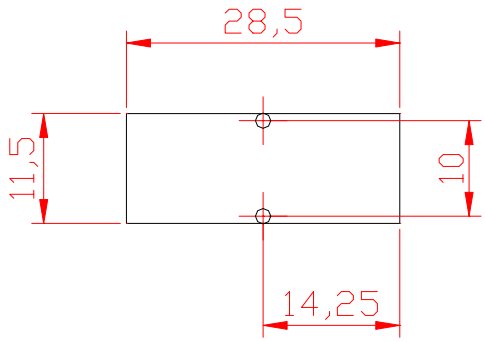
Text: 10µH / 5A / 30mΩ

Diagram:

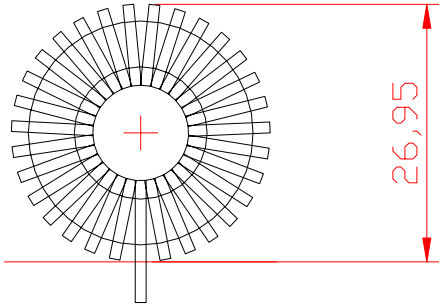


Material: Core: Micrometals T94-2
Wire: ø1.00mm Cu, one layer lacquer, 155°C

Foot-print top view



Mechanical:



Lead length: 8mm-12mm, stripped and pre-tinned.

Production: Step 1: N1, 35 turns ø1.00mm cu 2L, start 1, end 2
Step 2: bend and strip/pre-tin leads.

Test: Inductance: pin 1 –2 9 – 11 µH @ 0.1Vrms/10kHz

Release date: 2005-04-12, Jonas Svendsen / Kim Madsen

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