



Test Procedure for the ONS321B12VGEVB Evaluation Board

Test Equipment Required

5.1 Voltage Sources:

(i) DC Supply Source for Input Voltage: The input voltage source should be a 0 to 20V DC source. The input voltage may be increased further depending on the parts that are being used on the ONS321G evaluation board such that the part can withstand the applied voltage. Hence, based on the required input voltage to be applied, the requirement of the DC power supply varies.

(ii) DC Supply Source for Driver Voltage: The supply source for the driver should be a 0 to 20V DC source. The driver voltage varies depending on the type of driver used (i.e.) For NCP5911 driver, the driver voltage is 5V and for NCP5901 driver, it is 12V.

5.2 Electronic Load: The electronic load supplied to the ONS321G evaluation board ranges from 0A to 25A. Hence a DC current source of 0A to 30A is needed for the evaluation board.

5.3 Meters to measure voltages and currents:

In the ONS321G Evaluation Board, the voltages that are to be measured are V_{in} , V_{out} and V_{drv} . Similarly, the currents that are to be measured are I_{in} , I_{out} and I_{drv} . The set up for measuring these voltages and currents, and the meters required are shown in *Figure 3*. The currents are measured across the shunt resistances that are connected across each of the terminals of input, output and driver voltages as shown in *Figure 3*. For example, the output current is measured as, $I_{out} = V_{out} / R_{sh}$. Similarly the input and driver current can also be measured. The connecting wires from the output terminal to the electronic load should be thicker in order to avoid losses and to measure the exact voltage at the end of the terminals.

5.4 Oscilloscope: The oscilloscope is used to monitor the switch node waveforms. This should be an analog or digital oscilloscope set for DC coupled measurement with 50MHz bandwidth. The resolution can be set at 5V/division vertically and 20ns/division horizontally. The oscilloscope channels can be connected at various test points such as High Side Driver (JS6), Low Side Driver (JS10), Switch Node (JS8), G1 PWM Signal (JS11), $V_{in(sense)}$ (J9 & J10) and $V_{out(sense)}$ (J11 & J12).



Test Set up and Procedure

6.1 Test Setup

The test set up, test points and components present on the ONS321G Evaluation Board are shown in Figure 3.

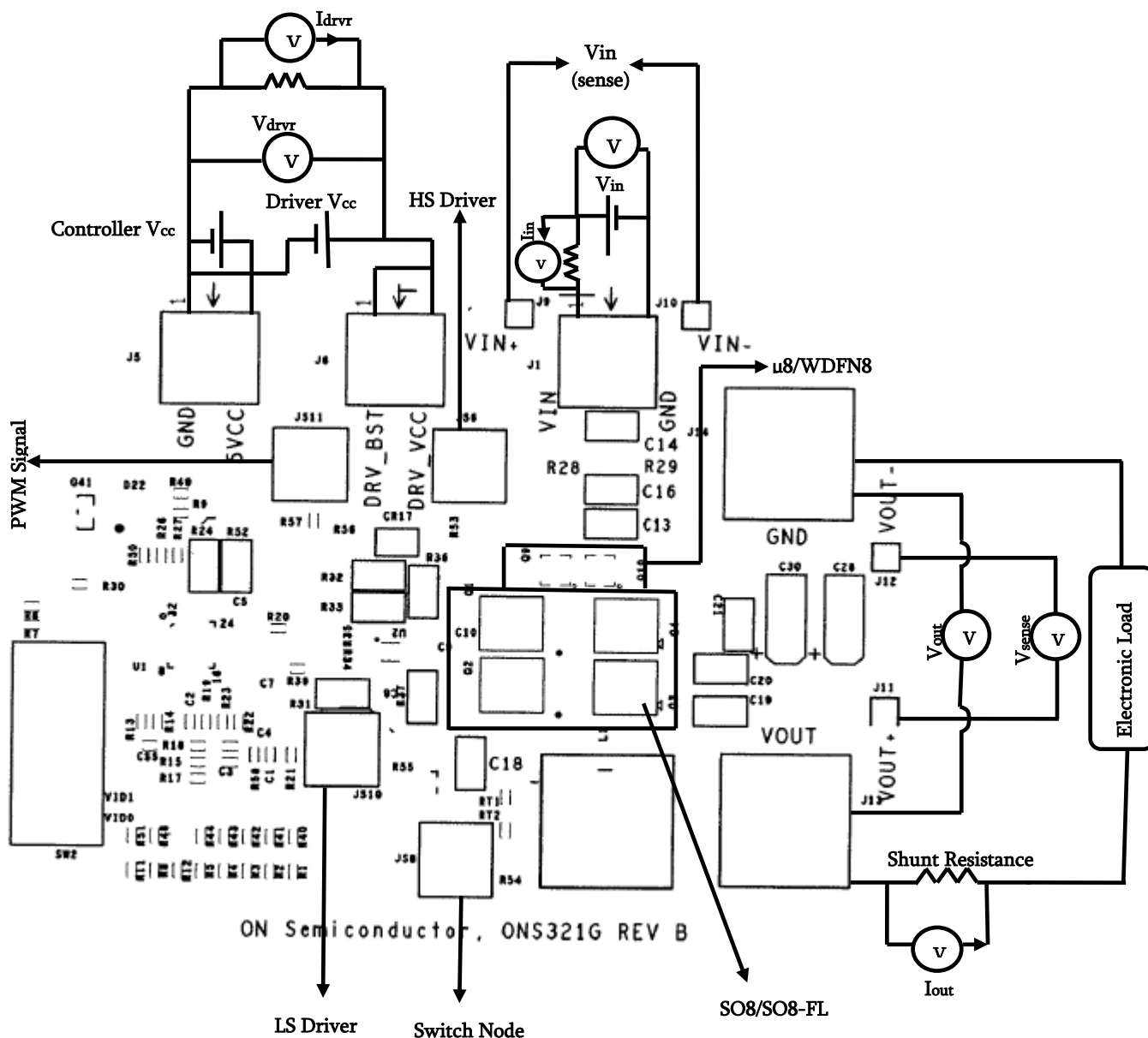




Figure 3: Schematic of the Test Setup

The SO-8 parts placed on the evaluation board are the Q1, Q2, Q3 and Q4; the μ -8 parts placed on the evaluation board are the Q9 and Q10 and the DPAKs placed on the board right below the SO-8 parts are Q5, Q6, Q7 and Q8 shown in the schematic of ONS321G. (Refer to Section 2-Figure 1).

6.2 Start up and Shut down procedures

Before starting the test, the oscilloscope probes should be connected. IR or k-type thermo-couples can be used to monitor the temperature of the parts to make sure that they are still within the limits. IR monitoring requires the removal of the oscilloscope probes due to the IR beam interference.

Start up procedure:

- (1) Initially set all the power supplies to 0V.
- (2) Set the output voltage by changing the VID settings. The output voltage should not be changed with either the controller or driver active.
- (3) Set the driver voltage and then set the input voltage.
- (4) Set the load current to required value. The load current must be incremented slowly to prevent the transient spikes at CS1/CS2 thereby shutting down the controller. If the controller shuts down, the input voltage must be set to zero, then the input power supply has to be turned off, then turned on and V_{in} re-established.

Shut down Procedure:

- (1) Shut down the Load.
- (2) Reduce the input voltage to zero and then shut down the input power supply.
- (3) Reduce the driver voltage to zero and then shut down the driver power supply.

6.3 Test Procedure

- (1) Before making any connections, make sure to set the power supplies for input voltage and the driver voltage at 0V. Also make sure that the load current is at 0A.
- (2) Connect the Oscillator probes at the desired test points.
- (3) Set the driver voltage to the required value (For example, $V_{drv} = 5V$).
- (4) After reaching the required driver voltage, set the input voltage as required. (For example, $V_{in} = 12V$).
- (5) Set the load current slowly to the desired value. For example, $I_{out} = 2.5A$. (Refer to *Start Up Procedure #4*).
- (6) The frequency is already set to 330 kHz. If a different switching frequency is required, R13 and R14 have to be changed as per the data sheet of NCP5386. (Refer to *Appendix*).
- (7) Connect the voltmeters/multi-meters to monitor the required parameters. (Refer to *Figure 3*).
- (8) Obtain the required data and waveforms.

Test Results



7.1 (a) Efficiency Comparison between the Standard in-house and ONS321G evaluation boards:

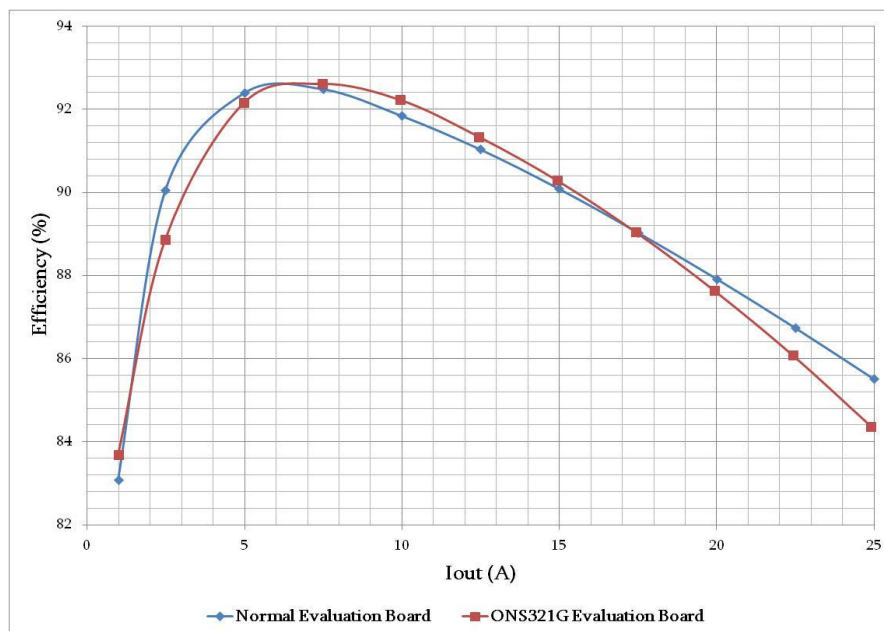


Figure 4: Comparison of Efficiency for Standard in-house and ONS321G Boards

7.1 (b) Efficiency Comparison of ONS321G Evaluation Board for $V_{\text{drv}} = 5\text{V}$ & 10V^{**}

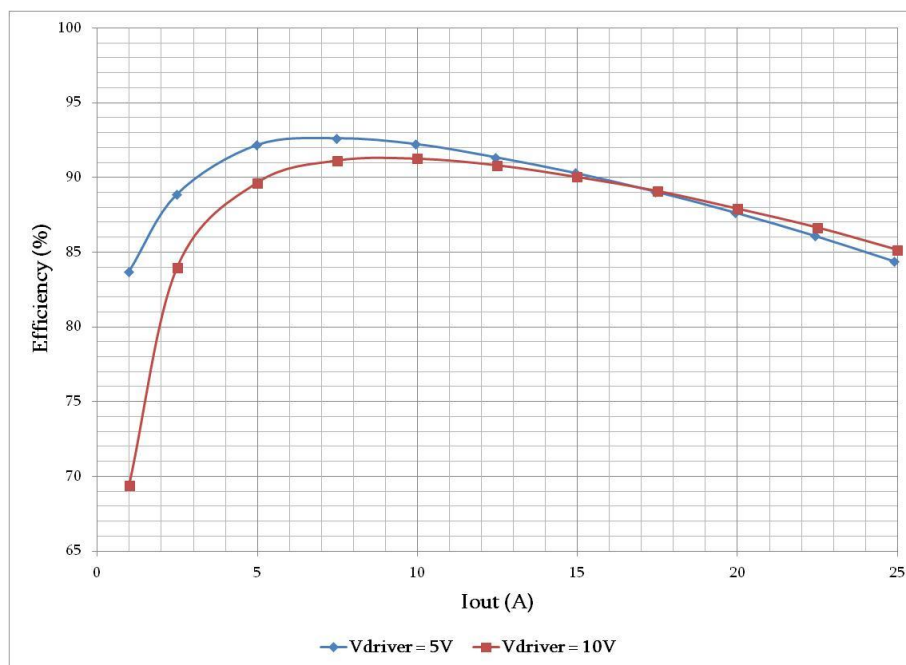


Figure 5: Comparison of Efficiency for ONS321G Board for $V_{\text{drv}} = 5\text{V}$ & 10V

7.2 (a) Power Loss Comparison between the Standard in-house and ONS321G Evaluation Boards

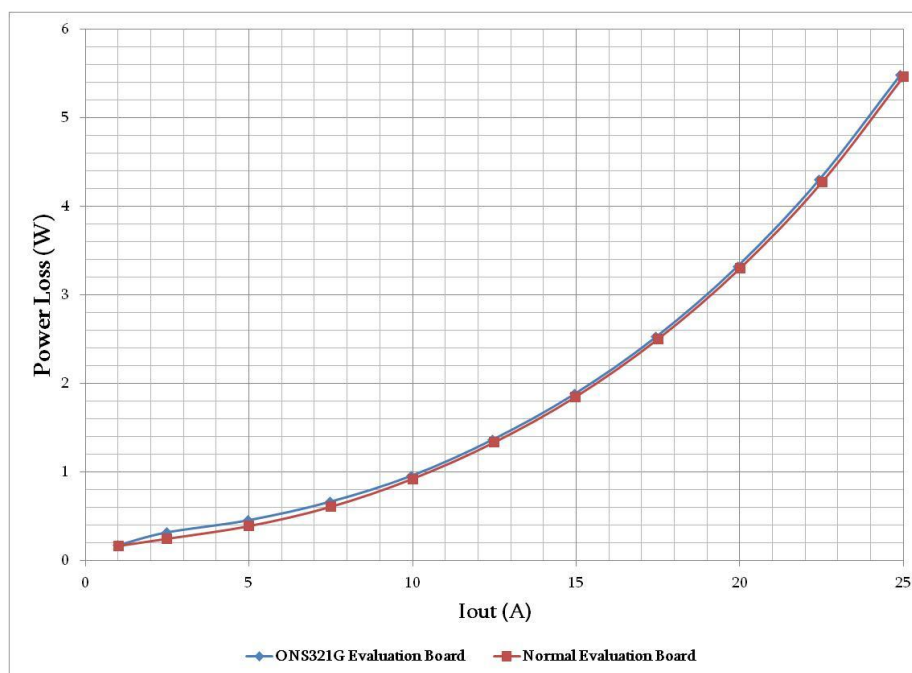


Figure 6: Comparison of Power Loss for Standard in-house and ONS321G Boards

7.2 (b) Power Loss Comparison of ONS321G Evaluation Board for $V_{\text{drv}} = 5\text{V}$ & 10V^{**}

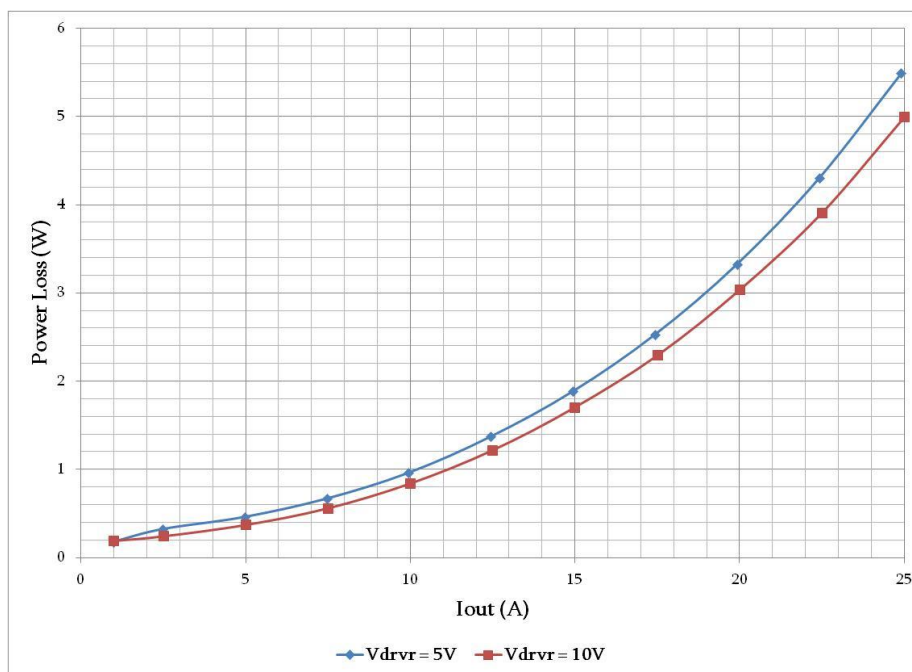


Figure 7: Comparison of Power Loss for ONS321G Board for $V_{\text{drv}} = 5\text{V}$ & 10V

7.3 Switch Node Voltage Waveforms of ONS321G Evaluation Board for $V_{\text{drv}} = 5\text{V}$ & 10V^{**}



(i) At $I_{out} = 20A$ and $V_{drv} = 5V$ ($V_{in} = 12V$; $V_{out} = 1.2V$; Freq = 330kHz)

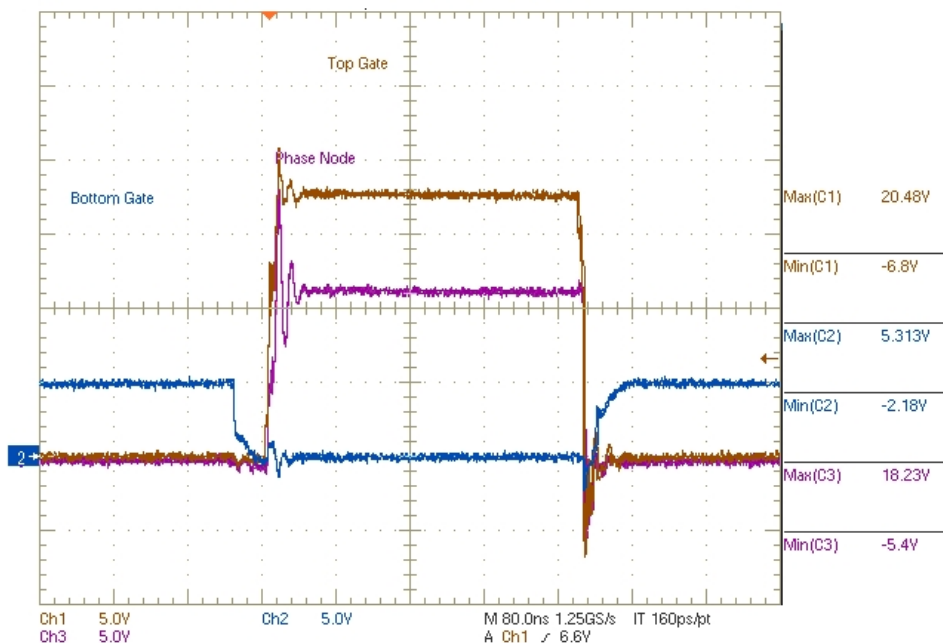


Figure 8: Switch Node Waveforms for $V_{drv} = 5V$

(ii) At $I_{out} = 20A$ and $V_{drv} = 10V$ ($V_{in} = 12V$; $V_{out} = 1.2V$; Freq = 330kHz)

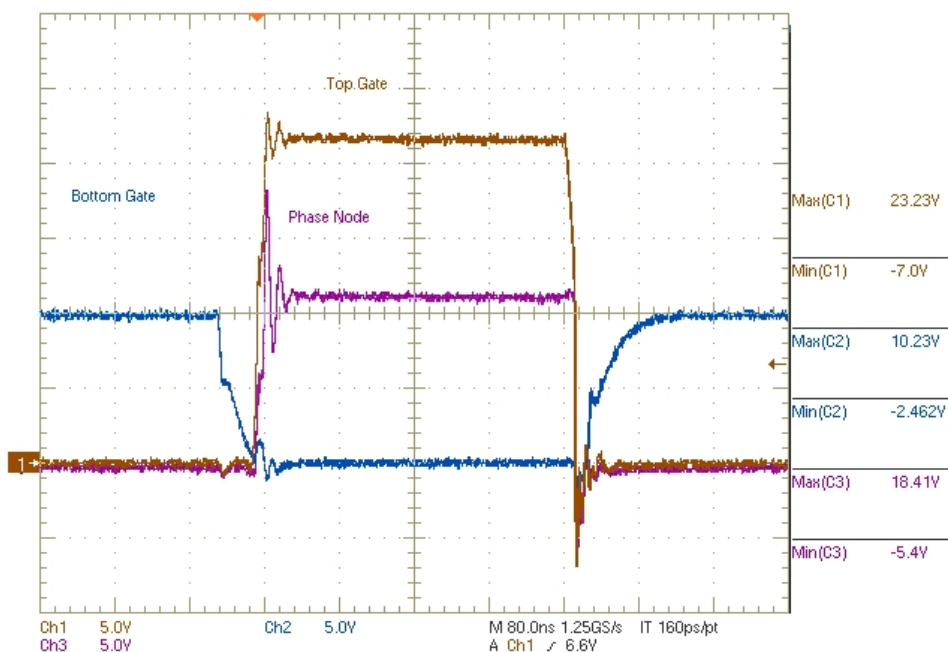


Figure 9: Switch Node Waveforms for $V_{drv} = 10V$

7.4 PWM Signals from the Controller to the Driver at G1 of ONS321G Evaluation Board for $V_{drv} = 5V$ & $10V^{**}$



(i) At $I_{out} = 0A$ ($V_{in} = 12V$; $V_{out} = 1.2V$; $V_{drvr} = 10V$; Freq = 330kHz)

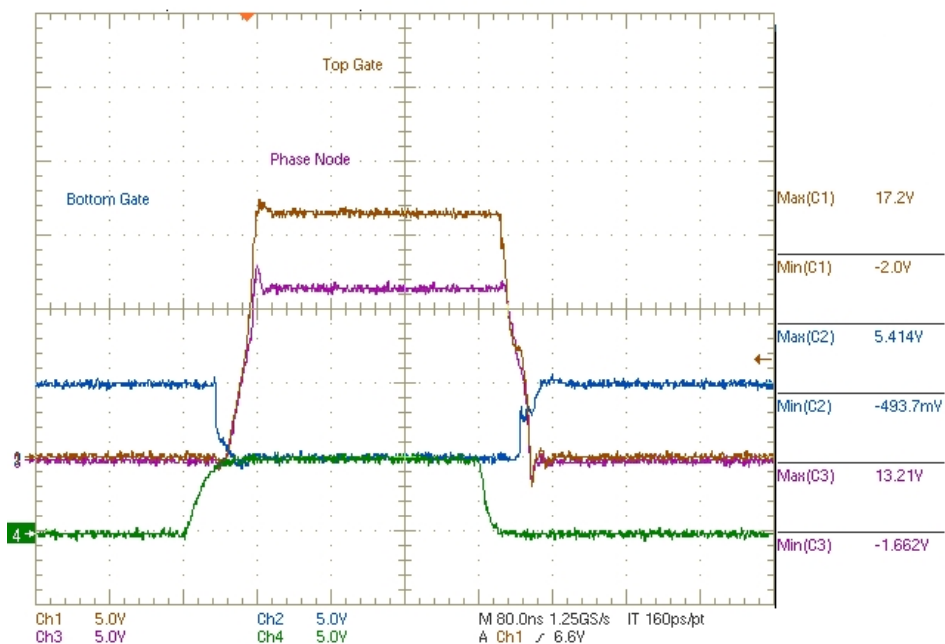


Figure 10: PWM Signal at G1 for $I_{out} = 0A$

(ii) At $I_{out} = 20A$ ($V_{in} = 12V$; $V_{out} = 1.2V$; $V_{drvr} = 10V$; Freq = 330kHz)

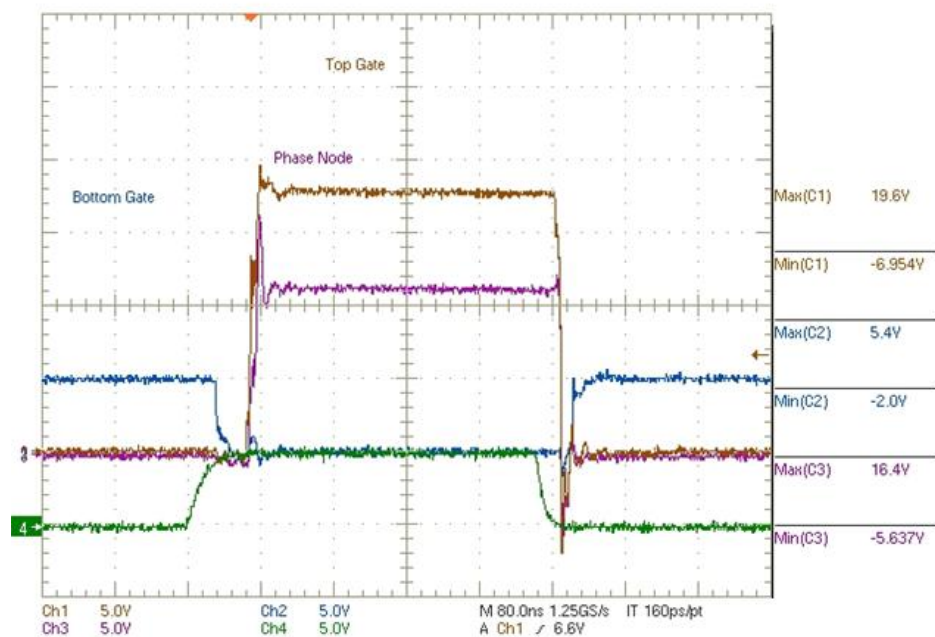


Figure 11: PWM signal at G1 for $I_{out} = 20A$

** The efficiency, power loss, switch node and the PWM signal waveforms presented above are obtained with the 12V driver (NCP5901) but by setting the driver voltage at 10V.