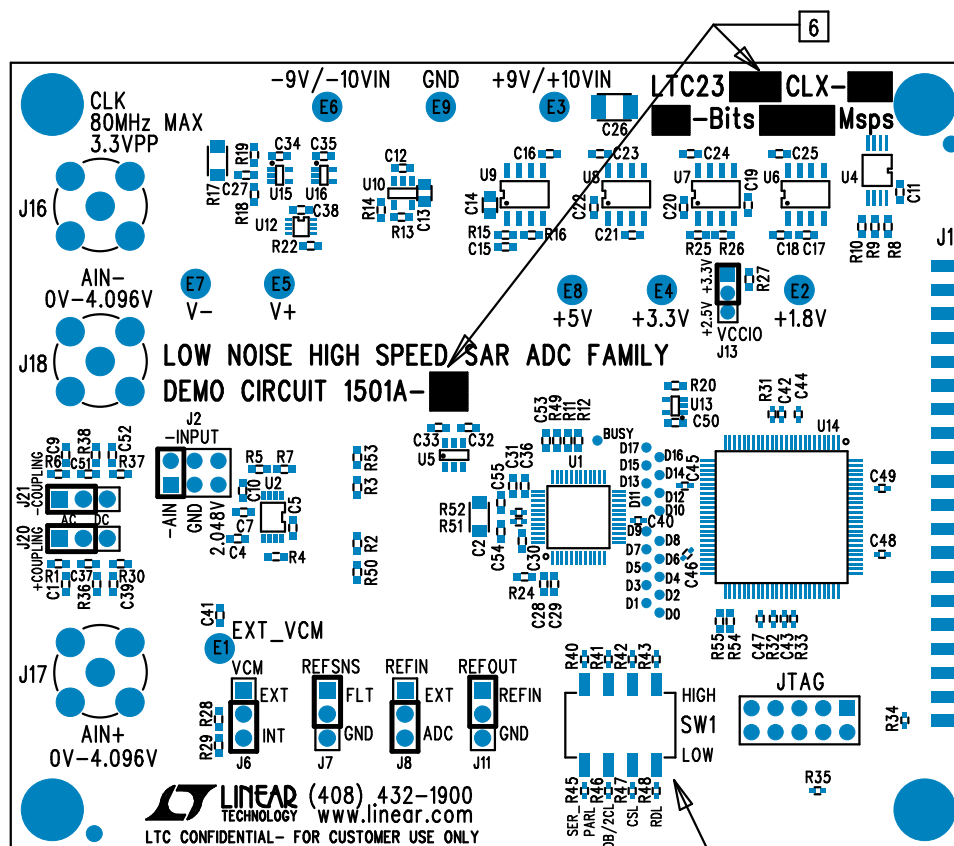


# REVISION HISTORY

ECO	REV	DESCRIPTION	APP ENG.	DATE
1	2	PRODUCTION	GUY H.	11-20-09



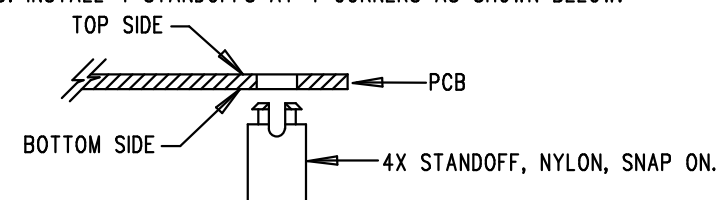
TOP SILKSCREEN

LINEAR TECHNOLOGY  
DC1501A-LTC23XXCLX FAMILY

LOW NOISE HIGH SPEED SAR ADC FAMILY  
11-20-09

## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
5. INSTALL 4 STANDOFFS AT 4 CORNERS AS SHOWN BELOW:



6. MARK EACH LTC PART NUMBER AND ASSEMBLY TYPE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	U1	Bits	Mbps
A	LTC2393CLX-16	16	1
B	LTC2392CLX-16	16	0.5
C	LTC2391CLX-16	16	0.25

7. SW1 DEFAULT SETUP:  
SET LOW FOR: SER\_PARL; CSL; RDL.  
SET HIGH FOR: OB/2CL

## APPROVALS

PCB DES. KT

APP ENG. GUY H.



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TITLE: TOP ASSEMBLY DRAWING

LOW NOISE HIGH SPEED SAR ADC FAMILY

SIZE N/A IC NO. LTC23XXCLX FAMILY  
DEMO CIRCUIT 1501A REV 2

SCALE = NONE

FILENAME: DC1501A-2.PCB

SHT 1 of 1