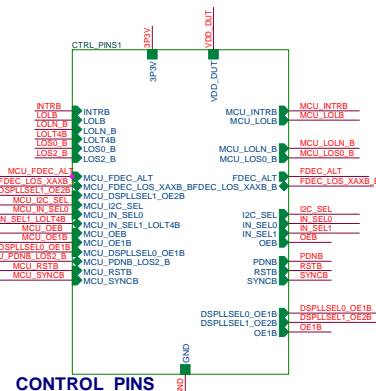
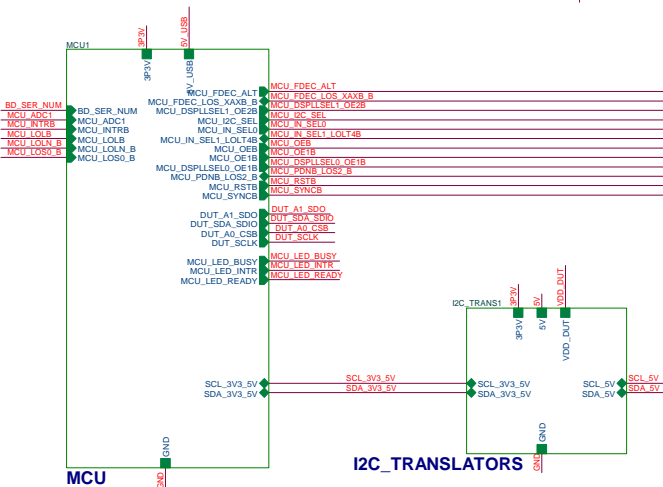
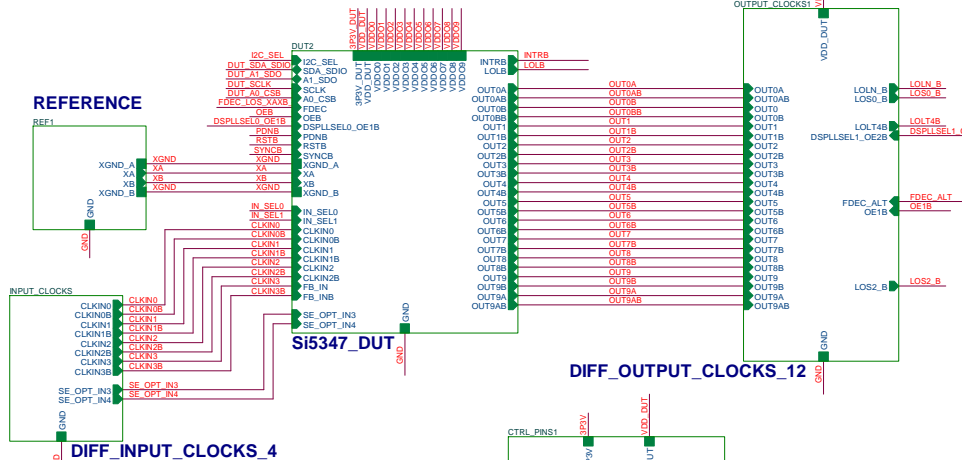


Select a block and then select "Descend" to open the block schematic.

To ascend from a block schematic, right click and select "Previous View".



5 4 3 2 1

D

C

B

A

REVISION CONTROL

Rev 1.0 - July 1, 2014, Initial Board Release

D

C

B

A

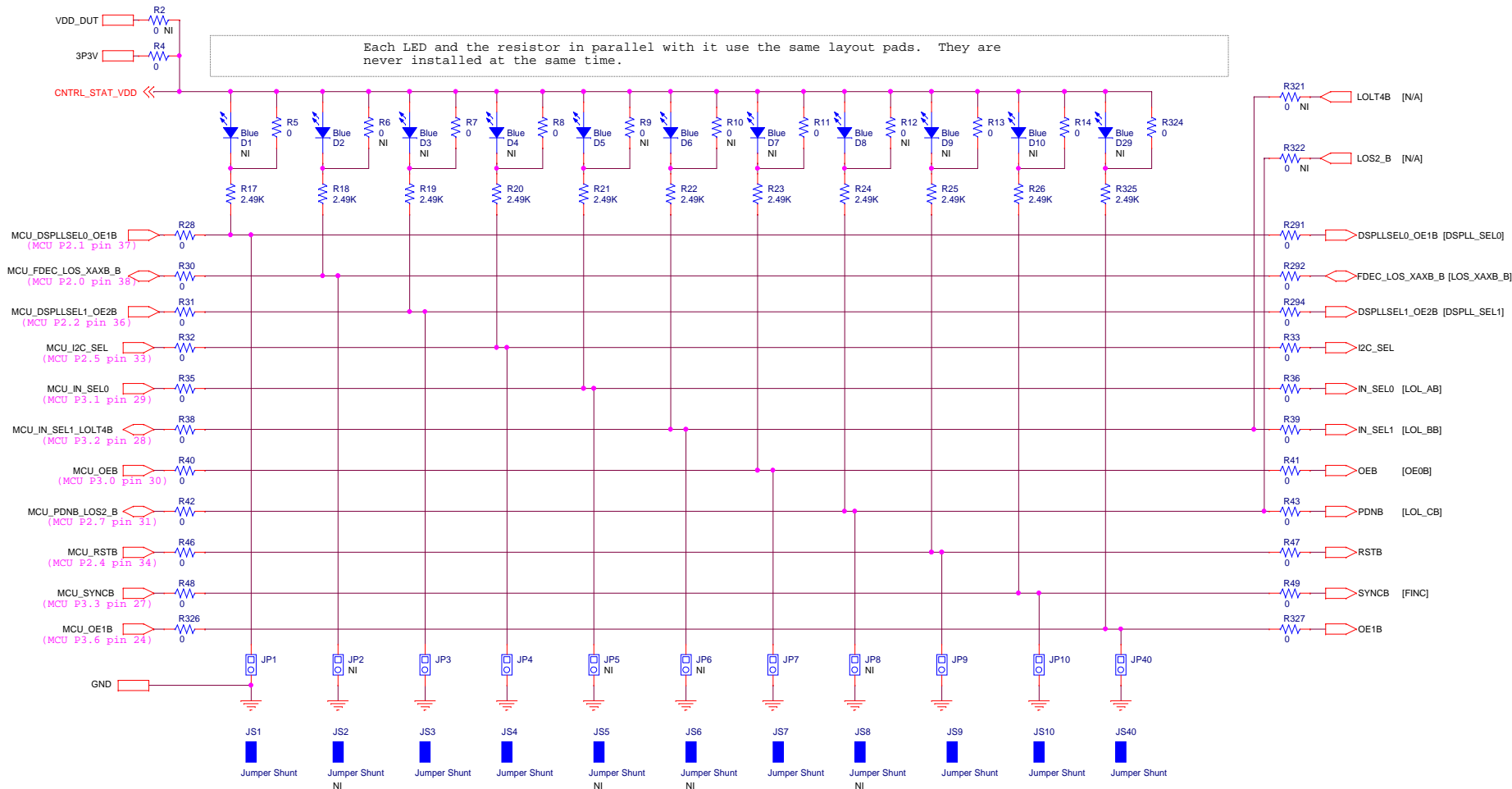
<Core Design>



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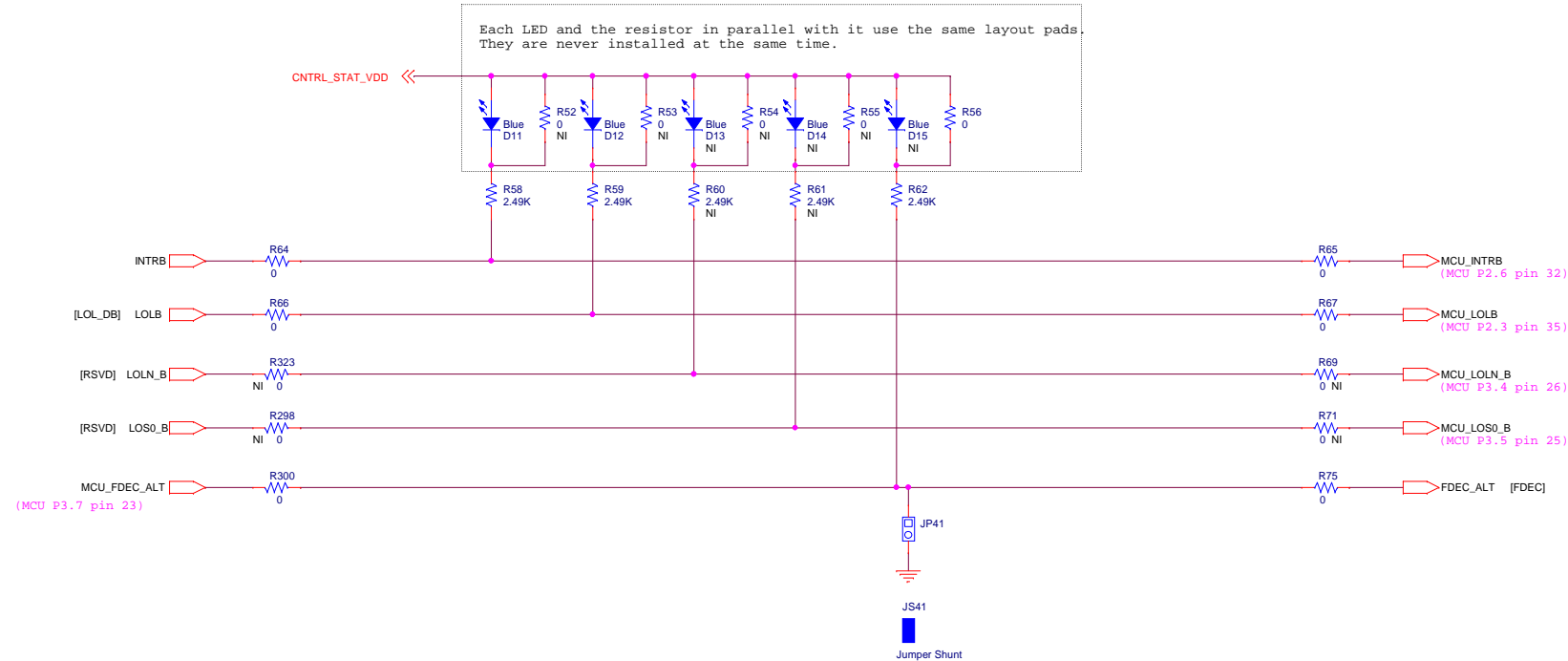
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Size	Document Number	Rev	
B	SI5347-EB	1.0	
Date:	Tuesday, July 22, 2014	Sheet	2 of 44

CONTROL PINS CONNECTIONS

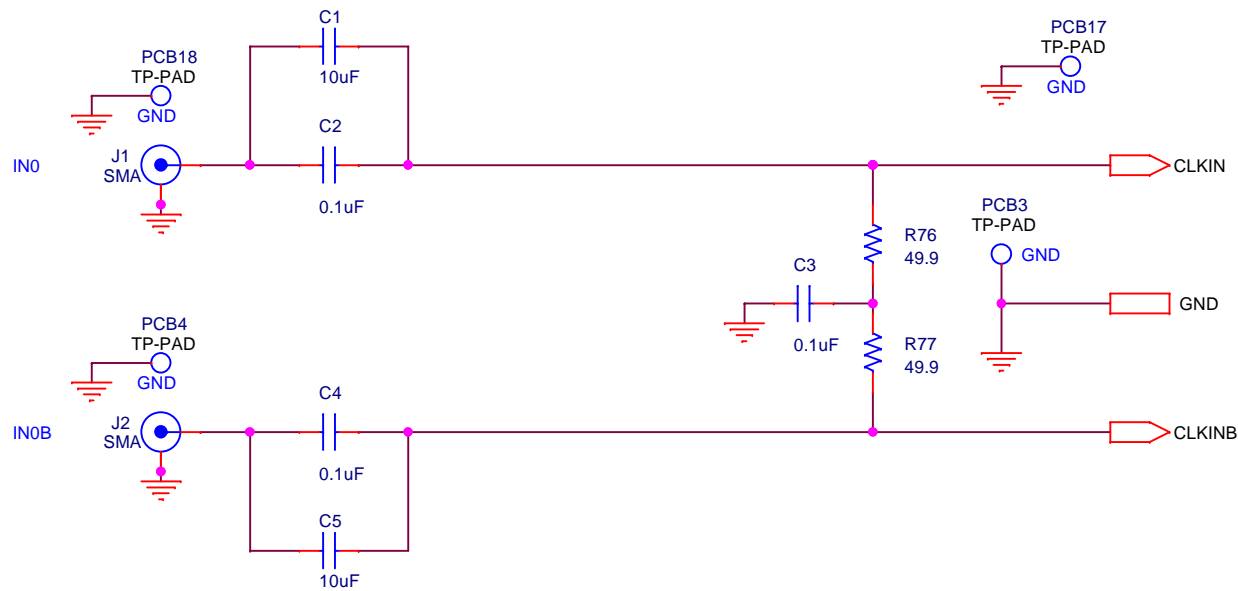


Note:
1. If the net name differs from the DUT pin name, the corresponding DUT pin name is enclosed in brackets. [DUT Pin Name]
2. "N/A" in brackets indicates that the function is not used for the DUT. [N/A]

CONTROL PINS CONNECTIONS



Note:
1. If the net name differs from the DUT pin name, the corresponding DUT pin name is enclosed in brackets. [DUT Pin Name]
2. "N/A" in brackets indicates that the function is not used for the DUT. [N/A]



<Core Design>



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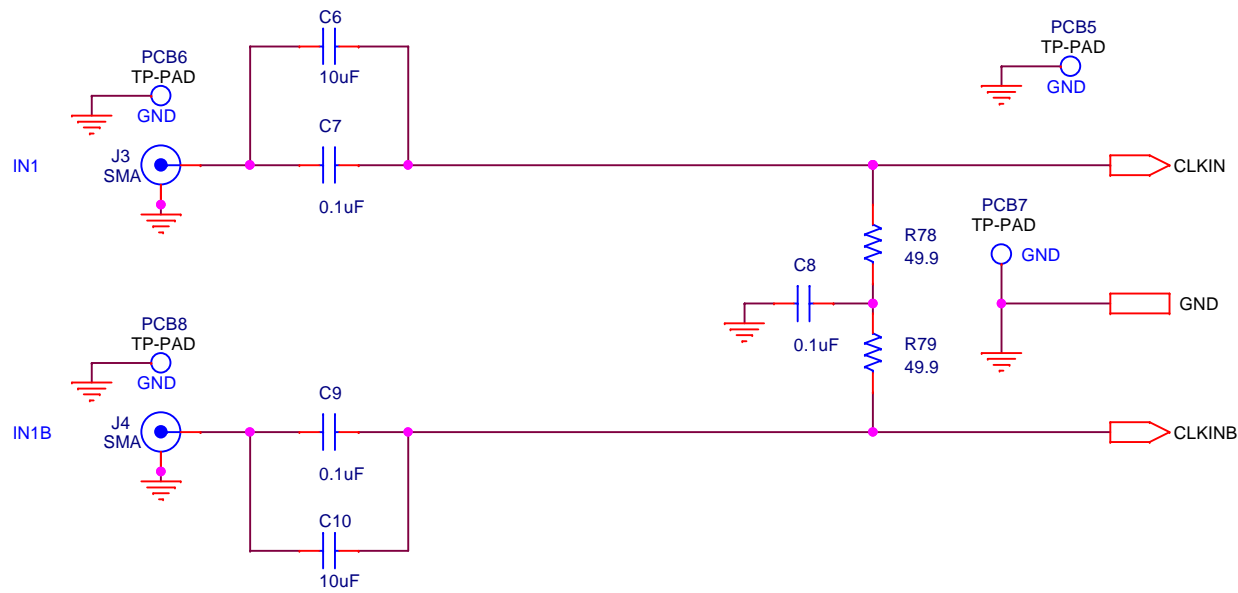
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DIFF_INPUT_CLK

Size
A Document Number
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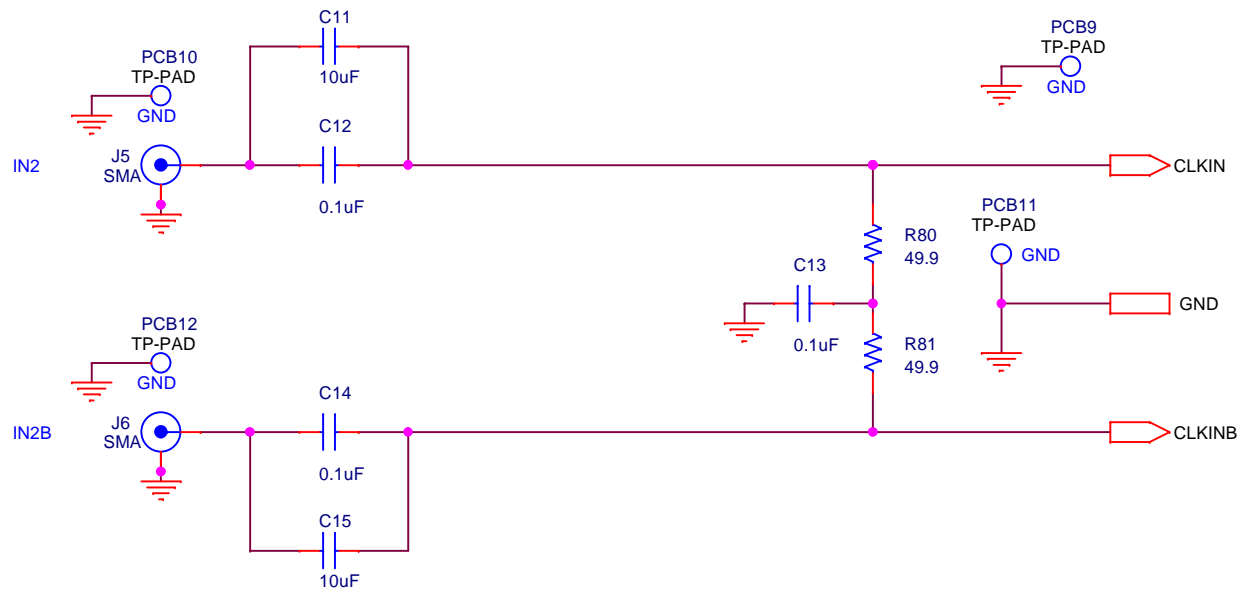
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Size
A Document Number
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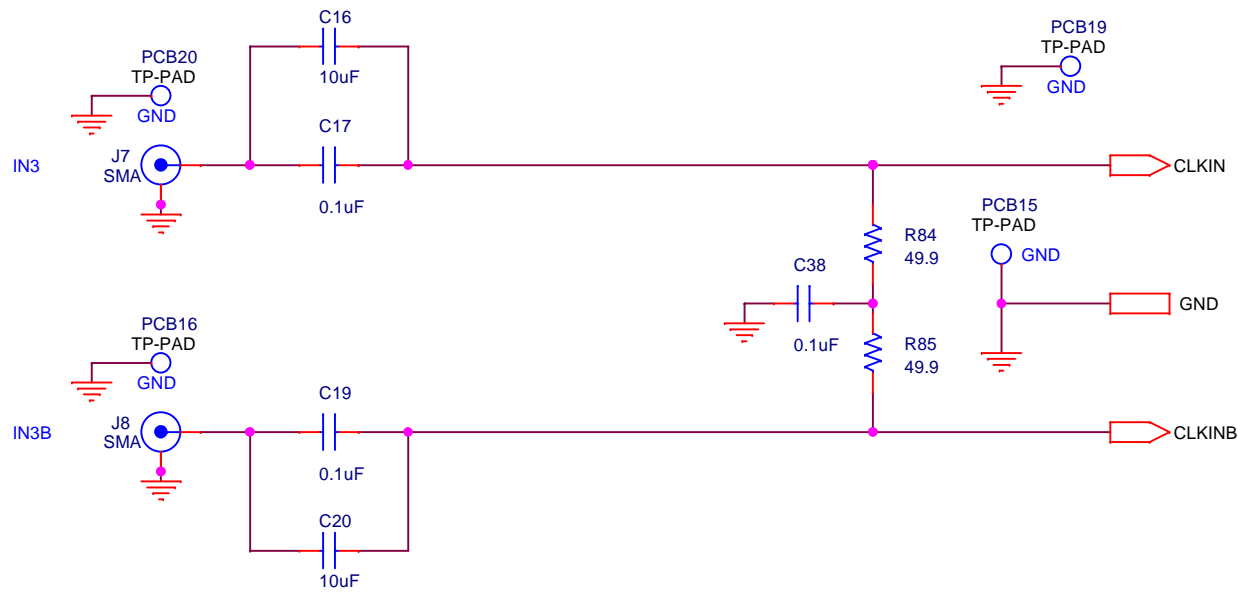
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<Core Design>

		400 W Cesar Chavez Austin, TX 78701	
Title		DIFF_INPUT_CLK	
Size A	Document Number SI5347-EB		Rev 1.0
Date: Tuesday, May 20, 2014		Sheet 7 of 44	



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Title
DIFF_INPUT_CLK

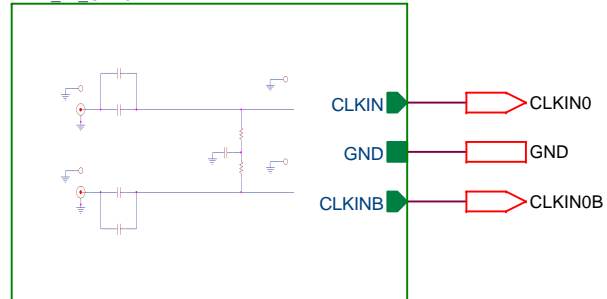
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A Document Number
SI5347-EB

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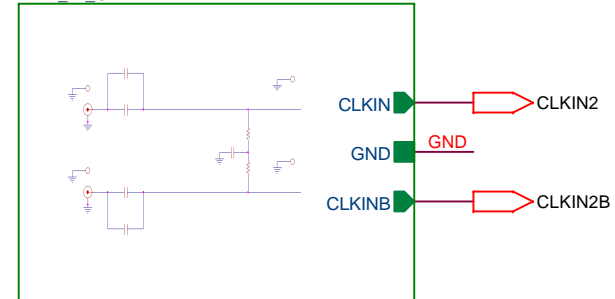
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DIFF_IN_CLK0



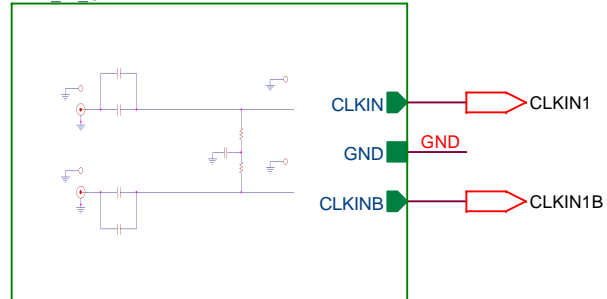
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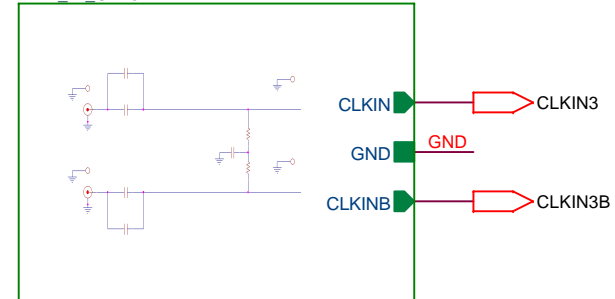
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DIFF_IN_CLK1

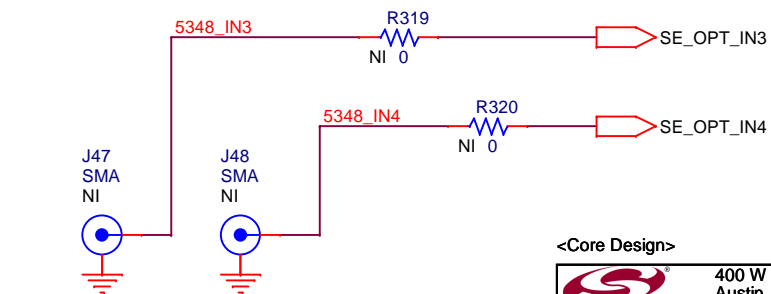


DIFF_INPUT_CLK

DIFF_IN_CLK3



DIFF_INPUT_CLK



Single-ended clocks only.

<Core Design>



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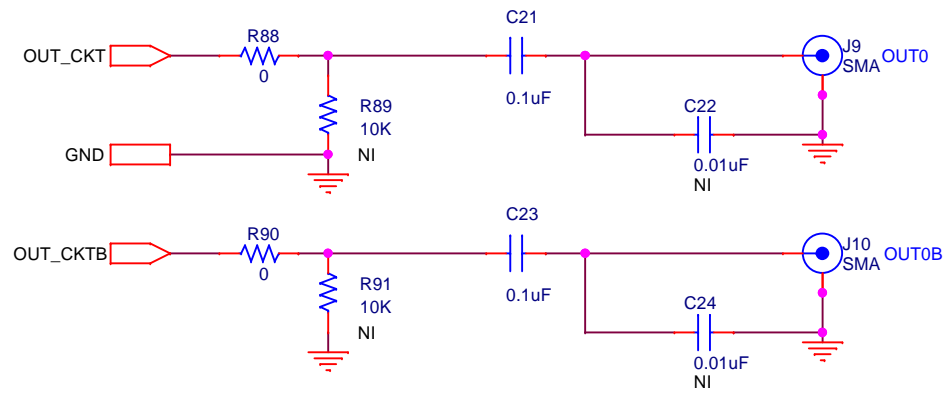
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Size
A Document Number
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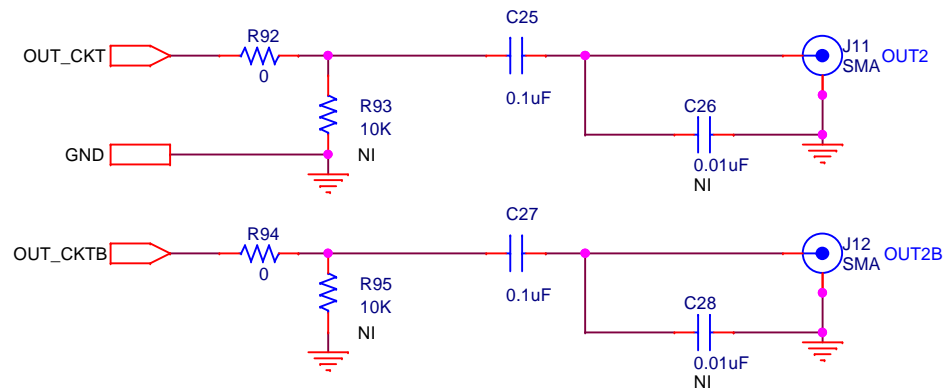
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Size
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Title
DIFF_OUTPUT_CLK

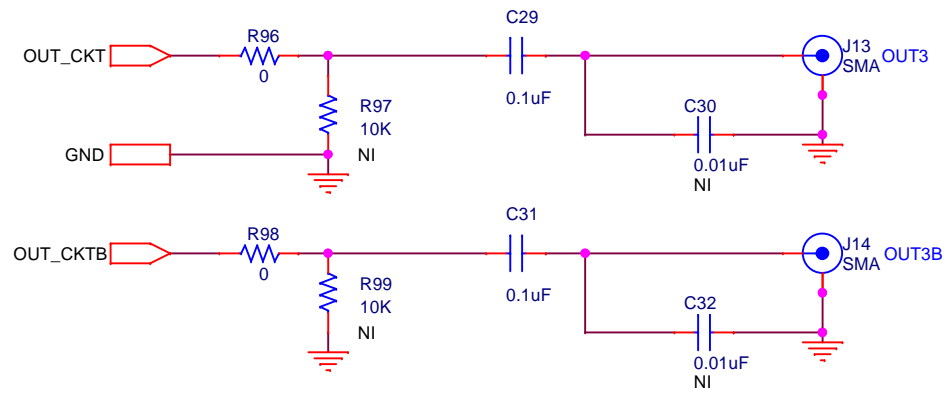
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<Core Design>



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Title

DIFF_OUTPUT_CLK

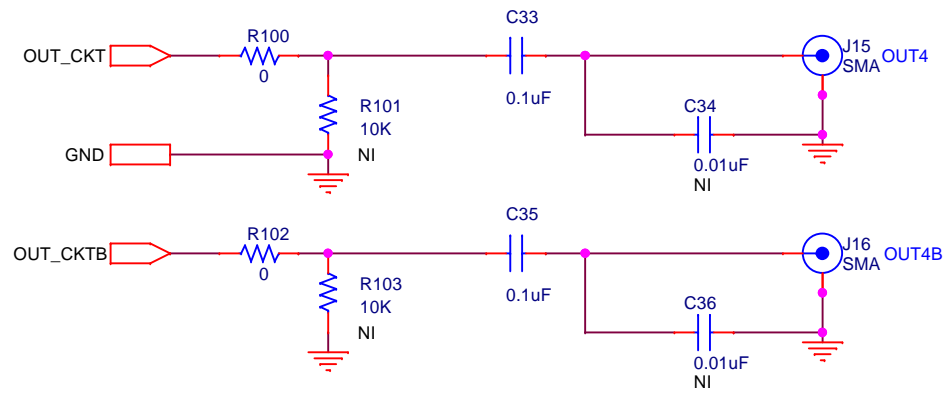
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Title

DIFF_OUTPUT_CLK

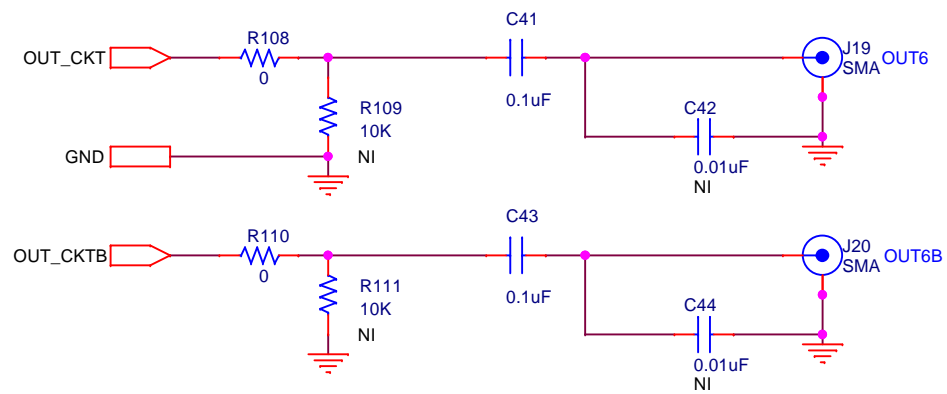
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<Core Design>



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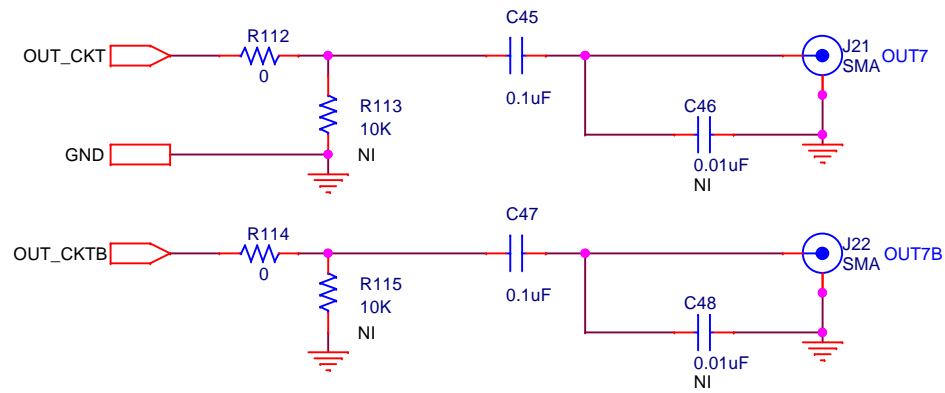
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Size
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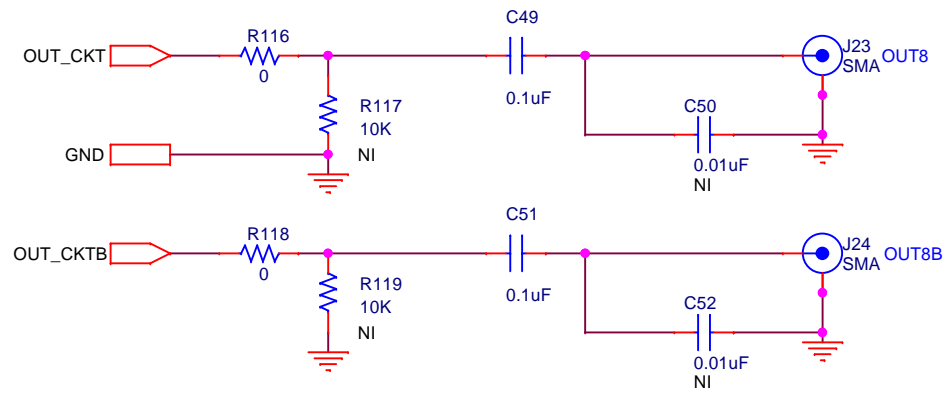
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Size
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<Core Design>



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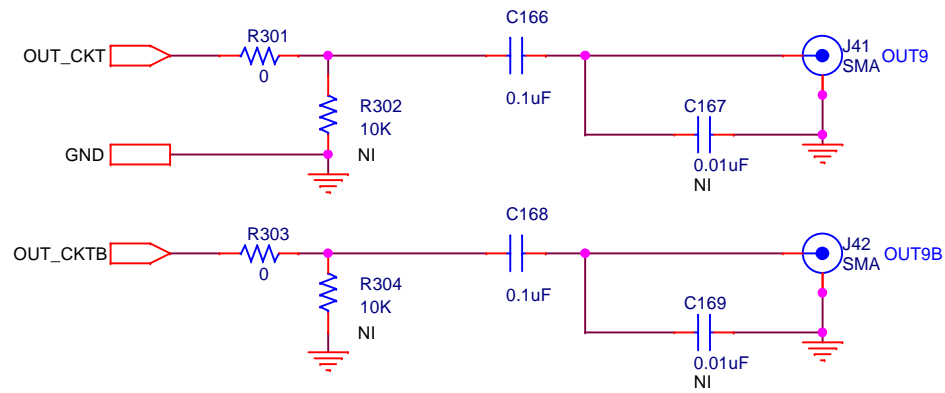
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<Core Design>



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Title
DIFF_OUTPUT_CLK

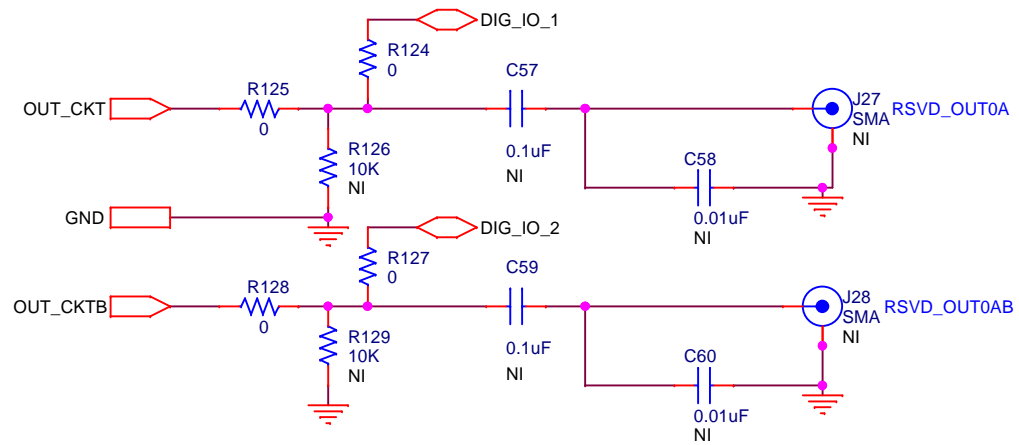
Size
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DIFF_OUTPUT_CLK_DIG_IO



<Core Design>



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Title
DIFF_OUTPUT_CLK_DIG_IO

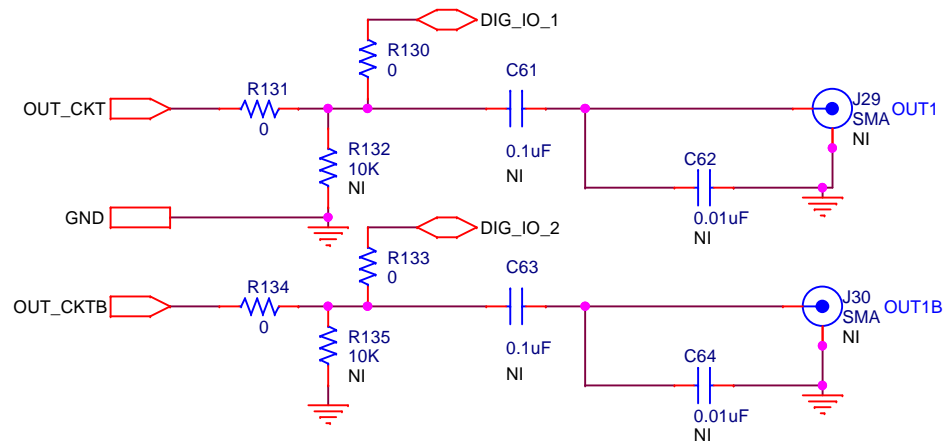
Size
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DIFF_OUTPUT_CLK_DIG_IO



<Core Design>



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Title
DIFF_OUTPUT_CLK_DIG_IO

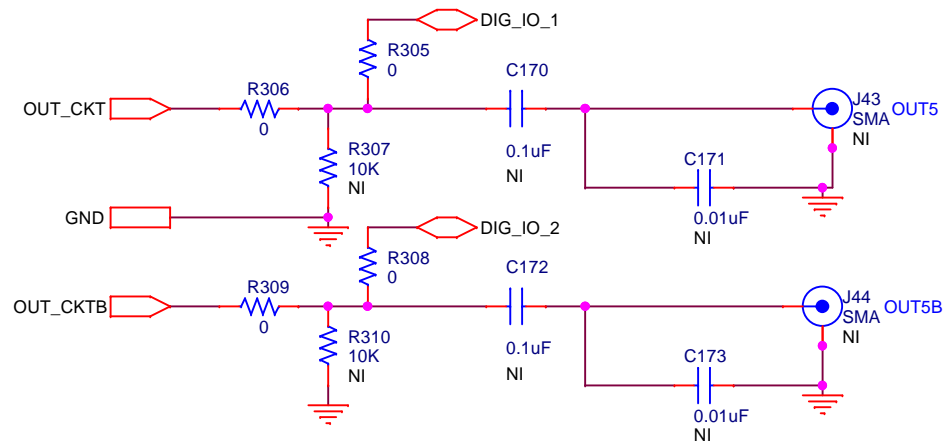
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DIFF_OUTPUT_CLK_DIG_IO



<Core Design>



400 W Cesar Chavez
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Title
DIFF_OUTPUT_CLK_DIG_IO

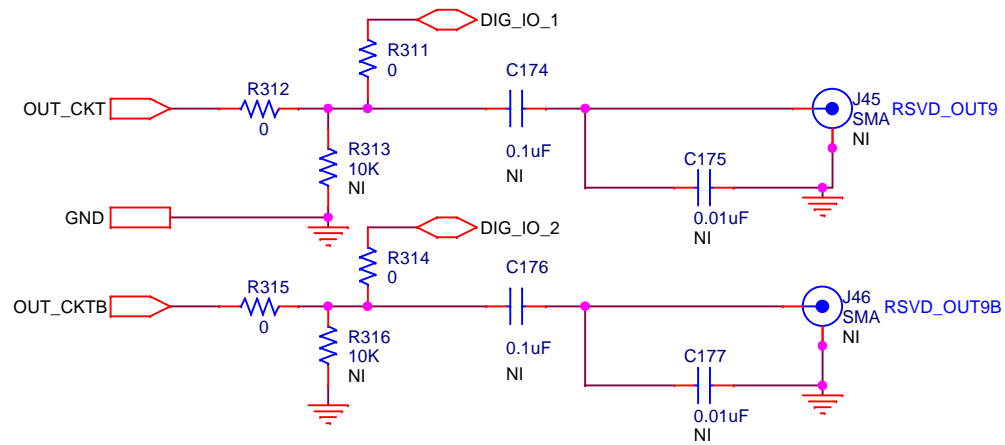
Size
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DIFF_OUTPUT_CLK_DIG_IO



<Core Design>



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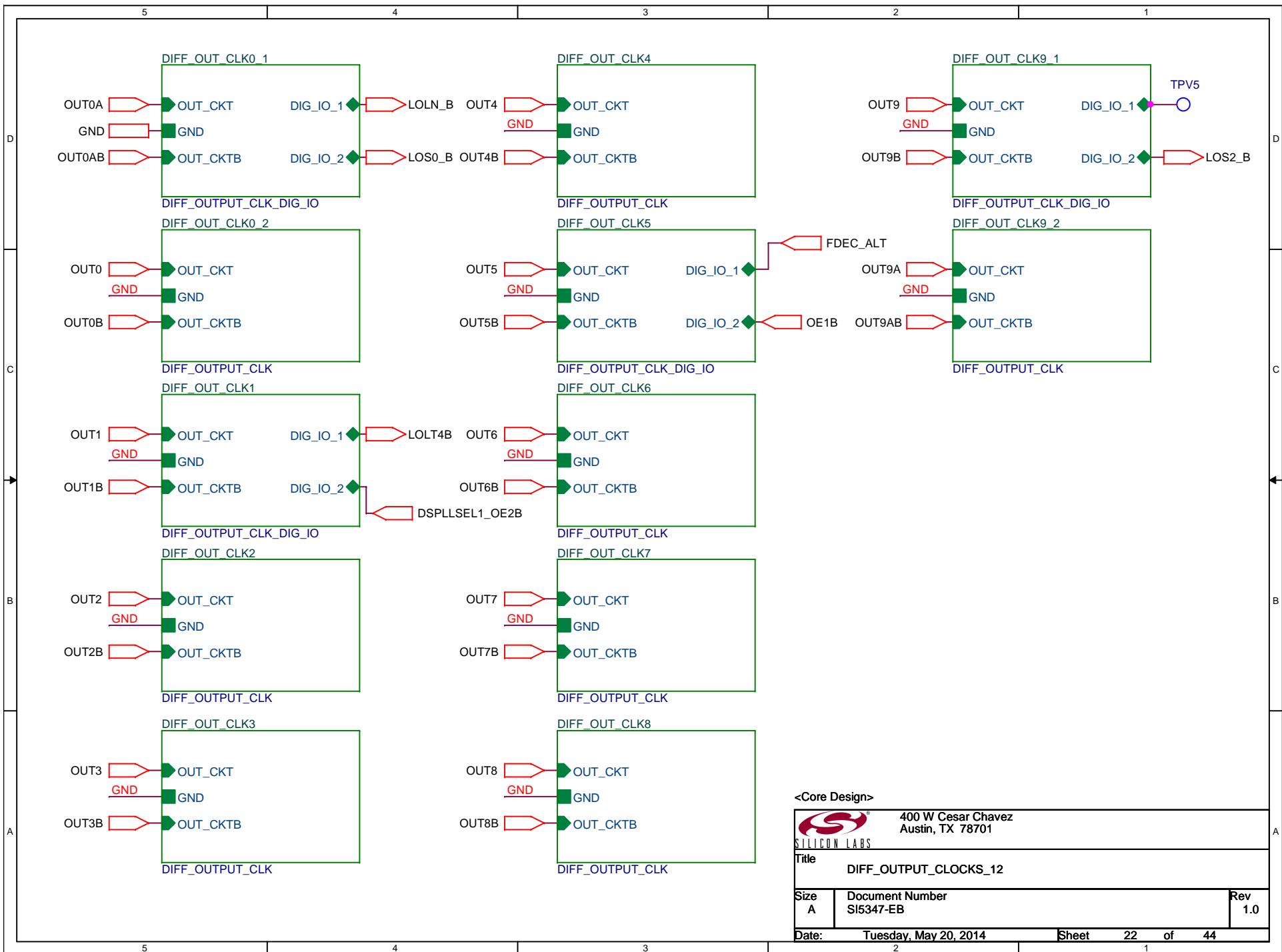
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SI5347-EB

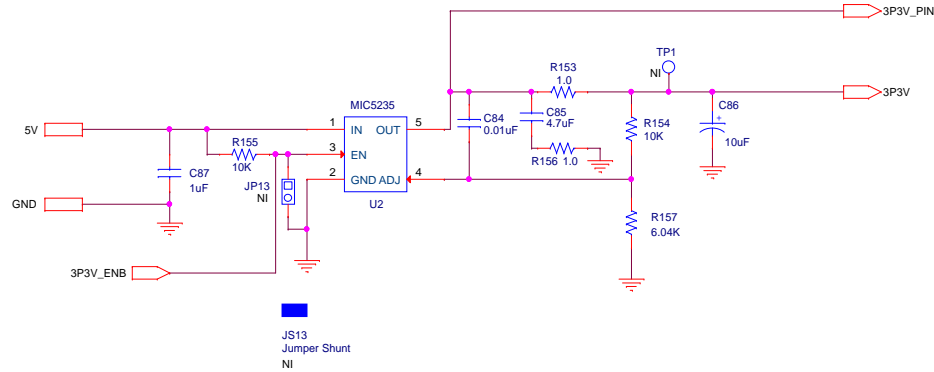
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Fixed 3.3V 150 mA Voltage Regulator



3P3V_DUT (In Schematic)
VDDA DUT (Board Silkscreen)



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number
SIS347-EB

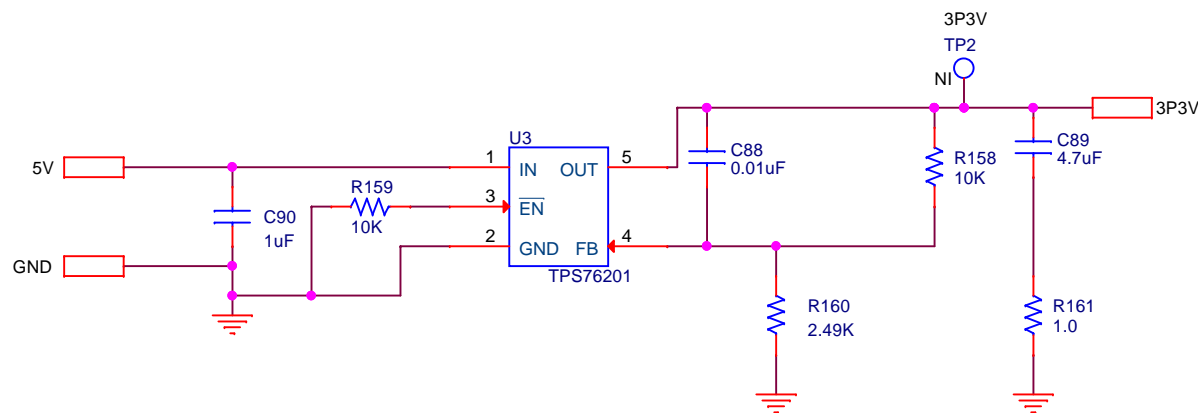
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Rev
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Date: Tuesday, May 20, 2014

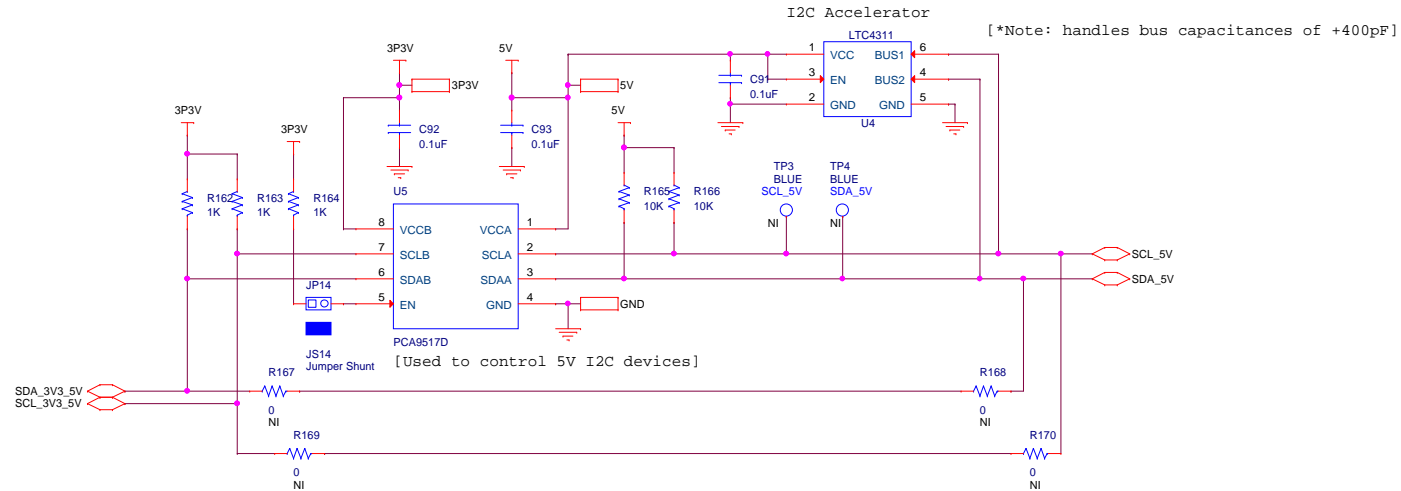
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Fixed 3.3V 100 mA Voltage Regulator (Enabled, no current measurement supported.)



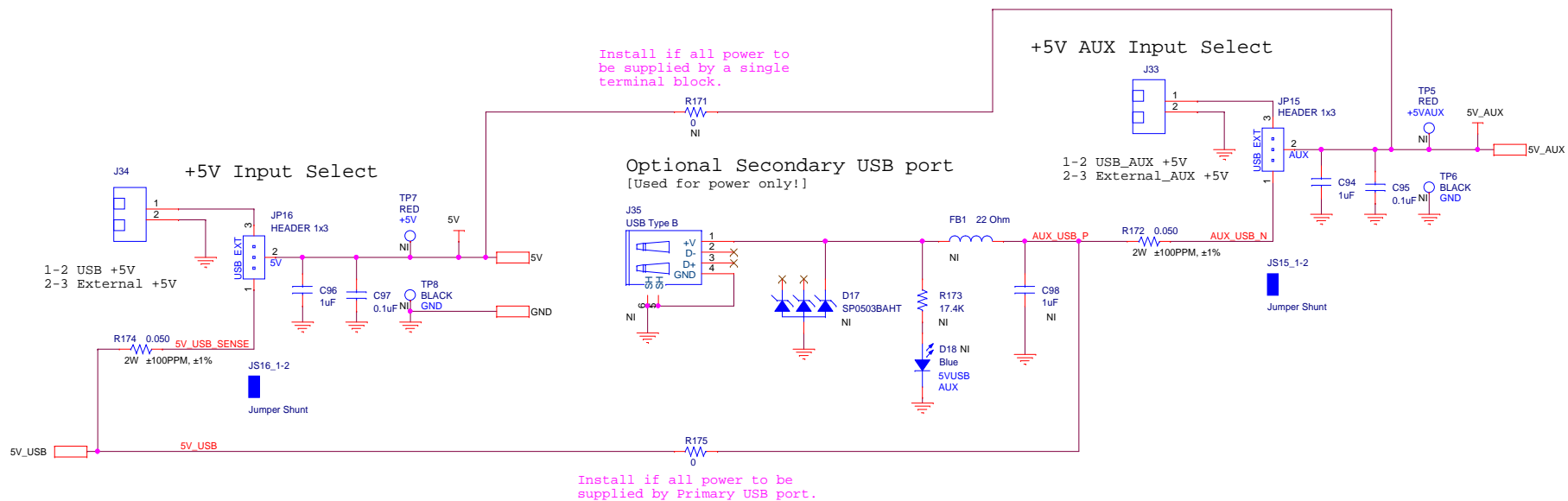
3P3V (In Schematic)
3P3V (Board Silkscreen)

I2C LEVEL TRANSLATORS



MAIN POWER INPUT SELECTS

EXTERNAL OR USB



<Core Design>



400 W Cesar Chavez
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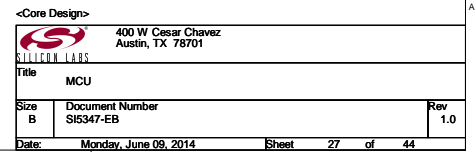
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Size B Document Number
SIS347-EB

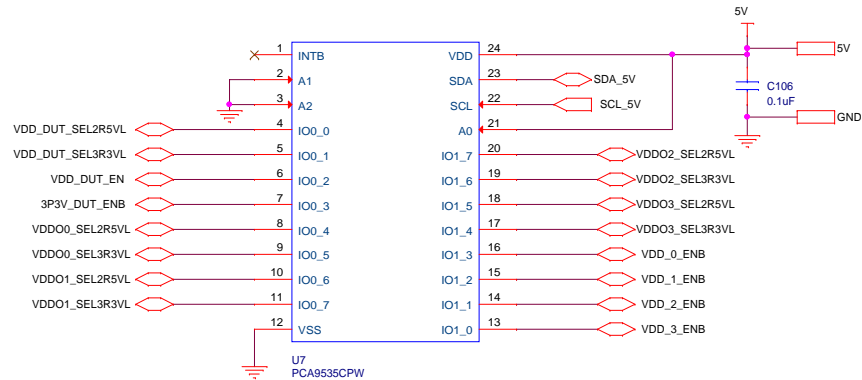
Rev
1.0

Date: Monday, June 09, 2014

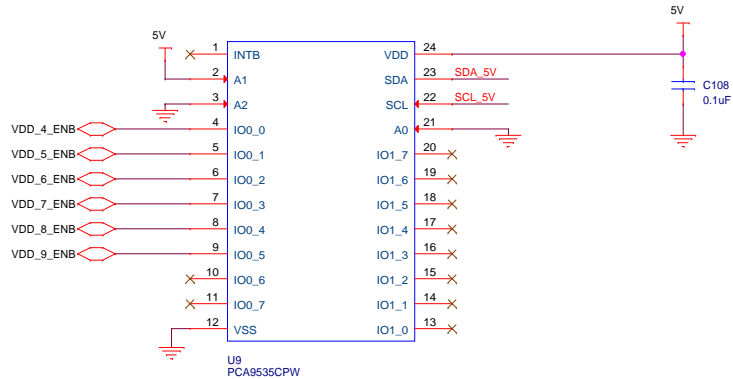
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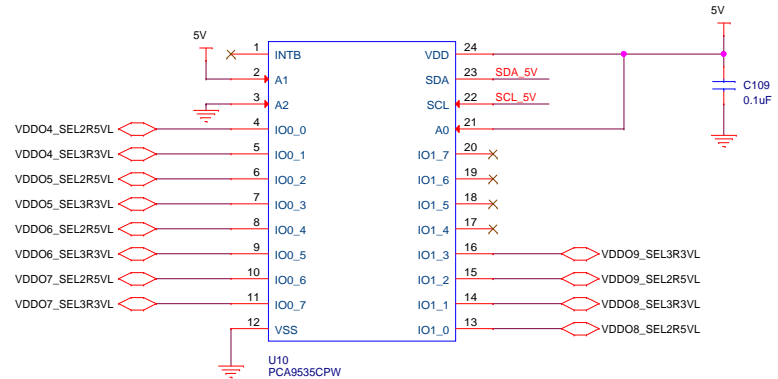
PERIPHERALS



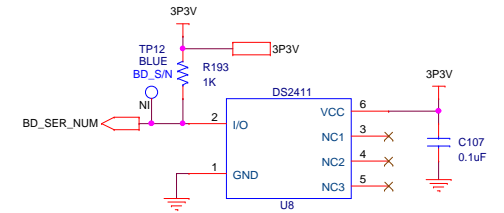
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I2C address: 0100001



I2C VDD ENABLES/SELECTS FOR REGULATORS
I2C address: 0100010



I2C VDD ENABLES/SELECTS FOR REGULATORS
I2C address: 0100011



Board Serial Number

<Core Design>



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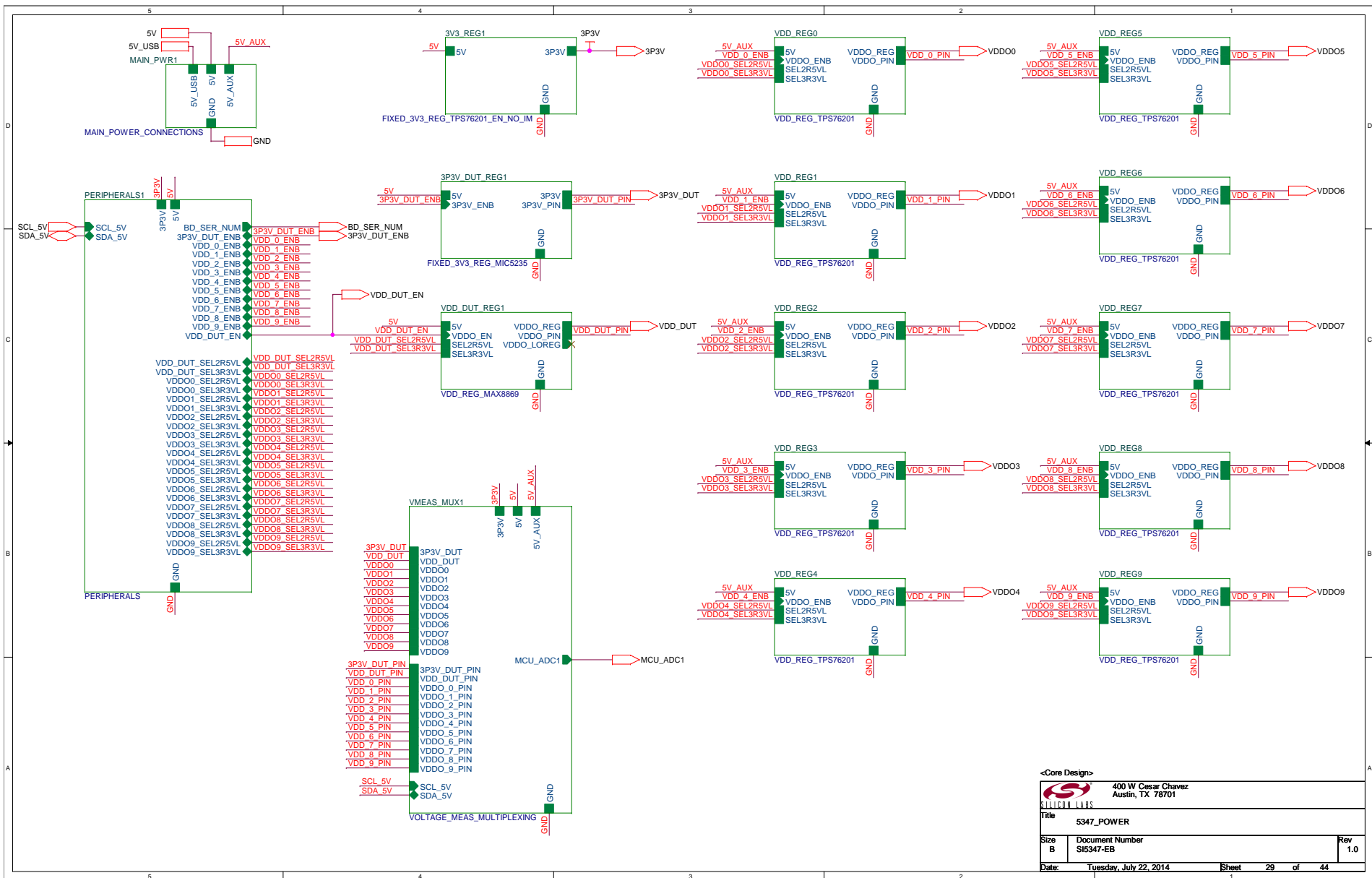
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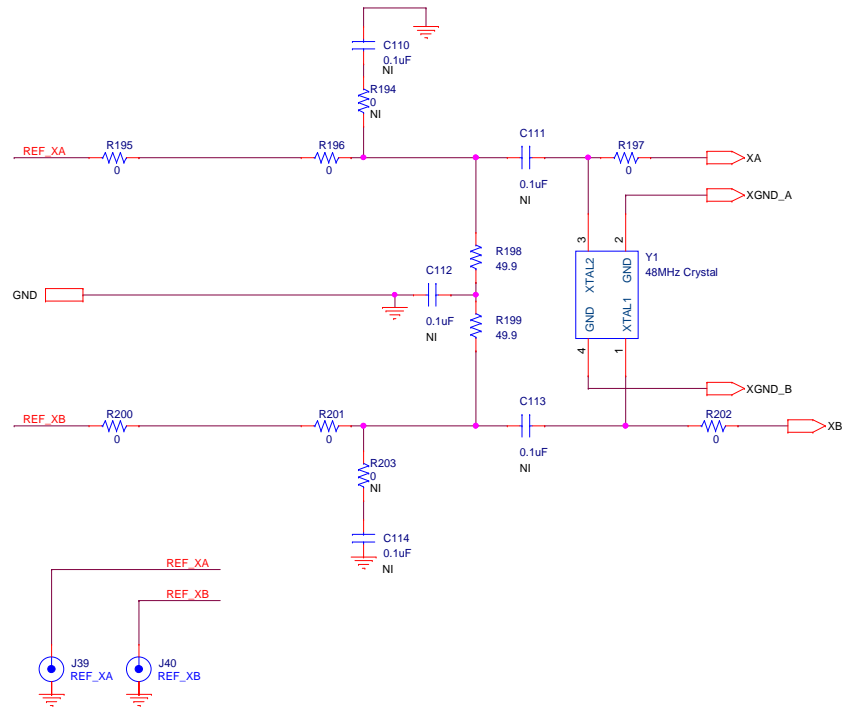
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REFERENCE INPUT CONNECTIONS



<Core Design>



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Title REFERENCE

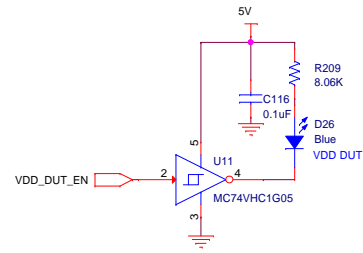
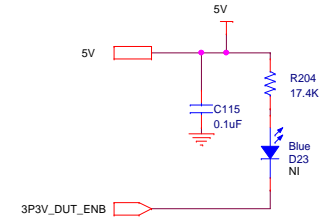
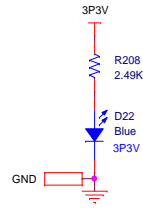
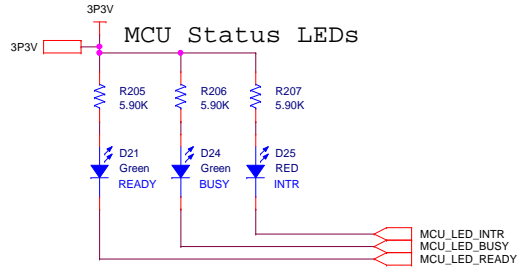
Size B Document Number
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LEDS - SUPPLIES & STATUS



<Core Design>



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Title STATUS_MAIN_SUPPLY_LEDS

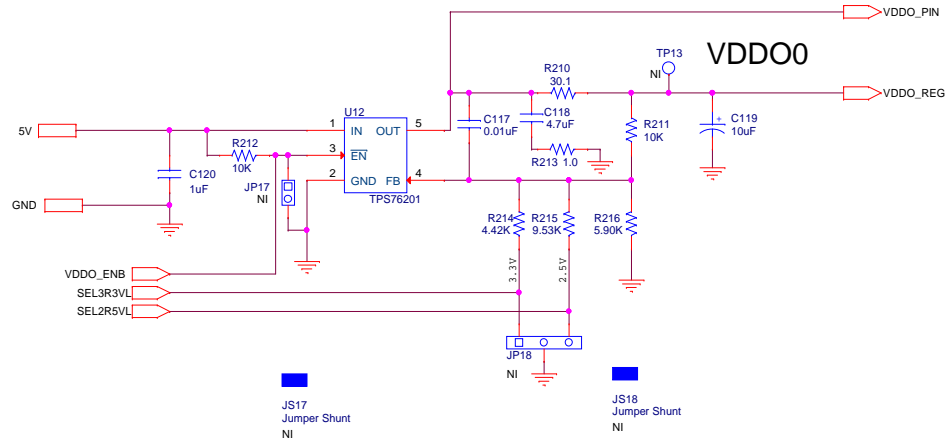
Size B Document Number
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number
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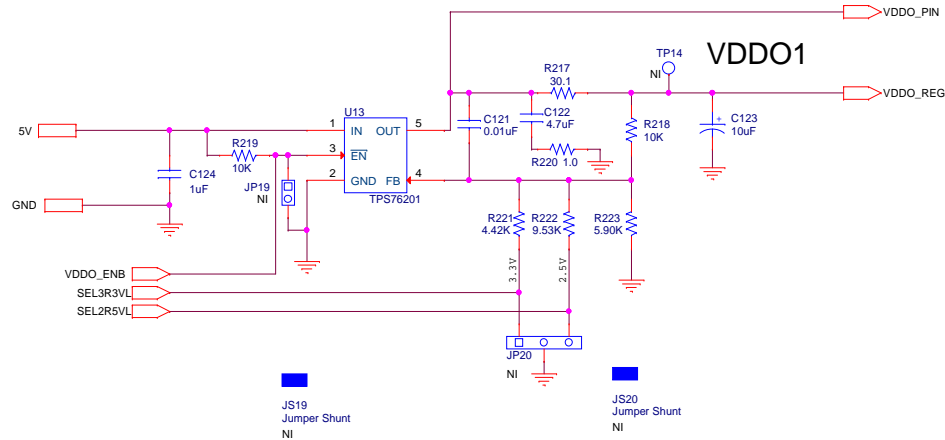
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B

Document Number
SIS347-EB

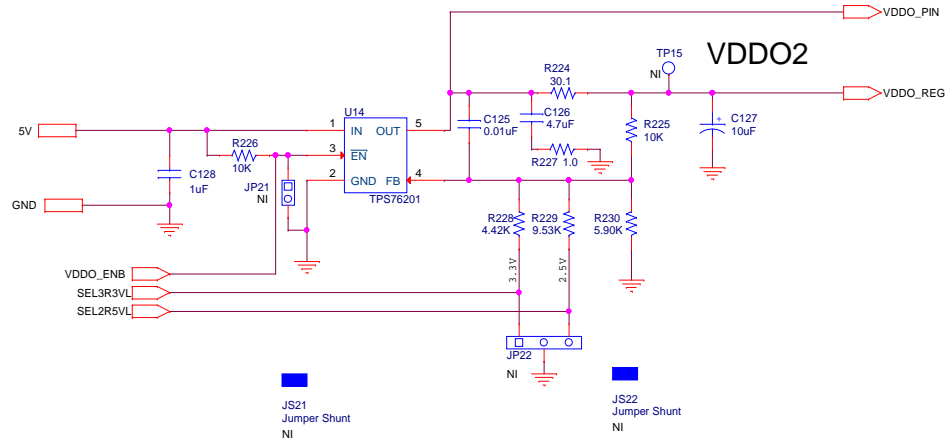
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number SIS347-EB

<Core Design>

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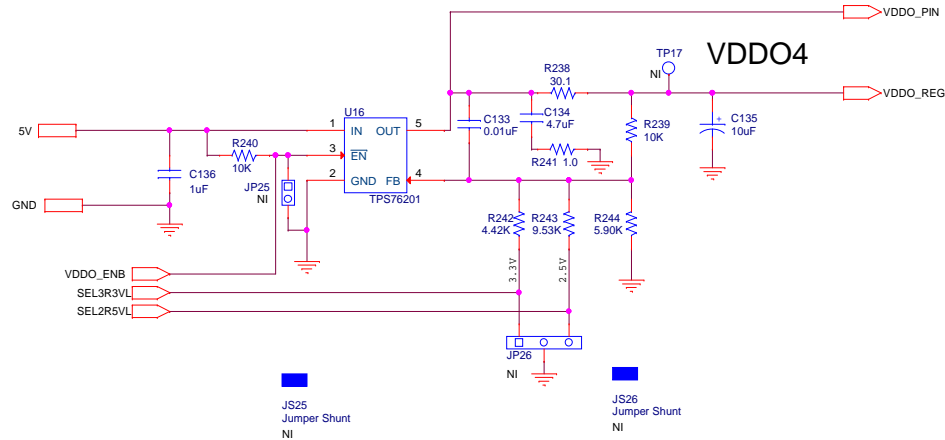
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D



CC

100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number
SIS347-EB

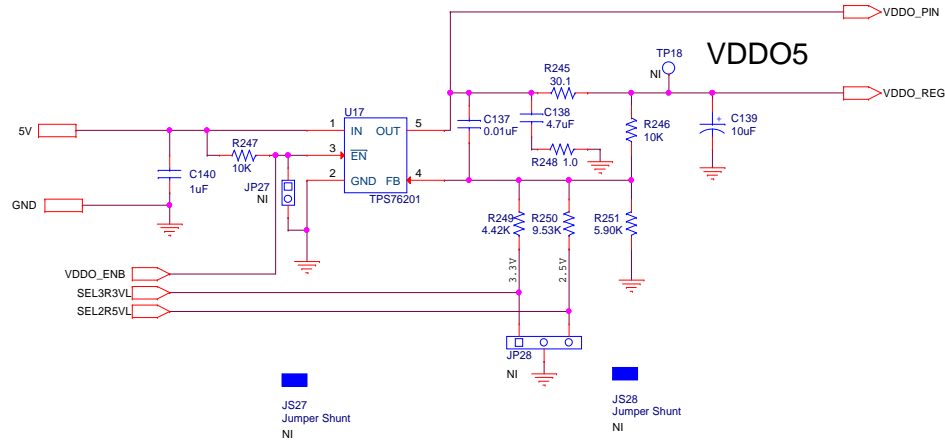
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number
SIS347-EB

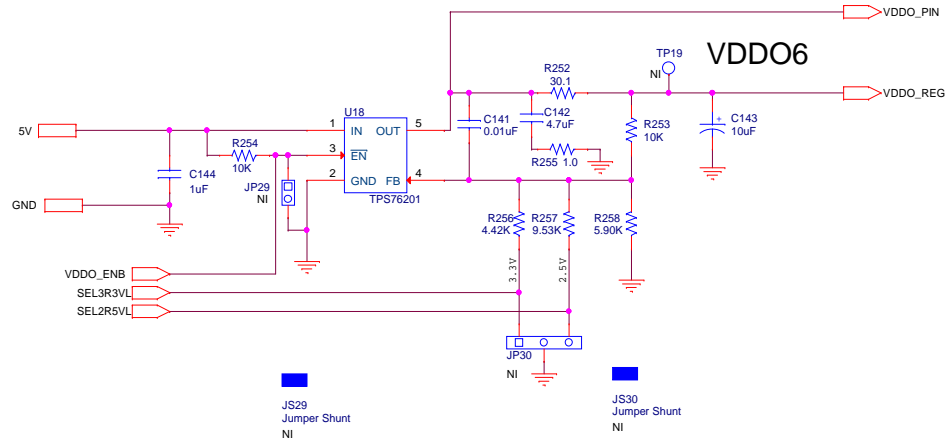
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

Size B Document Number
SIS347-EB

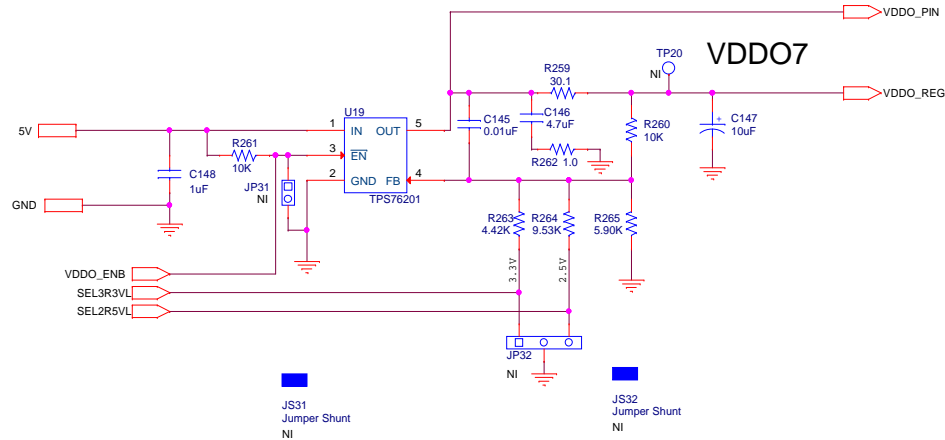
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100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

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<Core Design>

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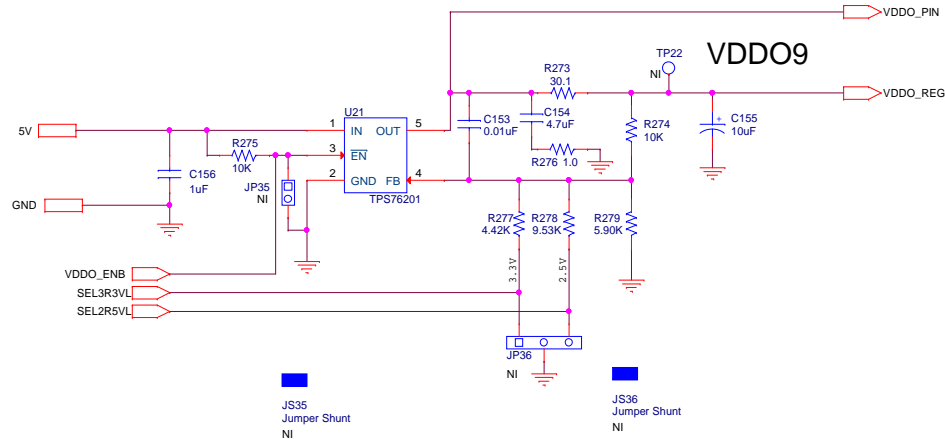
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D



100 mA Adjustable Voltage Regulator



400 W Cesar Chavez
Austin, TX 78701

Title VDD_REG_TPS76201

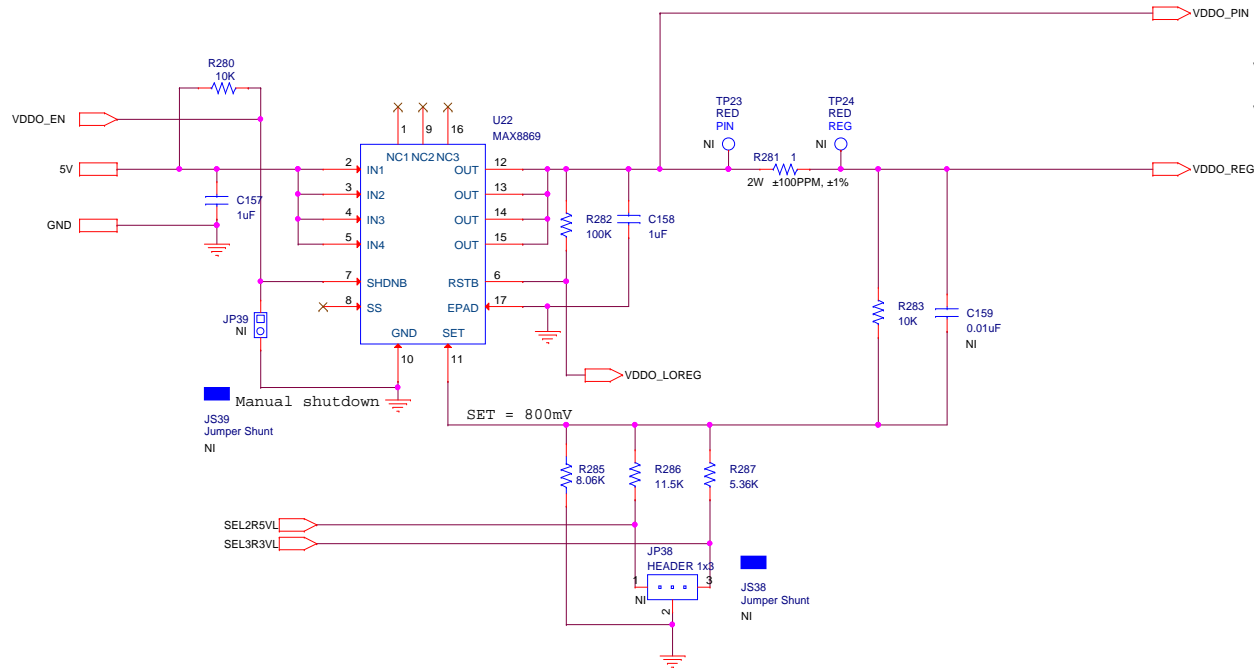
Size B Document Number SIS347-EB

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VDD_DUT (In Schematic)
VDD DUT (Board Silkscreen)

VOLTAGE MEASUREMENT MULTIPLEXING

