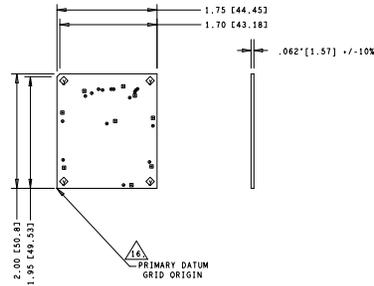


NOTES (UNLESS OTHERWISE SPECIFIED):

- THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
- THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.
- BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98
 T_g - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
 T_d - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
- COPPER FOIL WEIGHT - SEE STACKUP DETAIL "A"
- CHARACTERISTIC IMPEDANCE - SINGLE ENDED - 50 OHMS +/- 10% FOR ALL SIGNAL LAYERS .006" (0.1534 MM) TRACES.
- MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .006"/.00599"
- PLATING FINISH - A. BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):
 .05080-.232 MICRON (2-8 MICROINCH) OF GOLD OVER
 2.540-6.350 MICRON (100-250 MICROINCH) OF NICKEL.

- ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.
- SOLDERMASK - WHITE COLOR .BOTH SIDES.
 MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.
- SILKSCREEN - BLACK EPOXY INK .BOTH SIDES. NO SILK ON PADS.
- ELECTRICAL TEST - 100% IPCD356.
- PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.
- DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS.
 UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.
- ADD TEARDROPS TO ALL SIGNAL LAYERS.
- SOLDER SAMPLES TO BE PROVIDED.
- BASIC GRID INCREMENT AT 1:1 IS .0001.
- SUPPLIER MARKINGS - ON SECONDARY SIDE ONLY, WHERE SHOWN.
 - MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0
- THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (P6) ON SECONDARY SIDE SILKSCREENS
- THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C) ON SECONDARY SIDE SILKSCREENS
- ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.
 ALL HOLE LOCATION TOLERANCES ARE TO BE +/- .002 IN REFERENCE TO THE PRIMARY DATUM.
- FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURING REQUIREMENT.
 ADDITION OF .25" RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER AND FAB HOUSE.



DRILL CHART: TOP 12 HOLE					
FEATURE	SIZE	TOLERANCE	PLATED	IN	OUT
1	10.4	+/- .01-0.0	PLATED	IN	
2	20.8	+/- .01-0.0	PLATED	IN	
3	10.4	+/- .01-0.0	NON-PLATED	IN	
4	10.4	+/- .01-0.0	NON-PLATED	IN	



REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	ORIGINAL RELEASE	03-17-10	P.Lajksner

PART NO. 170-26490		FREESCALE SEMICONDUCTOR	
THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO FREESCALE AND SHALL NOT BE USED FOR ENGINEERING DESIGN PURPOSES OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.		6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES (MM) TOLERANCES ARE: DECIMALS .010 .005 .002 FRACTIONS 1/16 1/32 1/64 Holes .010 PWB ALL MACHINED SURFACES BREAK ALL SHARP EDGES AND CORNERS. REMOVE BURRS. UNDERLINED DIM. NOT TO SCALE. THIRD ANGLE ISOTHERMOMIC PROJECTION IS USED.	APPROVALS DESIGNER: PAVEL LAJSNER CHECKED: PAVEL LAJSNER DESIGN ENGINEER: PAVEL LAJSNER DATE: 03-17-10	TITLE PRINTED WIRING BOARD TWRPI - ROTARY	REV A
DATE: 03-17-10	SIZE: D	DWG. NO.: FAB-26490	SHEET 1 OF 1
SCALE: DO NOT SCALE DRAWING			