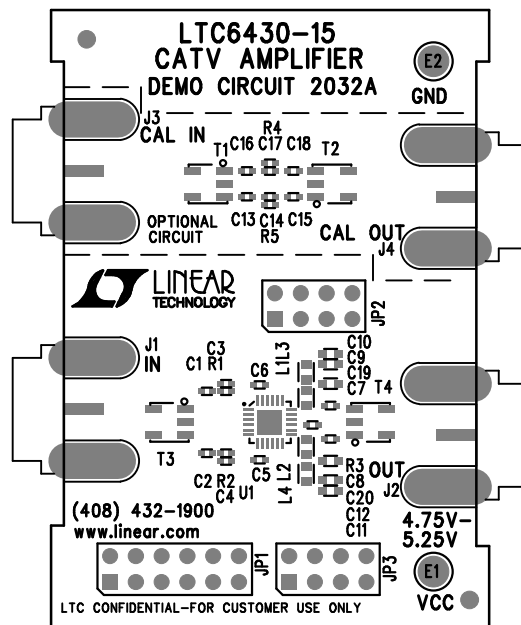


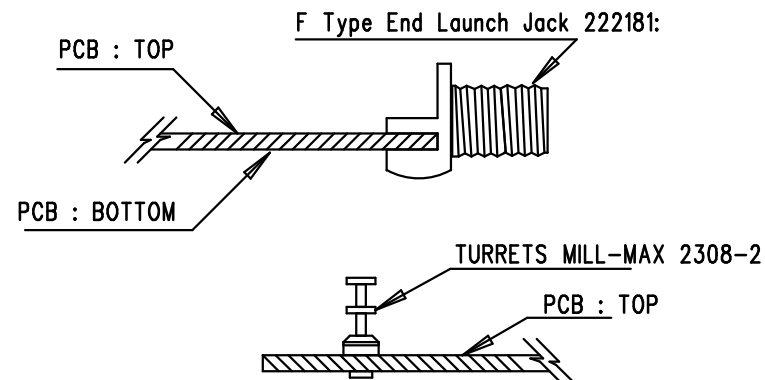
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	JOHN C.	09-17-12


NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. NO SHUNT.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL F TYPE CONNECTORS, AND TURRETS AS SHOWN BELOW:



TOP SILKSCREEN
LINEAR TECHNOLOGY
DC2032A-2, LTC6430-15
CATV AMPLIFIER
DATE: 09-28-12



APPROVALS		 <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL-FOR CUSTOMER USE ONLY </div>		
PCB DES.	AK			
APP ENG.	JOHN C.	TITLE: TOP ASSEMBLY DRAWING		
		CATV AMPLIFIER		
		SIZE N/A	IC NO. LTC6430-15 DEMO CIRCUIT 2032A	REV. 2
SCALE = NONE		FILENAME: DC2032A-2.PCB		SHT 1 OF 1