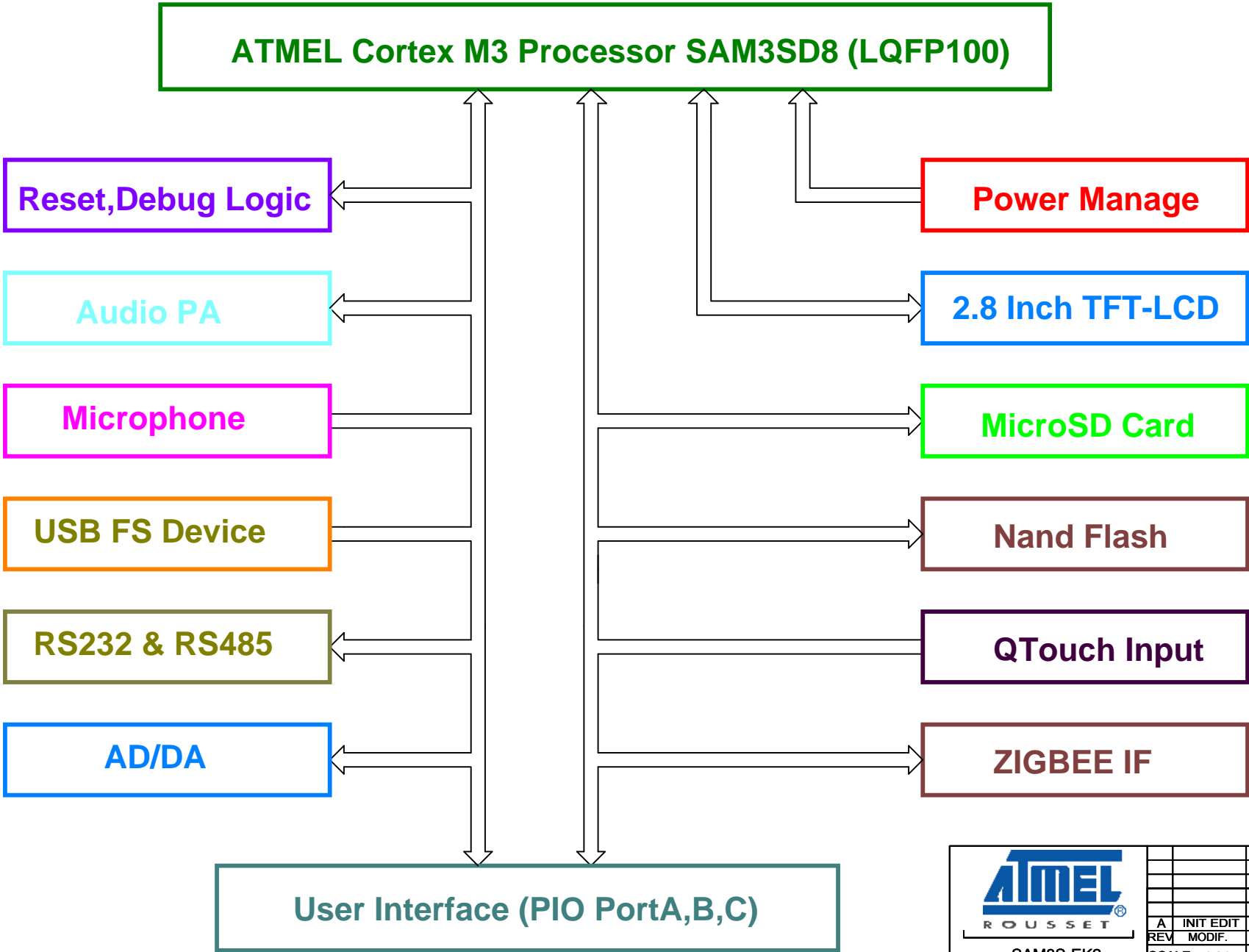



SAM3S-EK2 RevA Block Diagram





SAM3S-EK2

Block Diagram

A	INIT EDIT	JH	02-Mar-12	XXX	XX-XXX-XX
REV	MODIF.	DES.	DATE	VER.	DATE
SCALE	1/1			REV.	SHEET
				A	1/7

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REVISION HISTORY

REV	DATA	NOTE
A	2009.07	ORIGINAL RELEASED

TABLE OF CONTENTS

PAGE	DESCRIPTION
1	Block Diagram
2	Describe
3	Microcontroller
4	NAND Flash, RS232, RS485, MCI, JTAG
5	LCD, QTouch
6	Audio, AD/DA, Power
7	IO Expansion, USB, ZigBEE, LED, Bottom

PIO MUXING

PIOA	USAGE	PIOA	USAGE	PIOB	USAGE	PIOC	USAGE	PIOC	USAGE
PA0	TSLIDR_SL_SNS	PA16	TSC_IRQ/ZB_IRQ0	PB0	MIC INPUT	PC0	D0	PC16	NAND_ALE
PA1	TSLIDR_SL_SNSK	PA17	TSC_BUSY/ZB_IRQ1	PB1	ANA INPUT	PC1	D1	PC17	NAND_CLE
PA2	TSLIDR_SM_SNS	PA18	ZB_RSTN	PB2	ZB_NPCS2	PC2	D2	PC18	NAND_RDYBSY
PA3	TSLIDR_SM_SNSK	PA19	LED_BLUE	PB3	USER_PB1	PC3	D3	PC19	REGSEL_LCD
PA4	TSLIDR_SR_SNS	PA20	LED_GREEN	PB4	JTAG	PC4	D4	PC20	LED_RED(POWER)
PA5	TSLIDR_SR_SNSK	PA21	RXD1	PB5	JTAG	PC5	D5	PC21	USB_CNX
PA6	MCI_CD	PA22	TXD1	PB6	JTAG	PC6	D6	PC22	TVALID_SNS
PA7	CLK_32K	PA23	COM1EN	PB7	JTAG	PC7	D7	PC23	TVALID_SNSK
PA8	CLK_32K	PA24	RTS1	PB8	CLK_12M	PC8	WR_LCD	PC24	TUP_SNS
PA9	RX_UART0	PA25	CTS1	PB9	CLK_12M	PC9	NAND_OE	PC25	TUP_SNSK
PA10	TX_UART0	PA26	MCI	PB10	USB_DDM	PC10	NAND_WE	PC26	TDWN_SNS
PA11	TSC_CS	PA27	MCI	PB11	USB_DDP	PC11	RD_LCD	PC27	TDWN_SNSK
PA12	MISO	PA28	MCI	PB12	ERASE	PC12	USER_PB2	PC28	TLEFT_SNS
PA13	MOSI	PA29	MCI	PB13	AUDIO OUT R	PC13	EN_LCD	PC29	TLEFT_SNSK
PA14	SPCK	PA30	MCI	PB14	AUDIO OUT L	PC14	NAND_NCS0	PC30	TRIGHT_SNS
PA15	ZB_SLPTR	PA31	MCI			PC15	NSC1_LCD	PC31	TRIGHT_SNSK

EXPLAIN OF SCHEMATICS

- (1) Resistance Unit: "K" is "KΩ", "R" is "Ω"
- (2) "DNP" means the component is not populated by default

TEST POINT

PAGE	REFERENCE	FUNCTION
3	TP1, TP2, TP3, TP4	GND
4	TP5 TP6	UART TXD UART RXD
5	TP7 TP8, TP9	LCD BK driver anode Aux ADC input for TSC
6	TP12	Optional audio PA input

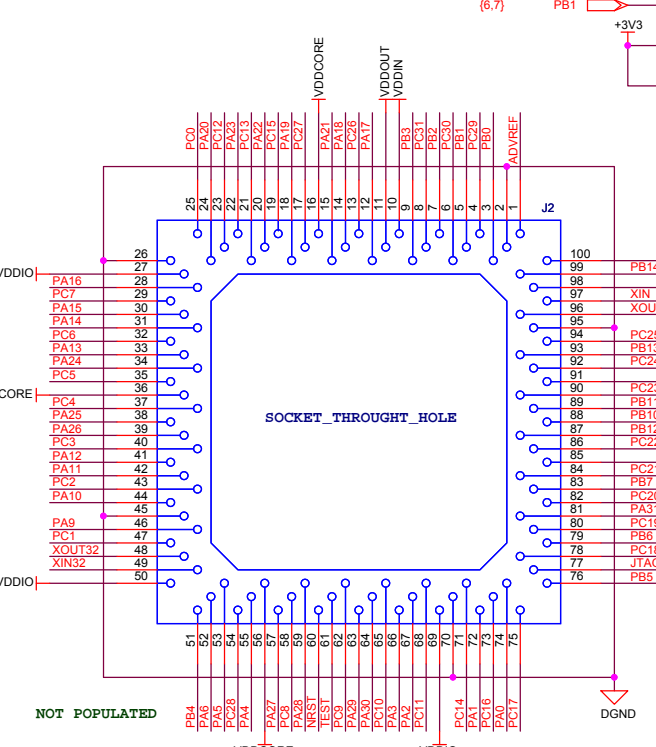
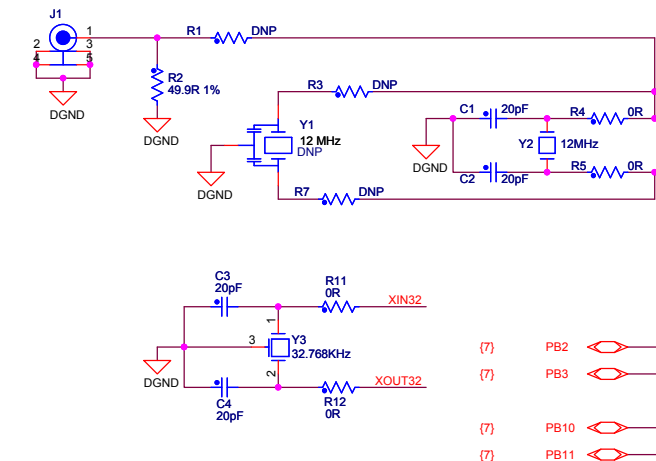
JUMPER and SOLDERDROP

PAGE	REFERENCE	DEFAULT	FUNCTION
3	JP1 JP2 JP3 JP4 JP5, JP6, JP7, JP8	OPEN 1-2 OPEN OPEN CLOSE	Close to select JTAG boundary scan Analog reference voltage selection from 3.3V and 2.5V Close to reinitialize the Flash content and some of its NVM bits Close for manufacturing test or fast programming mode Access for current measurement on each power rail
4	JP9 JP11 JP10, JP12	CLOSE CLOSE OPEN	Access to reconfigure NCS0 Differential impedance matching for RS485 cable Maintain differential for RS485 cable
5	JP13	CLOSE	Access to reconfigure NCS1
6	JP14, JP15 JP17, JP19 JP16, JP21 JP18 JP20 SD1 SD2	OPEN OPEN OPEN 1-2 OPEN OPEN CLOSE	Sync close to degrade gain stage on microphone input Close to mux RIN/LIN into MONO-IN path within audio PA Close for impedance matching on AD/DA BNC port ADC input selection between BNC port and potentiometer Close to fix in mono speaker mode, no matter stereo plug state DAC path isolation on sharing channel Lead PB14 as AUDIO_OUTL channel
7	JP22, JP23, JP24 JP25 JP26 JP27	1-2 CLOSE CLOSE CLOSE	DC voltage selection between 3.3V and 5V on PIO expansion ports Access to reconfigure NCS2 Access to reconfigure NCS3 Power consumption measure for ZigBEE module

DEFAULT NO POPULATE PARTS

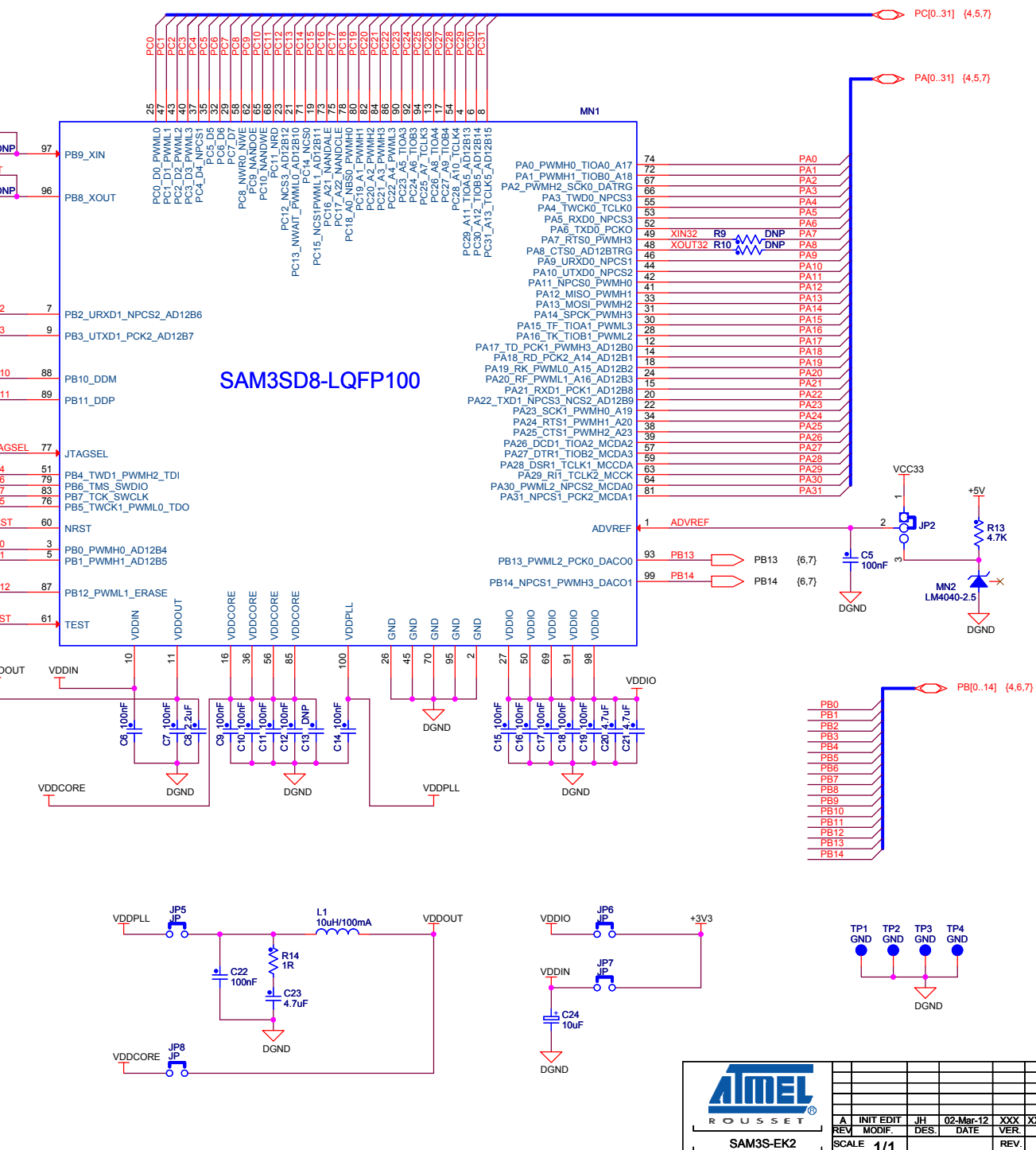
PAGE	REFERENCE	FUNCTION
3	J1, R1 Y1, R3, R7 R6, R8 R9, R10	External clock resource input Backup 12MHz crystal Isolation on 12MHz clock source and GPIO expansion Isolation on 32KHz clock source and GPIO expansion
4	R22 R23 R24, R30	Optional write protection on NAND flash Optional pull up for open drain output on equivalent device Differential impedance matching for RS485 cable
5	D1 R61, R62, RA2, RA3	Optional ESD protection for LCD touch panel Optional databus termination for LCD controller


NOT POPULATED



NOT POPULATED

SAM3SD8-LQFP100





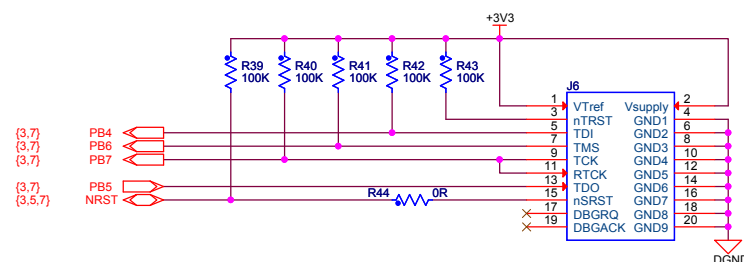
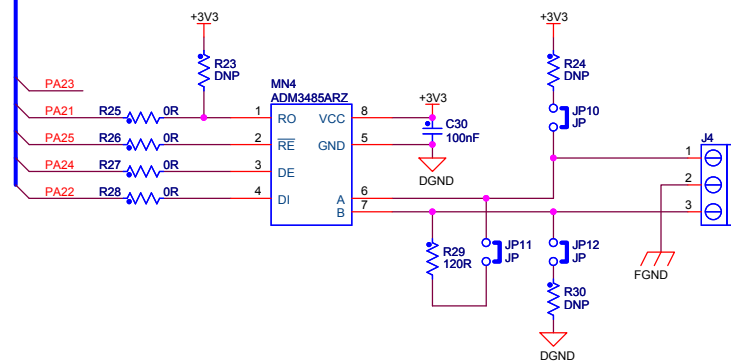
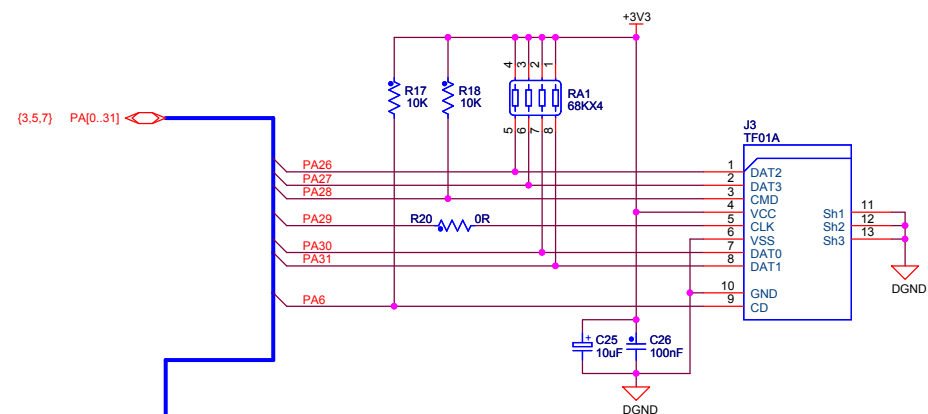
ROUSSET

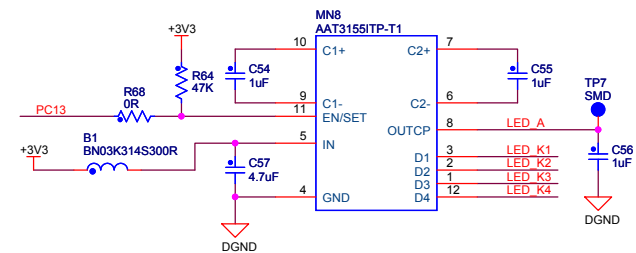
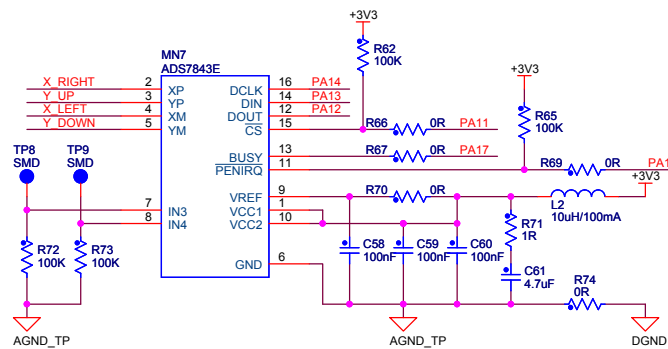
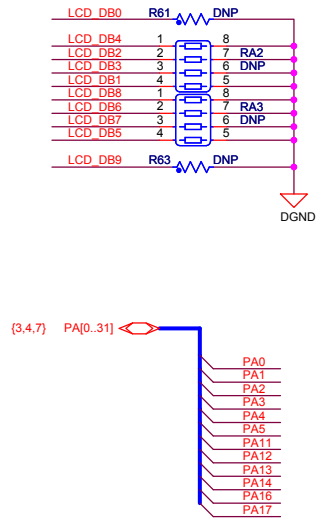
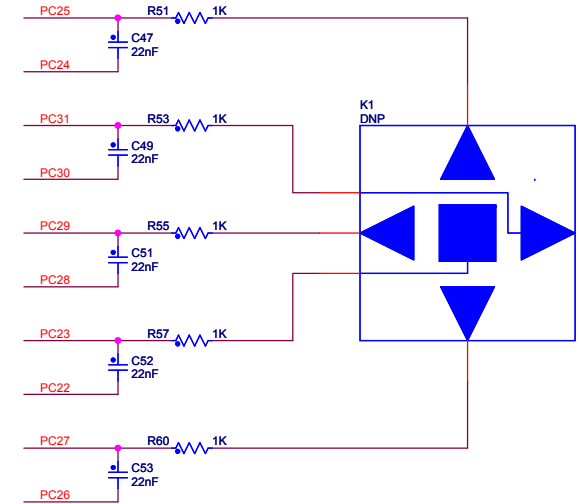
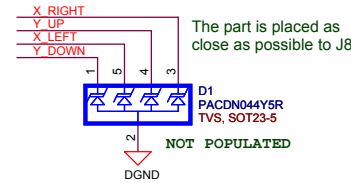
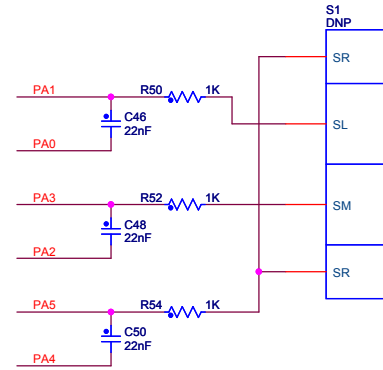
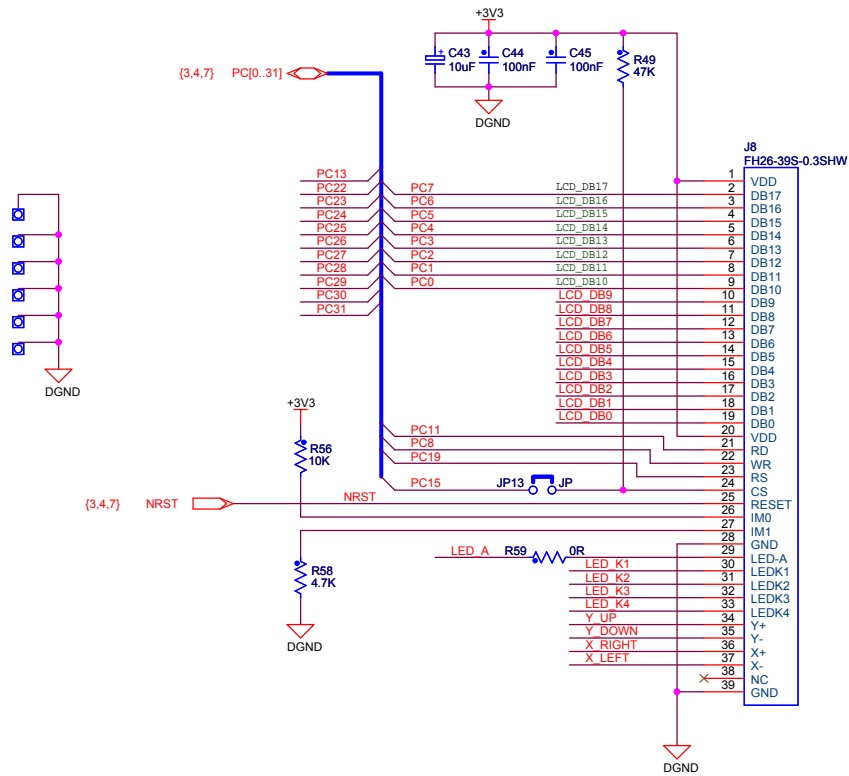
SAM3S-EK2

Microcontroller

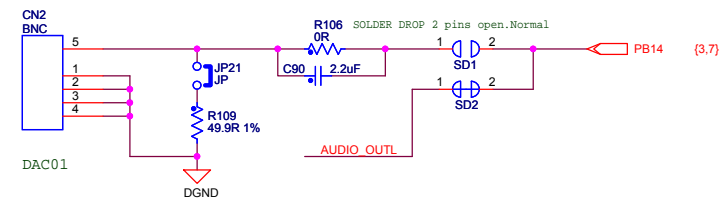
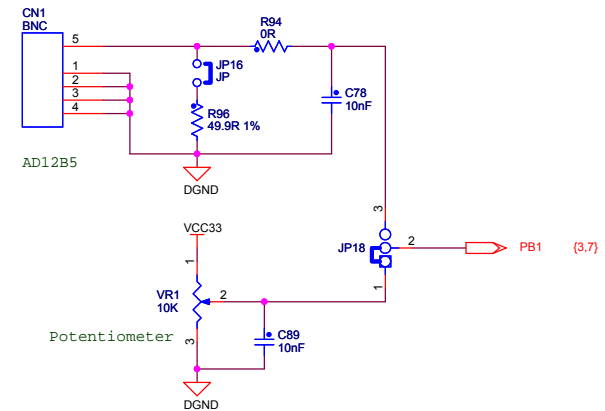
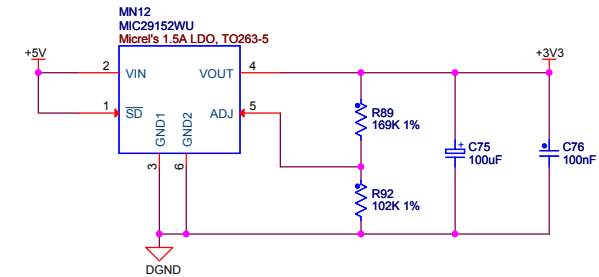
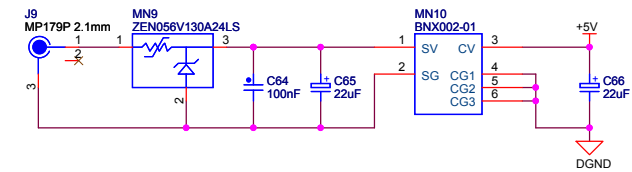
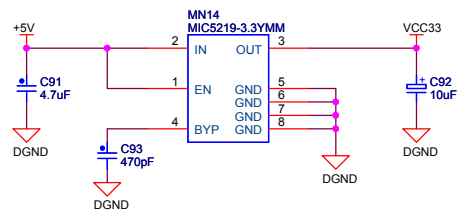
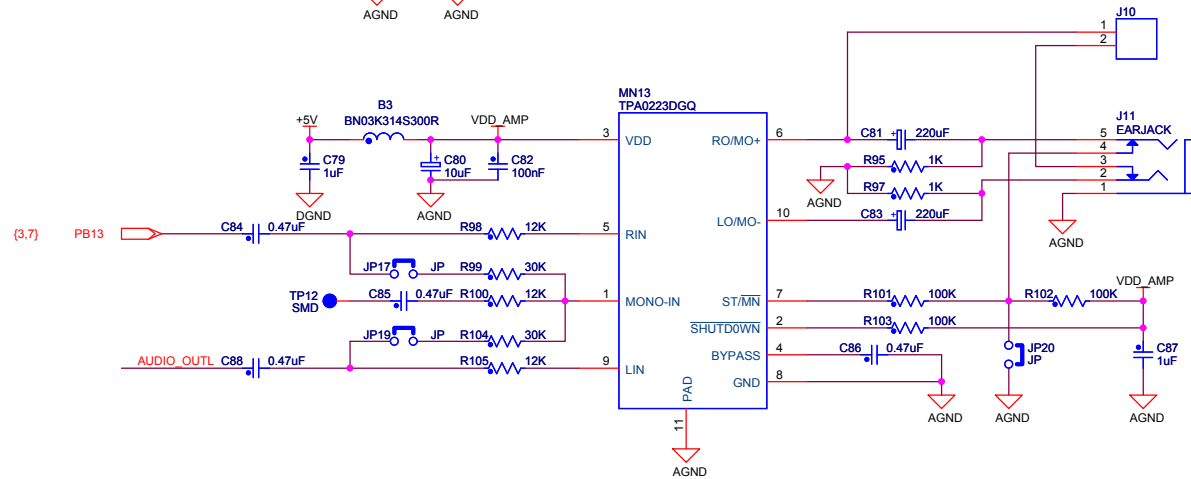
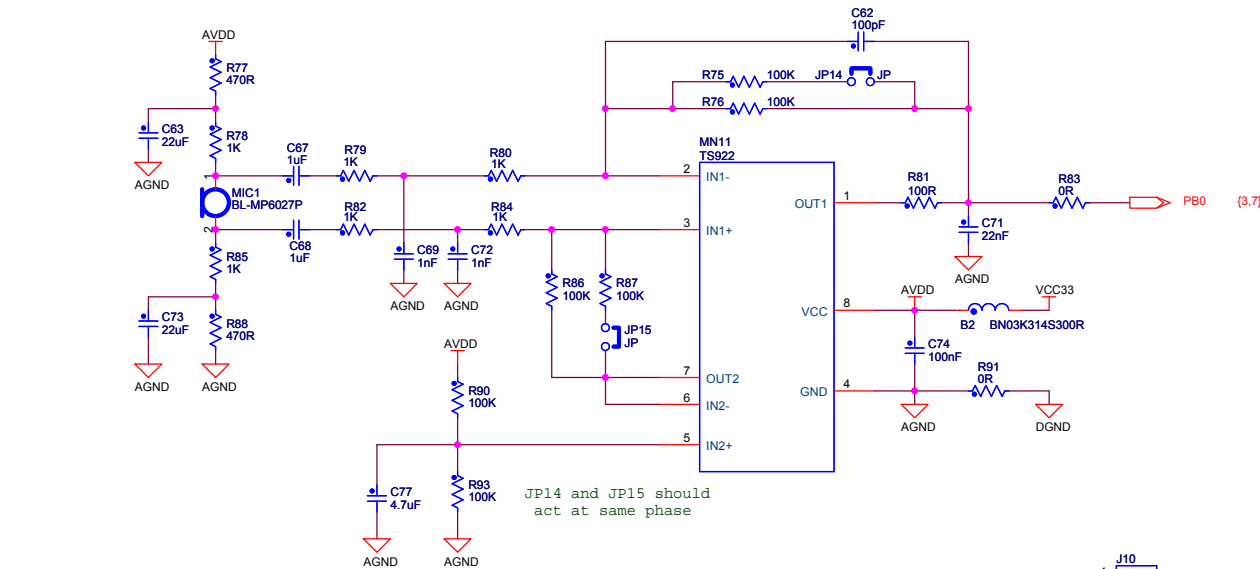
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SCALE	1/1					REV.	SHEET
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SAM3S-EK2		TFT-LCD & QTouch		A		5/				



		A		INIT	EDIT	JH	DES.	02-Mar-12	XXX	XX-XXX-XX
		REV	MODIF.	SCALE	1/1	DATE	VER.	DATE	REV.	SHEET
SAM3S-EK2		Audio & Power Supply		A		6/				

