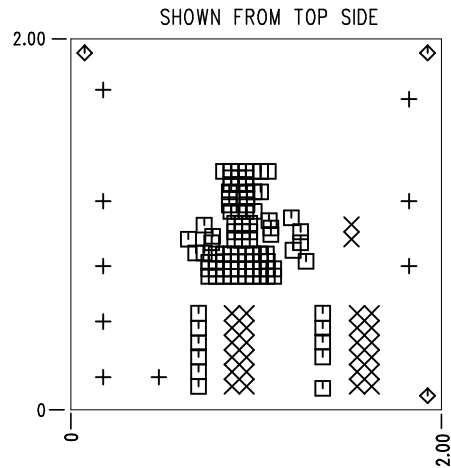
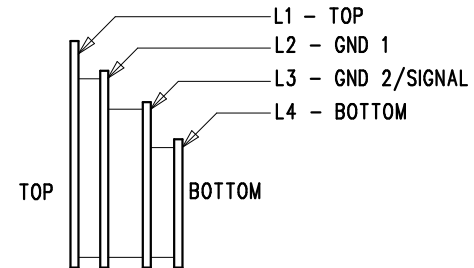


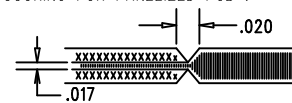
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
	2	PRODUCTION	EDWIN L.	10-29-13

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB :



SIZE	QTY	SYM	PLATED	TOL
0.01	96	□	YES	+/-0.003
0.035	26	×	YES	+/-0.003
0.07	3	◇	NO	+/-0.003
0.094	9	+	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED		APPROVALS			1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1800 WWW.LINEAR.COM LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT		
		APP ENG.	EDWIN L.	40V/2.1A ADJUSTABLE LDO WITH MONITORING AND CABLE DROP COMPENSATION	
				SIZE	IC NO. LT3086EFE
				N/A	DEMO CIRCUIT 1729A
				REV	2
		SCALE = NONE	FILENAME: DC1729A-2.PCB	SHT 1 OF 1	