

## Mini57 Series CMSIS BSP Guide

Directory Introduction for 32-bit NuMicro® Family

### Directory Information

Please extract the “Mini57 Series BSP\_CMSIS\_V3.02.000.zip” file firstly, and then put the “Mini57 Series BSP\_CMSIS\_V3.02.000” folder into the working folder (e.g. .\Nuvoton\BSP Library).

This BSP folder contents:

Document\	Device driver reference manual and reversion history.
Library\	Device driver header and source files.
SampleCode\	Device driver sample code.

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For additional information or questions, please contact: Nuvoton Technology Corporation.

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## 1 .\Document\

<b>CMSIS.html</b>	<p>Introduction of CMSIS version 4.5.0. CMSIS components included CMSIS-CORE, CMSIS-Driver, CMSIS-DSP, etc.</p> <ul style="list-style-type: none"> <li>● CMSIS-CORE: API for the Cortex-M0 processor core and peripherals.</li> <li>● CMSIS-Driver: Defines generic peripheral driver interfaces for middleware making it reusable across supported devices.</li> <li>● CMSIS-DSP: DSP Library Collection with over 60 functions for various data types: fix-point (fractional q7, q15, q31) and single precision floating-point (32-bit).</li> </ul>
<b>NuMicro Mini57 Series CMSIS BSP Revision History.pdf</b>	The revision history of Mini57 BSP.
<b>NuMicro Mini57 Series Driver Reference Guide.chm</b>	The usage of drivers in Mini57 BSP.

## 2 .\Library\

<b>CMSIS\</b>	Cortex® Microcontroller Software Interface Standard (CMSIS) V4.5.0 definitions by ARM® Corp.
<b>Device\</b>	CMSIS compliant device header file.
<b>StdDriver\</b>	All peripheral driver header and source files.

### 3 .\Sample Code\

<b>Hard_Fault_Sample\</b>	<p>Show hard fault information when hard fault happened.</p> <p>The hard fault handler shows some information included program counter, which is the address where the processor was executing when a hard fault occurred. The listing file (or map file) can show what function and instruction that was.</p> <p>It also shows the Link Register (LR), which contains the return address of the last function call. It can show the status where CPU comes from to get to this point.</p>
<b>Semihost\</b>	<p>Show how to print and get character through IDE console window.</p>
<b>RegBased\</b>	<p>The sample codes which access control registers directly.</p>
<b>StdDriver\</b>	<p>Demonstrate the usage of Mini57 series MCU peripheral driver APIs.</p>
<b>Template\</b>	<p>A project template for Mini57 series MCU.</p>

## 4 .\SampleCode\RegBased

<b>ACMP</b>	Demonstrate analog comparator (ACMP) comparison by comparing ACMP0_P0 input and VBG voltage and show the result on UART console.
<b>BPWM_DeadZone</b>	Demonstrate the BPWM dead-zone feature.
<b>BPWM_DoubleBuffer</b>	Demonstrate the BPWM double buffer feature.
<b>EADC_Ind2SH</b>	Convert ADC0 channel 0 and ADC1 channel 0 in Independent 2SH mode and print conversion results.
<b>EADC_IndSimple</b>	Convert ADC0 channel 0 and ADC1 channel 0 in Independent Simple mode and print conversion results.
<b>EADC_IndSimple_BandGap</b>	Convert ADC0 channel 6 (Band-Gap) in Independent Simple mode and print conversion results.
<b>EADC_IndSimple_TempSensor</b>	Convert ADC1 channel 6 (Temperature Sensor) in Independent Simple mode and print conversion results.
<b>EADC_PWMTrigger</b>	Configure PWM0 to trigger ADC0 channel 0 periodically and print conversion results.
<b>EADC_SimSeq3R</b>	Convert ADC0 channel 0, channel 6, and ADC1 channel 0 in EADC Simultaneous Sequential 3R mode and print conversion results.
<b>EADC_SimSeq4R</b>	Convert ADC0 channel 0, channel 6, ADC1 channel 0, and channel 3 in EADC Simultaneous Sequential 4R mode and print conversion results.
<b>EADC_SimSimple</b>	Convert ADC0 channel 0 and ADC1 channel 0 in Simultaneous Simple mode and print conversion results.
<b>EADC_TimerTrigger</b>	Configure Timer0 to trigger ADC0 channel 0 periodically and print conversion results.

<b>EADC_Valid_Overrun</b>	Demonstrate how to check OVERRUN status, VALID status, and read data by correct order.
<b>EADC_WCompare</b>	Demonstrate EADC conversion and window comparison function by monitoring the conversion result of ADC0 channel 0.
<b>ECAP_Capture</b>	Configure ECAP channel 0 to capture input square wave and print capture results. The input square wave is generated by Timer0 and GPIO output pin.
<b>ECAP_CmpMatch</b>	Demonstrate ECAP capture and compare match function by monitoring the capture result of ECAP channel 0.
<b>EPWM_DeadZone</b>	Demonstrate the EPWM dead-zone feature.
<b>EPWM_DoubleBuffer</b>	Demonstrate the EPWM double buffer feature.
<b>FMC_CRC32</b>	Show FMC CRC32 calculation capability.
<b>FMC_IAP</b>	Include LDROM image (fmc_ld_iap) and APROM image (fmc_ap_main), which shows how to branch between APROM and LDROM. To run this sample code, the boot mode must be "Boot from APROM with IAP".
<b>FMC_RW</b>	Show FMC read Flash IDs, erase, read, and write functions.
<b>GPIO_IOTest</b>	Use GPIO driver to control the GPIO pin direction and the high/low state, and show how to use GPIO interrupts.
<b>GPIO_PowerDown</b>	Demonstrate how to wake up system form Power-down mode by GPIO interrupt.
<b>HDIV</b>	Demonstrate how to divide two signed integers by HDIV engine.
<b>PGA_PGAO</b>	Demonstrate how to amplify input signals with different gain levels and output to PGA_O output pin.
<b>SYS_CLKO</b>	Demonstrate how to output different clocks one after another to the same CLKO (PA0) pin.

<b>SYS_Control</b>	Demonstrate some system manager controller functions, including reading PDID, getting reset source, system write-protection, power-down wake up by Watchdog timer, and CPU reset.
<b>TIMER_Delay</b>	Demonstrate the usage of <code>TIMER_Delay()</code> API to generate a 1 second delay.
<b>TIMER_EventCounter</b>	Use pin PB.3 to demonstrate timer event counter function.
<b>TIMER_FreeCountingMode</b>	Use the ACMP0 positive input pin to demonstrate timer free counting mode function, and display the measured input frequency to console.
<b>TIMER_Periodic</b>	Use the timer periodic mode to generate timer interrupt every 1 second.
<b>TIMER_ToggleOut</b>	Demonstrate the timer 0 toggle out function on the pin PB.3.
<b>TIMER_Wakeup</b>	Use the timer to wake up system from Power-down mode periodically.
<b>USCI_I2C_EEPROM</b>	Show how to use USCI_I2C interface to access EEPROM.
<b>USCI_I2C_Master</b>	Show how to set USCI_I2C in Master mode and send data to Slave device. This sample code needs to work with USCI_I2C_Slave.
<b>USCI_I2C_Slave</b>	Show how to set USCI_I2C in Slave mode and receive the data from Master. This sample code needs to work with USCI_I2C_Master.
<b>USCI_SPI_Loopback</b>	Implement USCI_SPI1 Master loop back transfer. This sample code needs to connect USCI_SPI1_MISO pin and USCI_SPI1_MOSI pin together. It will compare the received data with transmitted data.
<b>USCI_SPI_MasterMode</b>	Configure USCI_SPI1 as Master mode and demonstrate how to communicate with an off-chip SPI Slave device. This sample code needs to work with USCI_SPI_SlaveMode.

<b>USCI_SPI_SlaveMode</b>	Configure USCI_SPI1 as Slave mode and demonstrate how to communicate with an off-chip SPI Master device. This sample code needs to work with USCI_SPI_MasterMode.
<b>USCI_UART_TxRxFunction</b>	Transmit and receive data from PC terminal through an RS232 interface.
<b>WDT_TimeoutWakeupAndReset</b>	Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired.



## 5 .\SampleCode\StdDriver

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