EVALSPEAr600

Evaluation board for SPEAr600 embedded MPU

Data brief

Features

- SPEAr600 embedded MPU
- Up to 2 Gbits DDR2 333 MHz (std 128 MB)
- Up to 16 MB Serial Flash (std 8 MB)
- Up to 2 Gb NAND Flash (std 64 MB)
- 4 Kb Serial I^2C
- Two USB 2.0 full Host port channels
- One USB 2.0 HS Device
- 10/100/1000 Ethernet port
- Two serial ports (up to 115 Kbaud)
- Debug ports (JTAG + ETM9)
- 8 ADC channels (10 bit, 1 Msamples)
- 10 GPIOs
- LCD I/F up to 24 bits-per-pixel (bpp)
- Additional 112 GPIOs when the external FPGA is used

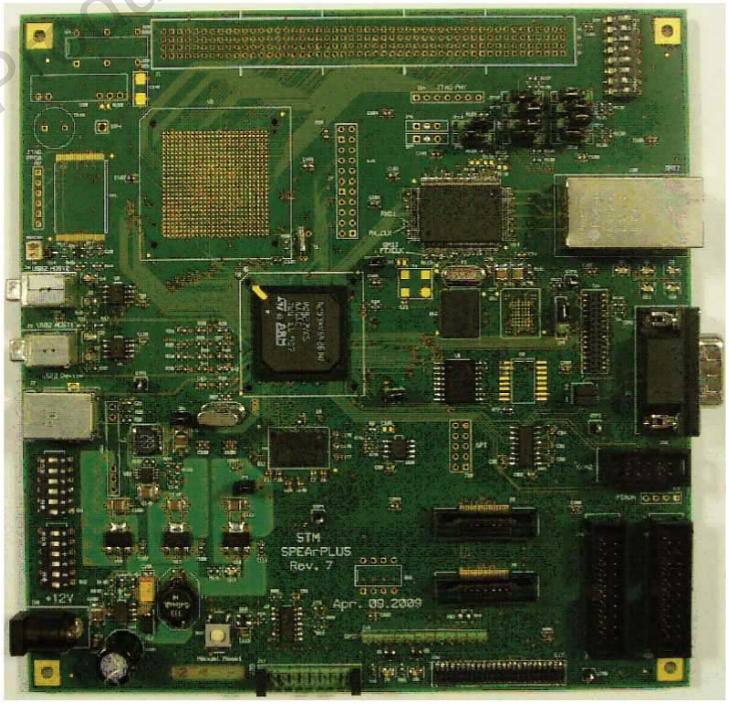
Description

The EVALSPEAr600 evaluation board allows you to quickly evaluate and debug software for the SPEAr600. It acts as a learning tool to rapidly get familiar with the SPEAr600 features and provides a starting point for the development of the final application board. It is equipped with most of the interfaces offered by the SPEAr600.

It is equipped with all the interfaces offered by the SPEAr600. A special version of the board populated with an FPGA also exists for developing customer-specific IPs.

Obsolete Product(s) - Obsolete Product(s)

Figure 1. EVALSPEAr600 evaluation board



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Revision history

Table 1. Document revision history

Date	Revision	Changes
20-Sep-2010	1	Initial release.

Obsolete Product(s) - Obsolete Product(s)

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