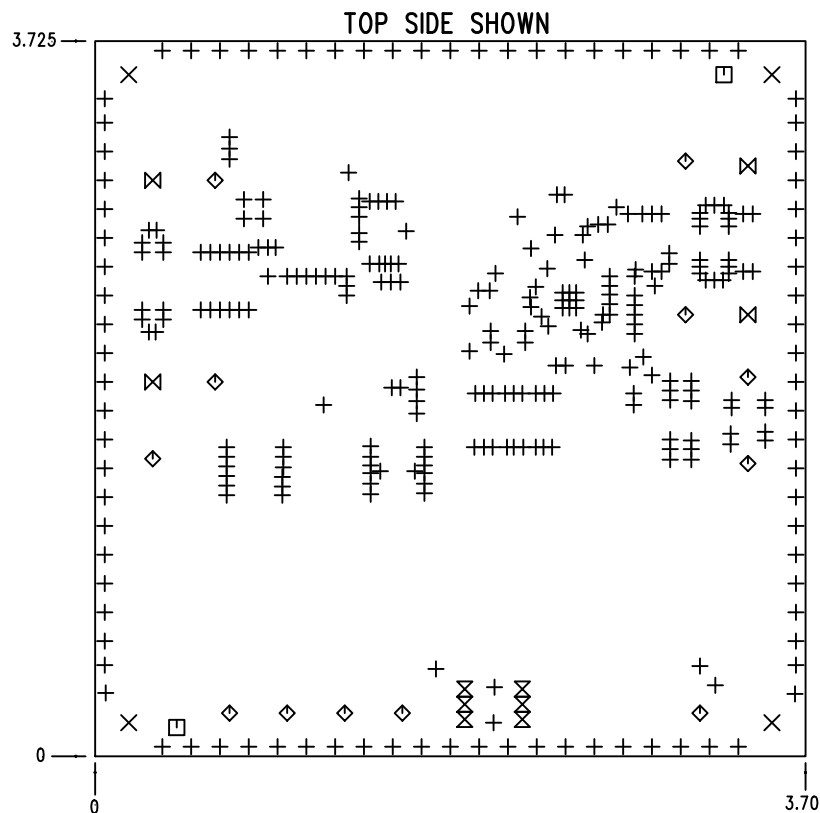
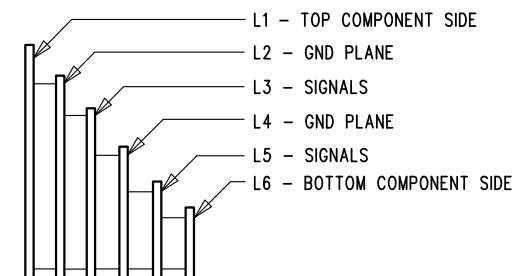


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	4	PRODUCTION	VICTOR K.	01-05-18



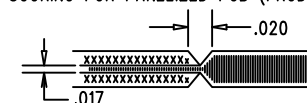
SIZE	QTY	SYM	PLATED	TOL
0.01	316	+	YES	+/-0.003
0.035	6	⊗	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.094	12	◇	YES	+/-0.003
0.187	4	×	NO	+/-0.003
0.207	4	⊗	YES	+/-0.003



LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		  www.linear.com FOR ADI CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT		
		APP ENG.	VICTOR K.	TITLE: FABRICATION DRAWING	
				DUAL OUTPUT SEPIC AND BUCK CONVERTER	
				SIZE	IC NO.
				N/A	LTC3892EUH-2
					DEMO CIRCUIT 2727A
				REV	4
		SCALE = NONE		FILENAME: DC2727A-4.PCB	
				SHT 1 OF 1	