



2SC6099

Bipolar Transistor

100V, 2A, Low $V_{CE(sat)}$, NPN Single TP/TP-FA

ON Semiconductor®

<http://onsemi.com>

Applications

- DC / DC converter, relay drivers, lamp drivers, motor drivers, inverter

Features

- Adoption of FBET, MBIT process
- Low collector-to-emitter saturation voltage
- High allowable power dissipation
- Large current capacity
- High-speed switching

Specifications

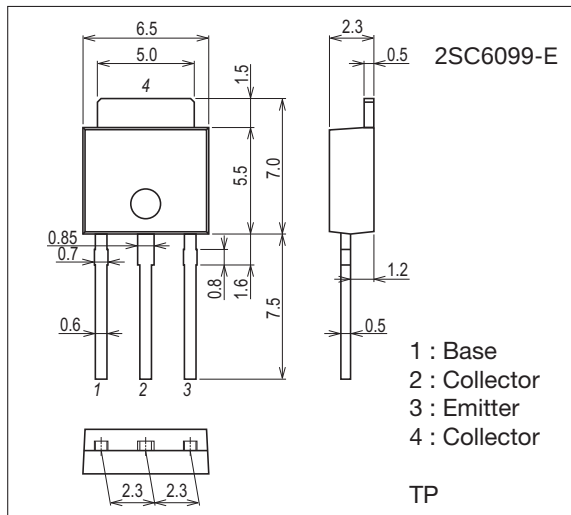
Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		120	V
Collector-to-Emitter Voltage	V_{CES}		120	V
Collector-to-Emitter Voltage	V_{CEO}		100	V
Emitter-to-Base Voltage	V_{EBO}		6.5	V
Collector Current	I_C		2	A
Collector Current (Pulse)	I_{CP}		3	A
Base Current	I_B		400	mA

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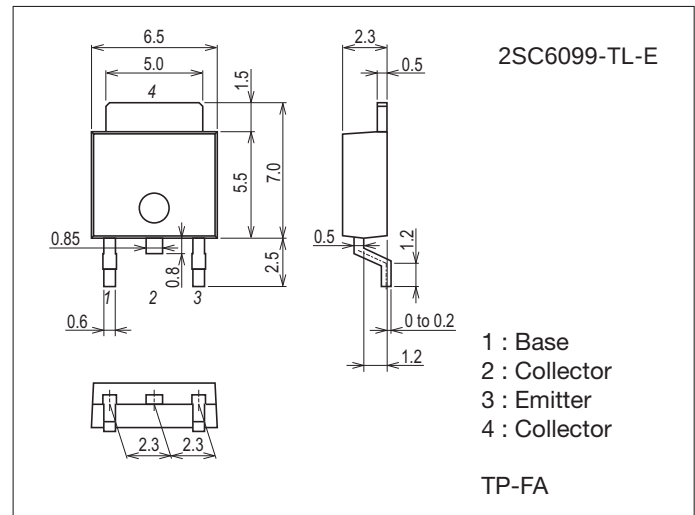
Package Dimensions unit : mm (typ)

7518-003



Package Dimensions unit : mm (typ)

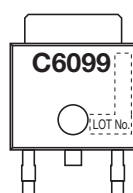
7003-003



Product & Package Information

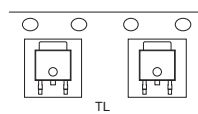
- Package : TP
- JEITA, JEDEC : SC-64, TO-251
- Minimum Packing Quantity : 500 pcs./bag

Marking (TP, TP-FA)

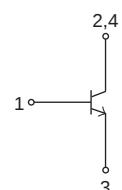


- Package : TP-FA
- JEITA, JEDEC : SC-63, TO-252
- Minimum Packing Quantity : 700 pcs./reel

Packing Type (TP-FA) : TL



Electrical Connection



2SC6099

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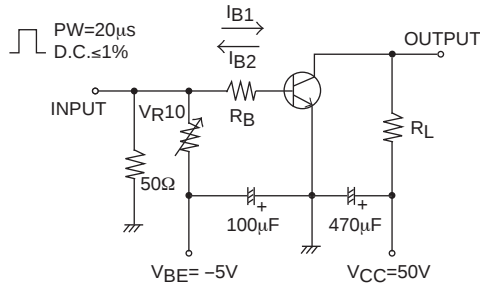
Parameter	Symbol	Conditions	Ratings	Unit
Collector Dissipation	P _C		0.8	W
		T _C =25°C	15	W
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Electrical Characteristics at T_a=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CBO}	V _{CB} =80V, I _E =0A			1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0A			1	μA
DC Current Gain	h _{FE}	V _{CE} =5V, I _C =100mA	300		600	
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =300mA		300		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, f=1MHz		13		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =1A, I _B =100mA		110	165	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =1A, I _B =100mA		0.9	1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C =10μA, I _E =0A	120			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CES}	I _C =100μA, R _{BE} =0Ω	120			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =1mA, R _{BE} =∞	100			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =10μA, I _C =0A	6.5			V
Turn-On Time	t _{on}	See specified Test Circuit.		40		ns
Storage Time	t _{stg}			1100		ns
Fall Time	t _f			40		ns

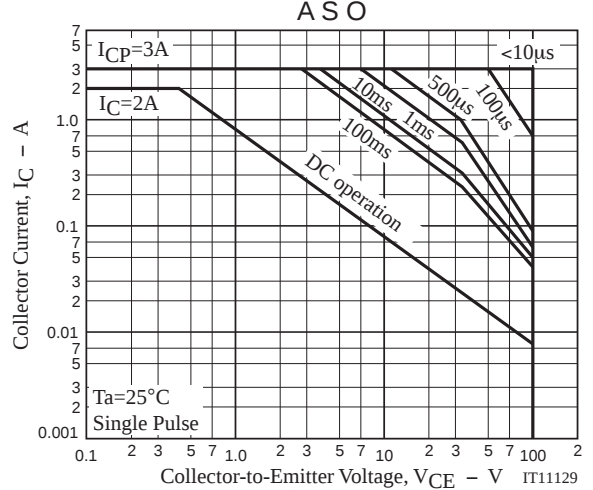
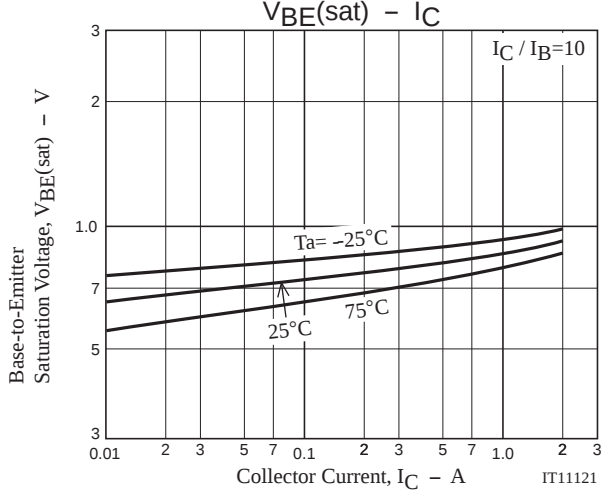
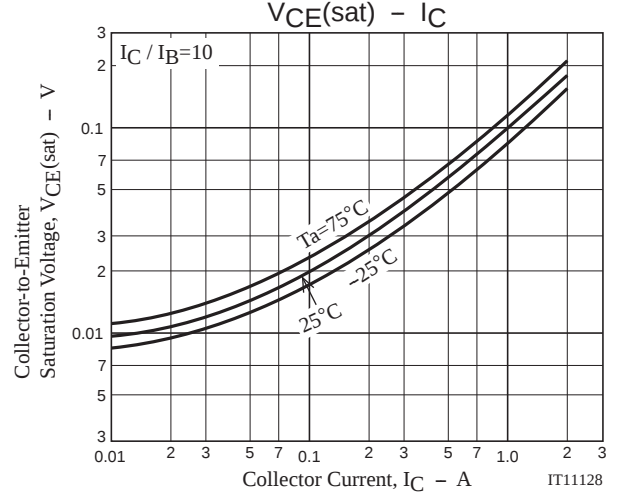
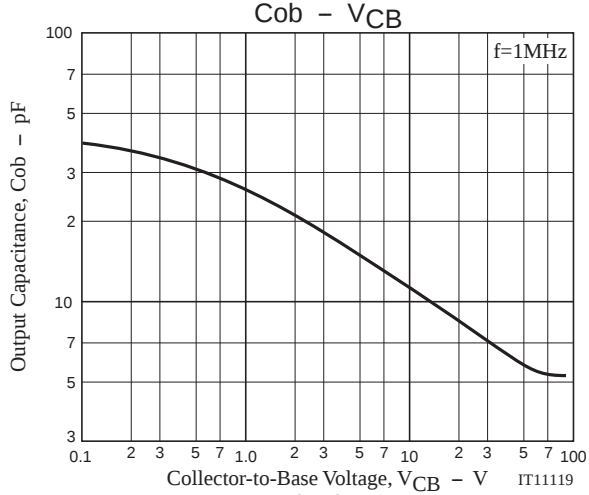
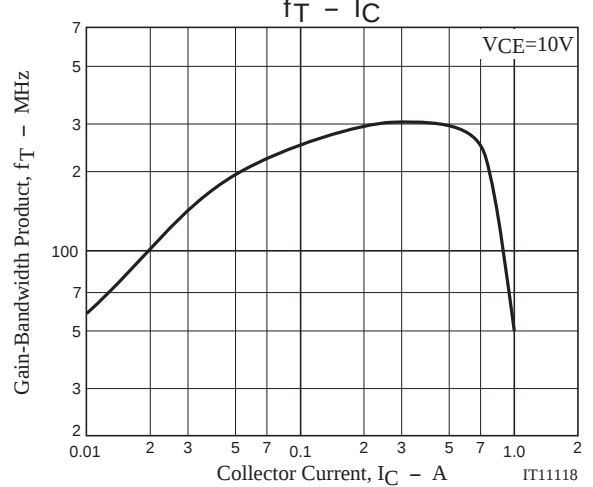
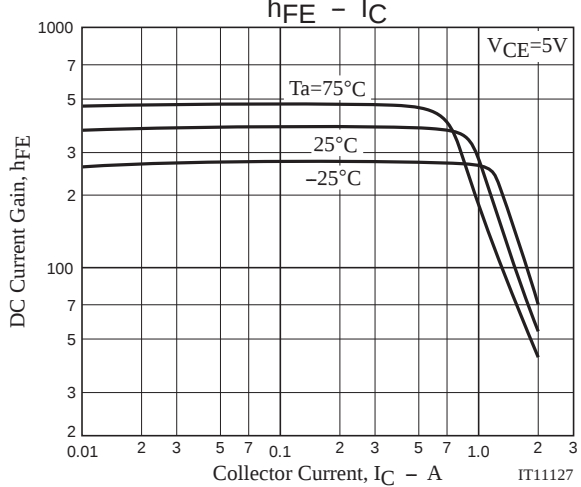
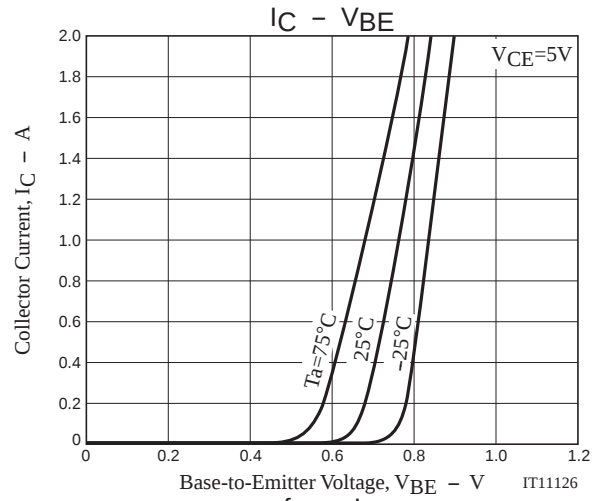
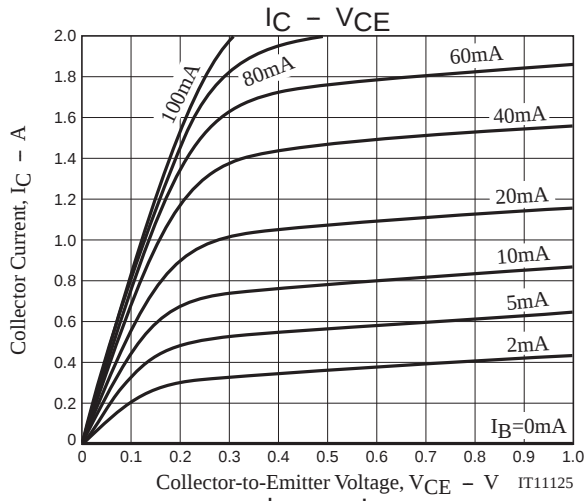
Switching Time Test Circuit

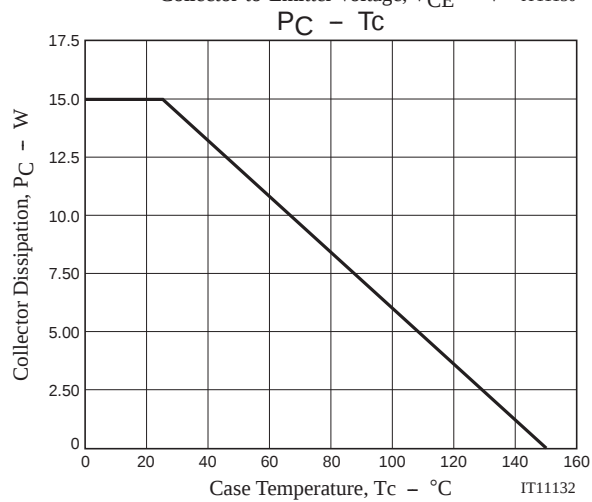
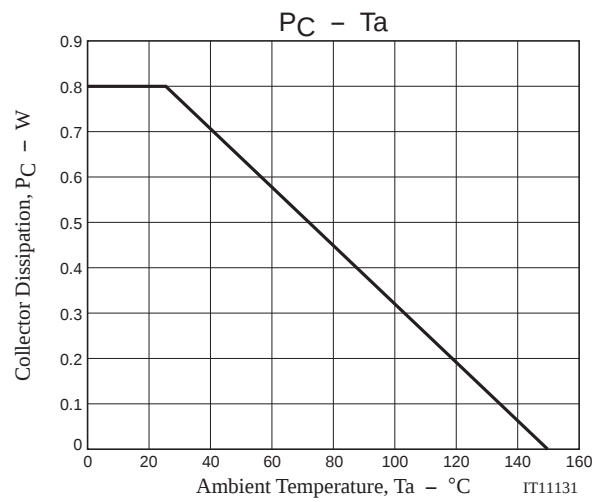
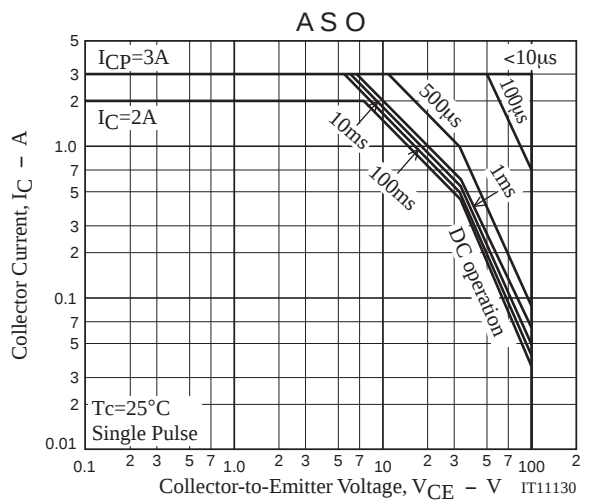


$$I_C = 10I_{B1} = -10I_{B2} = 0.5A$$

Ordering Information

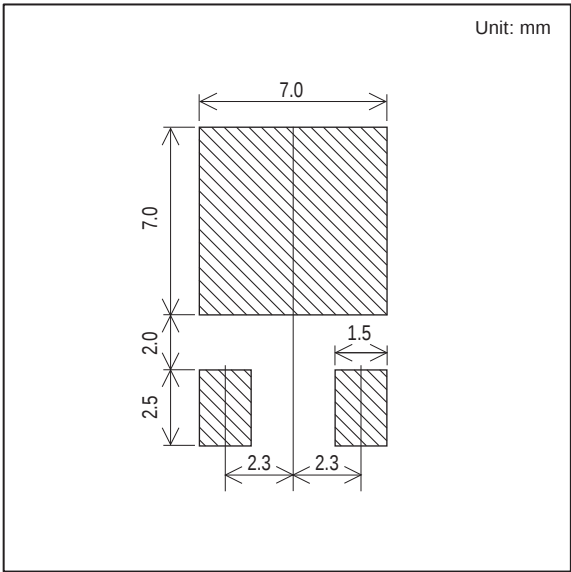
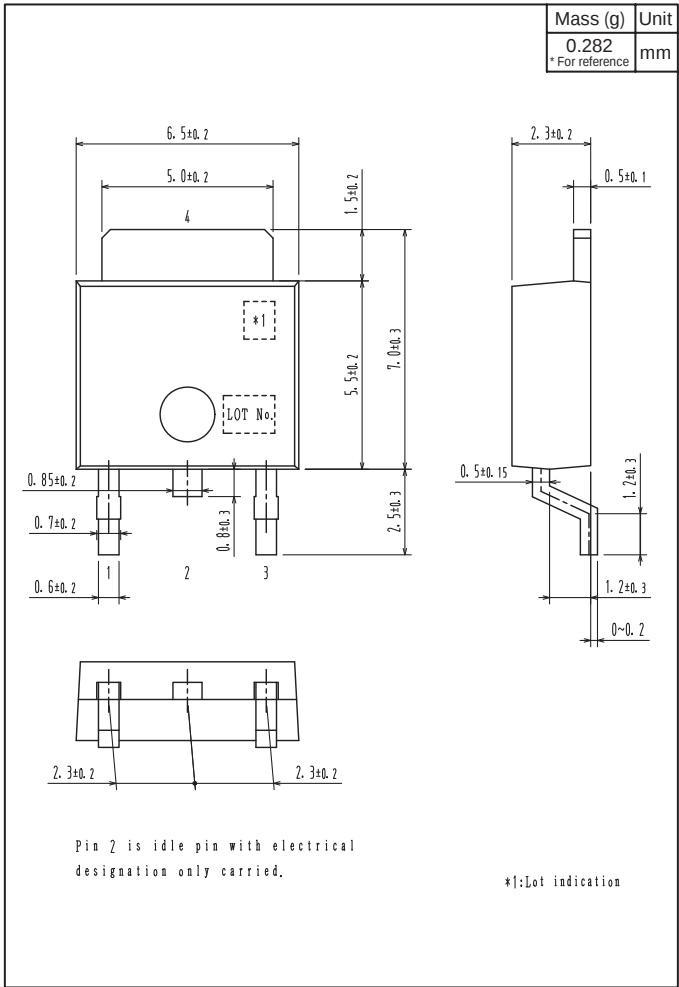
Device	Package	Shipping	memo
2SC6099-E	TP	500pcs./bag	Pb Free
2SC6099-TL-E	TP-FA	700pcs./reel	





Outline Drawing
2SC6099-TL-E

Land Pattern Example

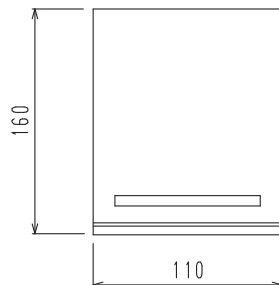


Bag Packing Specification

2SC6099-E

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			
	Bag	Inner box	Outer box	
TP	500	B-1	A-1	A-2
		10, 000	50, 000	30, 000
		Packing format (Dimensions:mm (external))		
		Inner box	Outer box	
		B-1	A-1	A-2
		445×225×55	470×250×300	470×250×190

2. Bag dimensions
(unit:mm)3. Bag label, Inner box label
(unit:mm)

Type No. →	(P) TYPE 0000000000	43
LOT No. →	(1) LOT 00	
Quantity →	(Q) QTY 0,000 (1) LEAD FREE *	
Origin →	(2) SPECIAL *20722005310C* ASSEMBLY:**** (DIFFUSION:****)	

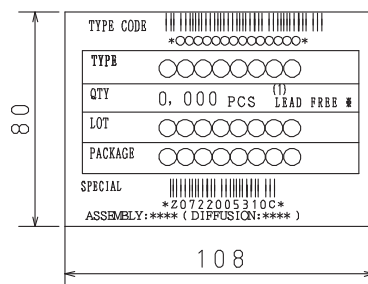
4. Outer box label
(unit:mm)

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.

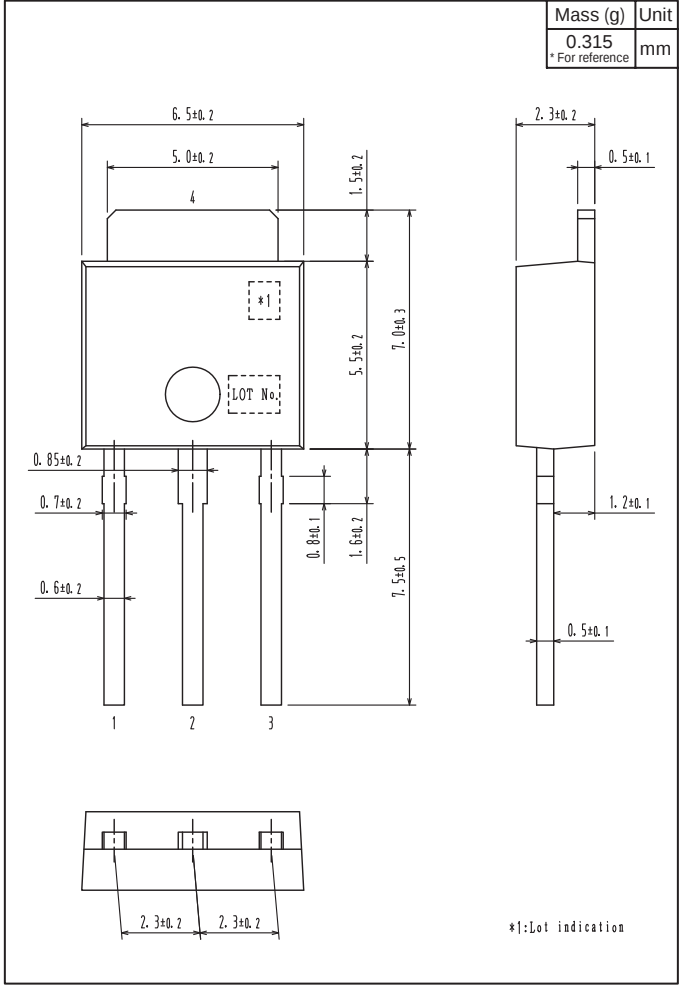
NOTE (1)

The LEAD FREE * description shows that the
surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3



Outline Drawing
2SC6099-E



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