

TUSB9261 Mass Storage Device Errata

Problem:	<i>Device may enumerate as HS instead of SS behind a hub after a hot plug event on upstream port of the HUB while VBUS stays active (PWRSWTCH = OFF). Because it's a self powered HUB and power switch is off, VBUS would stay on even when the upstream port is unplugged.</i>
Work Around:	Disconnect the TUSB9261 USB cable attached to the downstream port of the HUB, then re-attach the cable to get a SuperSpeed connection.
Problem:	<i>The TUSB9261 does not transmit a U2_Exit for the required 2 ms before timing out, it only transmits for 0.5 ms. This is an intermittent issue that will occur only when the hub/host downstream port is performing far-end RX detect while in U2.</i>
Work Around:	Problem is infrequent, but using TUSB9261 FW with U1/U2 disabled corrects the issue.
Problem:	<i>If the 3.3-V supply of the TUSB9261 ramps before the 1.1-V supply, the device I/O is in an unknown state before the core logic is active. This allows the potential for the GRSTz I/O cell to incorrectly configure as an output and drive the GRSTz signal high until the core logic is powered on and correctly configures the cell. The behavior can shorten or eliminate the needed reset pulse if a passive reset circuit is used.</i>
Work Around:	Ramp the 1.1-V power supply before or at the same time as the 3.3-V power supply if a passive reset circuit is used. Another option is to use an active reset source such as a power good signal from the 1.1-V voltage regulator or a voltage supervisory circuit. The reset pulse must be at least 2 ms long, but shorter than 100 ms.
Problem:	<i>TUSB9261 may immediately issue LGO_U* after ERDY when used behind a SS HUB.</i>
Work Around:	None needed, SS HUB rejects LGO_U*.
Problem:	<i>U1/U2 is not supported by the TUSB9261 and must be disabled by firmware.</i>
Work Around:	Use TUSB926x Default Firmware, U1/U2 Disabled available at http://www.ti.com/product/tusb9261 .

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