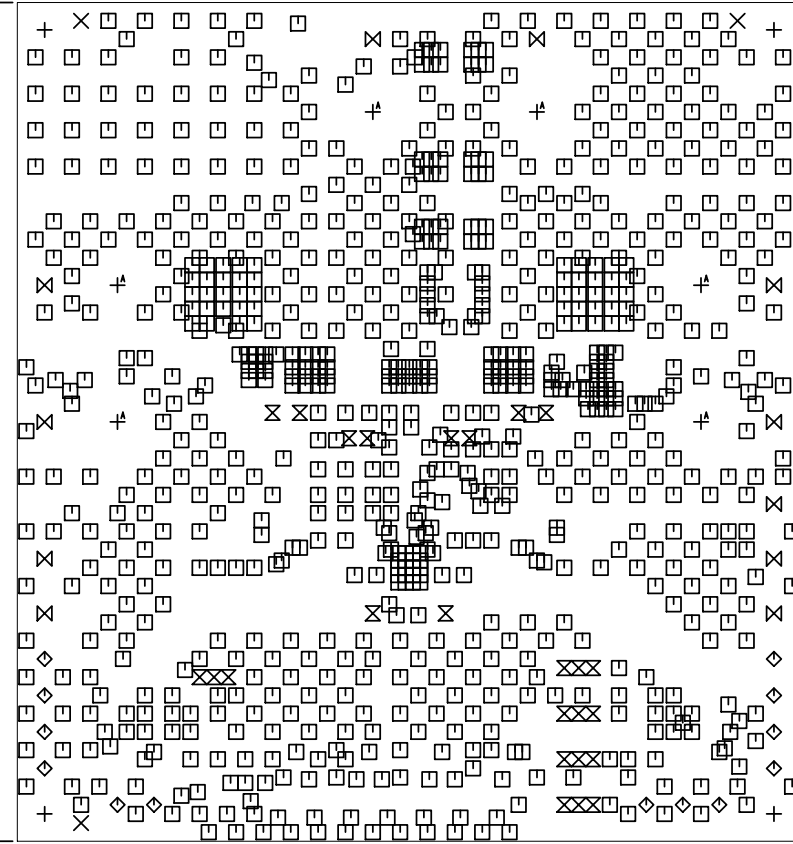


SHOWN FROM TOP SIDE

4.60"

0.00"



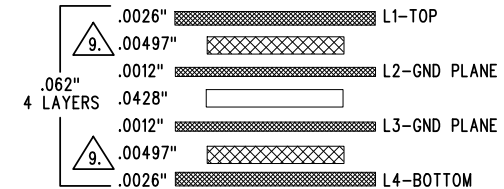
DRILL DRAWING LAYER
ANALOG DEVICES
DC2866A-2 LTC4421UHE DUAL CHANNEL PRIORITIZED POWERPATH CONTROLLER

SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003"
0.07	3	X	NO	+/-0.003"
0.012	852	□	YES	+/-0.003"
0.064	13	◇	YES	+/-0.003"
0.035	25	⊗	YES	+/-0.003"
0.094	11	⊗	YES	+/-0.003"
0.207	6	⊕ ^A	YES	+/-0.003"

REVISION HISTORY

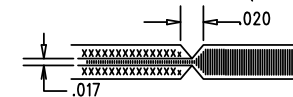
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	MANPREET S.	09-28-18

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS PCB DES. KIM T. APP ENG. MANPREET S.		ANALOG DEVICES www.analog.com POWER BY LINEAR www.linear.com 1630 MCCARTHY BLVD. MILPITAS, CA 95035 TEL: (408)432-1900	
TITLE: FABRICATION DRAWING DUAL CHANNEL PRIORITIZED POWERPATH CONTROLLER		SIZE N/A IC NO. LTC4421UHE DEMO CIRCUIT 2866A		REV 2	
SCALE = NONE		FILENAME: DC2866A-2.PCB		SHT 1 OF 1	