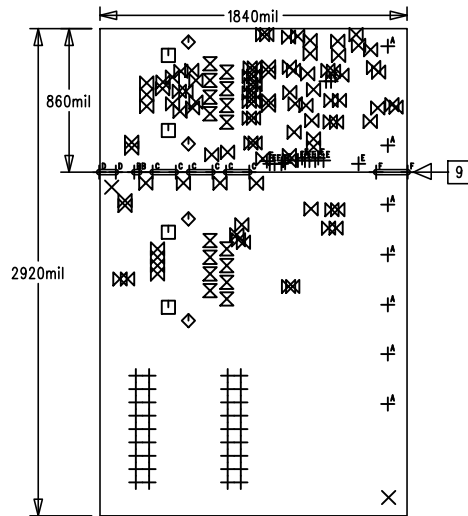


SHOWN FROM TOP SIDE



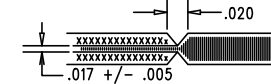
DRILL DRAWING LAYER
LINEAR TECHNOLOGY
DC2093A-1 LT4275XIDD LT4321UUF DATE: 06-26-13
LTPoE++ POWERED DEVICE

SIZE	QTY	SYM	PLATED	TOL
35	36	+	YES	+/-0.003
70	2	X	NO	+/-0.003
128	4	□	NO	+/-0.003
62	4	◇	YES	+/-0.003
36	16	⊗	YES	+/-0.003
10	98	⊗	YES	+/-0.003
94	7	⊕ ^A	YES	+/-0.003
31 x 60	1	⊕ ^B	NO	+/-0.003
31 x 170	3	⊕ ^C	NO	+/-0.003
31 x 130	1	⊕ ^D	NO	+/-0.003
6	15	⊕ ^E	YES	+/-0.003
31 x 220	1	⊕ ^F	NO	+/-0.003

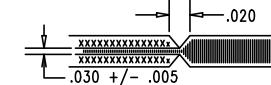
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	KAUNG H.	6-26-13

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 1 OZ. CU FOR ALL LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE, PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MINIMUM EXCEPT SLOT HOLES INDICATED BY THE ARROW #9
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING EXCEPT SLOT HOLES INDICATED BY THE ARROW #9
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
-GOLD IMMERSION BOTH SIDES.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



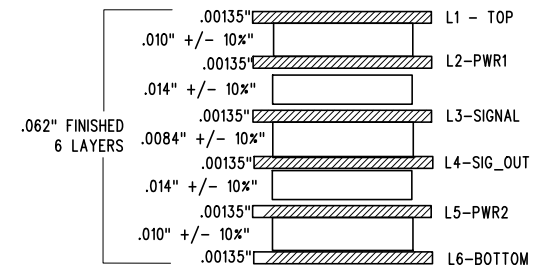
9. ADD A SCORE LINE AS INDICATED BY THE ARROW WITH THE PARAMETERS AS SHOWN BELOW:



SCORING ANGLE IS 60 DEGREE MEASURED FROM THE SURFACE OF THE BOARD.

10. CONTROLLED DIELECTRIC FOR ALL LAYERS

LAYER STRUCTURE



UNLESS OTHERWISE SPECIFIED		APPROVALS		<p>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</p>	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	KAUNG H.		
		APP ENG.	KAUNG H.	TITLE: FABRICATION DRAWING	
				LTPoE++ POWERED DEVICE	
		SIZE	IC NO.	LT4275XIDD, LT4321UUF	REV
		N/A		DEMO CIRCUIT 2093A	2
SCALE = NONE		FILENAME: DC2093A-2.PCB		SHT 1 OF 1	