Quad TSSOP Op Amp Evaluation Board

Part Number CLC 730131
February 2002

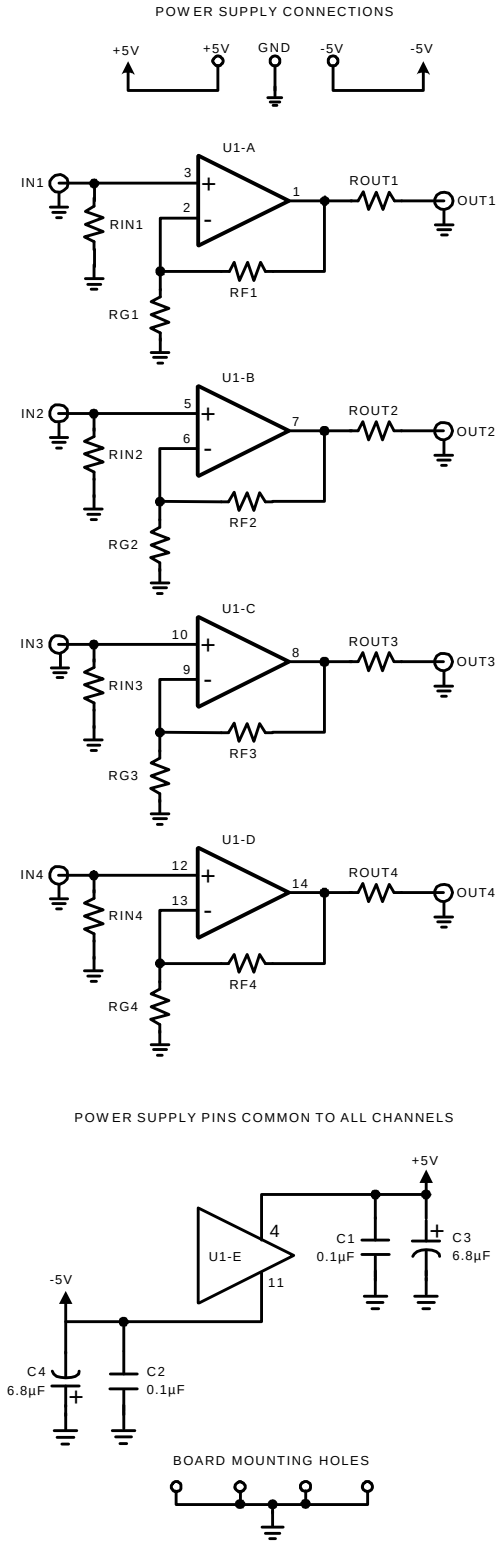


Figure 1: Non-Inverting Op Amp Configuration
(Default Board Configuration)

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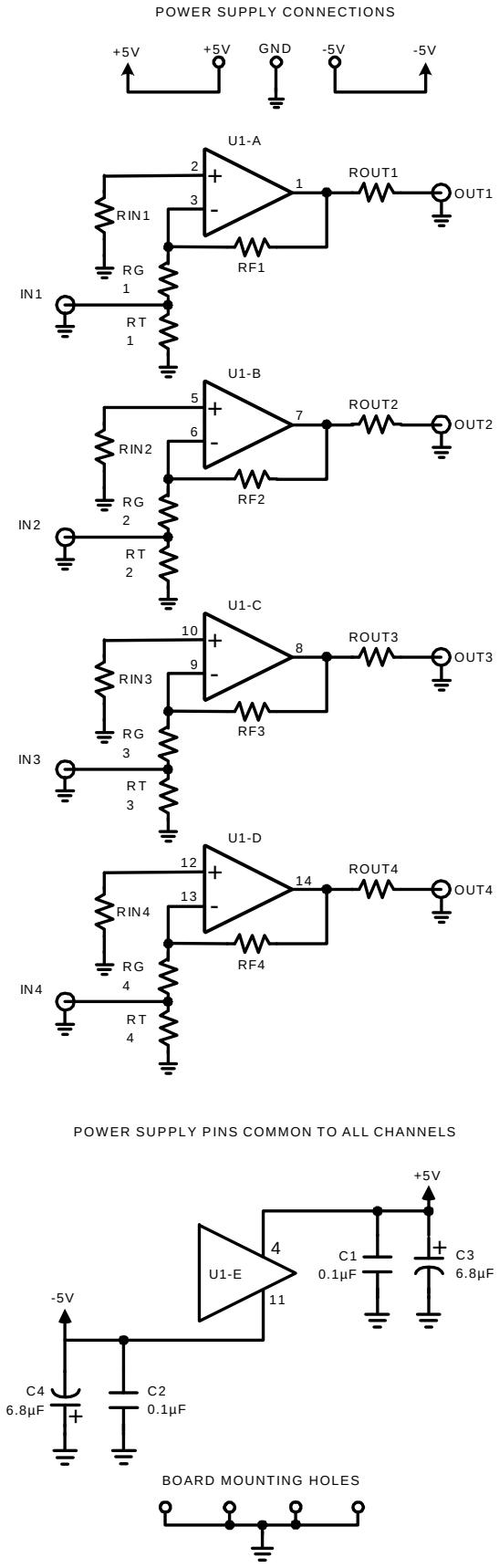


Figure 2: Inverting Op Amp Configuration
(Please Follow Page 5 Modification for Inverting Gain)

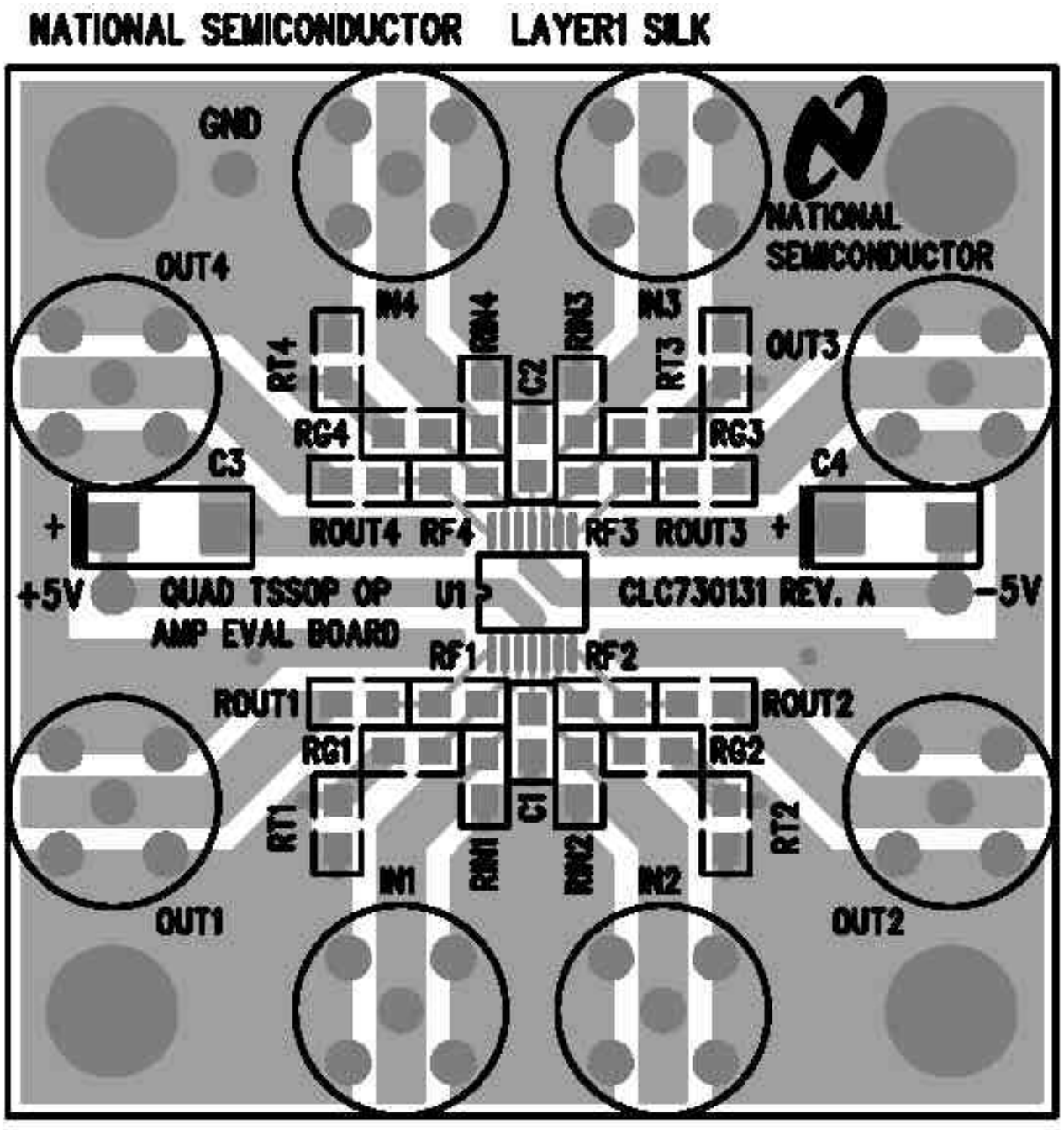


Fig. 3: Component Side View

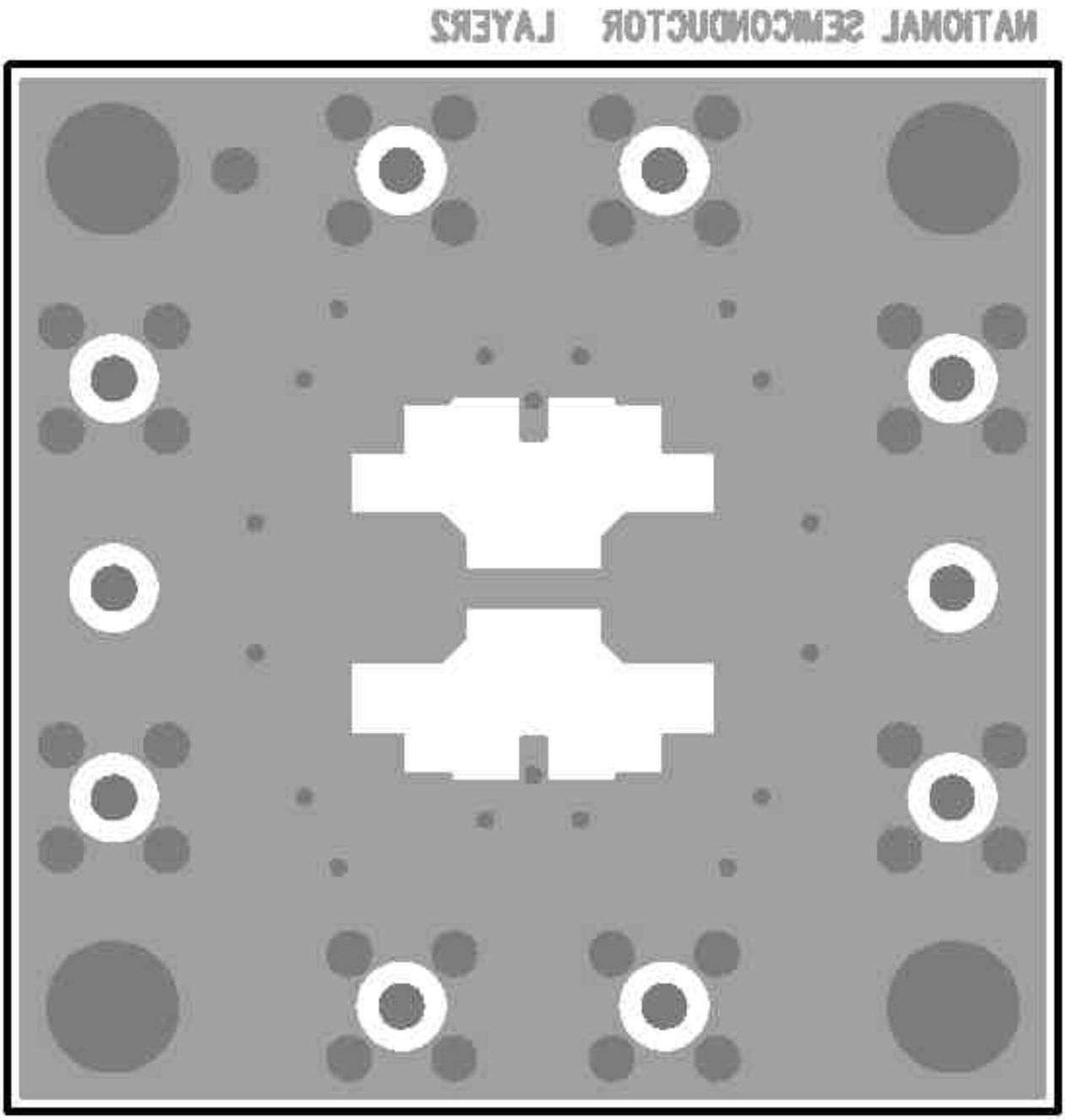


Fig. 4: Circuit Side View

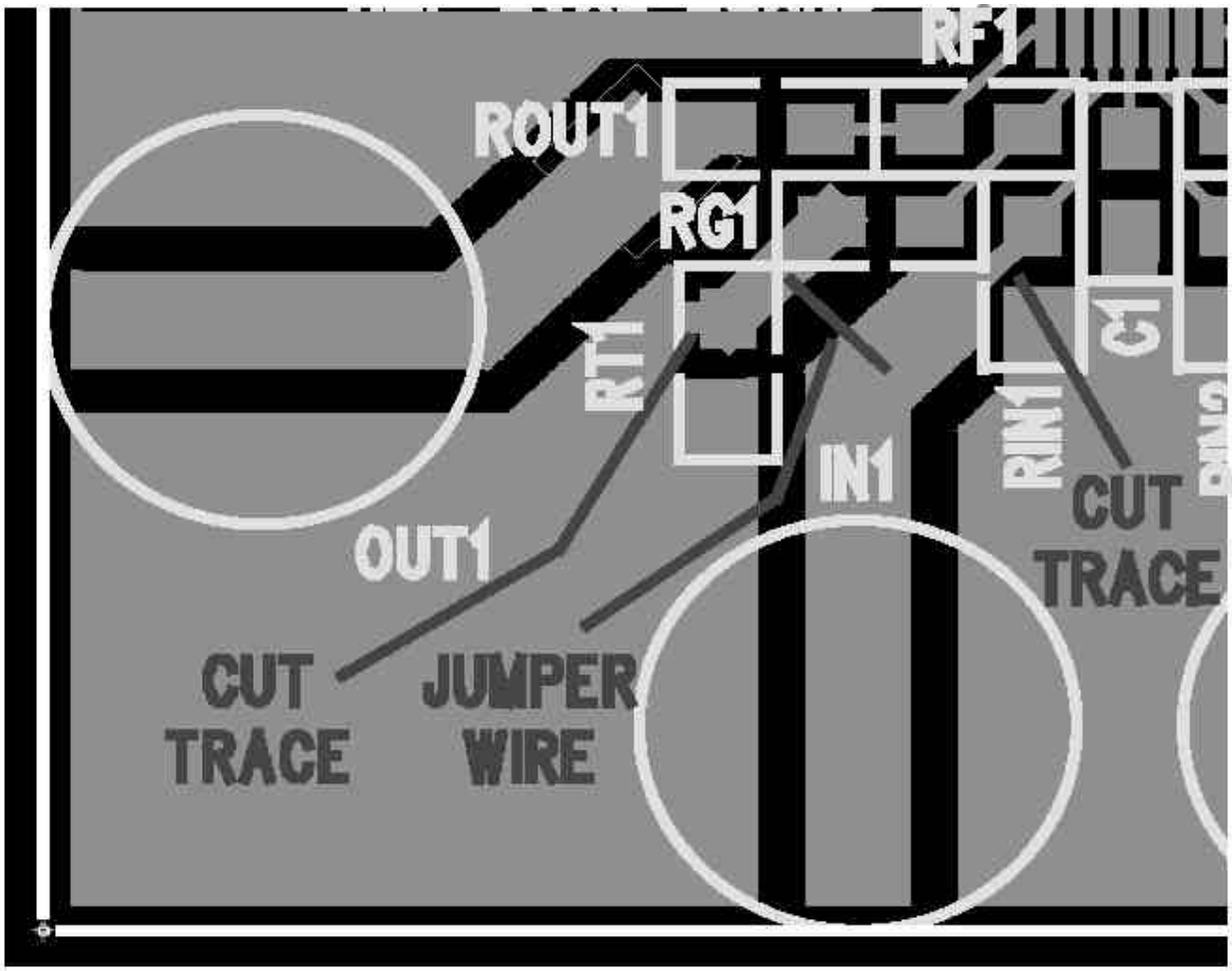


Fig. 5: Details of Modification to Inverting Gain

CLC730131 evaluation board is configured for non-inverting gain. However, it is possible to configure the board for inverting gain. To convert to inverting gain, do the following changes:

How to convert the board for Inverting Gain:

1. Cut RT1 from ground plane by cutting trace going from RT1 (terminal closest to RG1) to the top ground plane. See drawing above.
2. Cut RIN1 from input signal trace by cutting trace going from RIN1 (terminal closest to RG1) to the input signal trace, see drawing above.
3. Add jumper wire from input signal trace to trace connecting RG1 to RT1, see drawing above.

Changes shown for channel 1 only. Repeat these steps on the corresponding components for the other three channels.

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