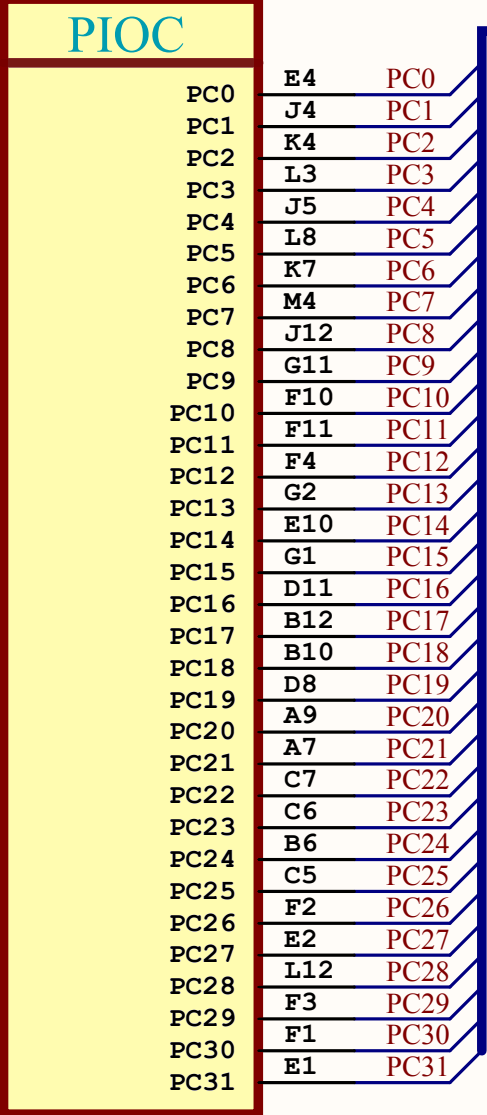
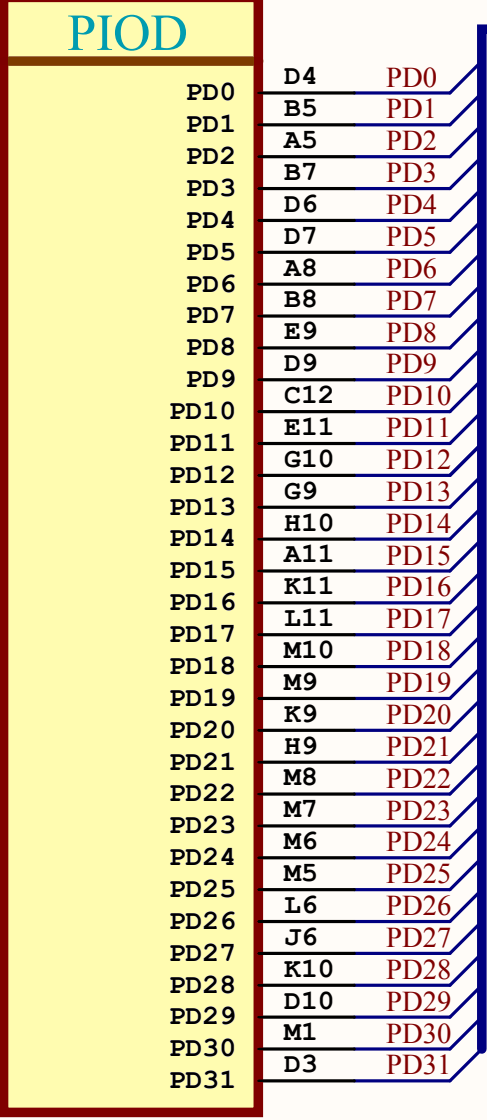


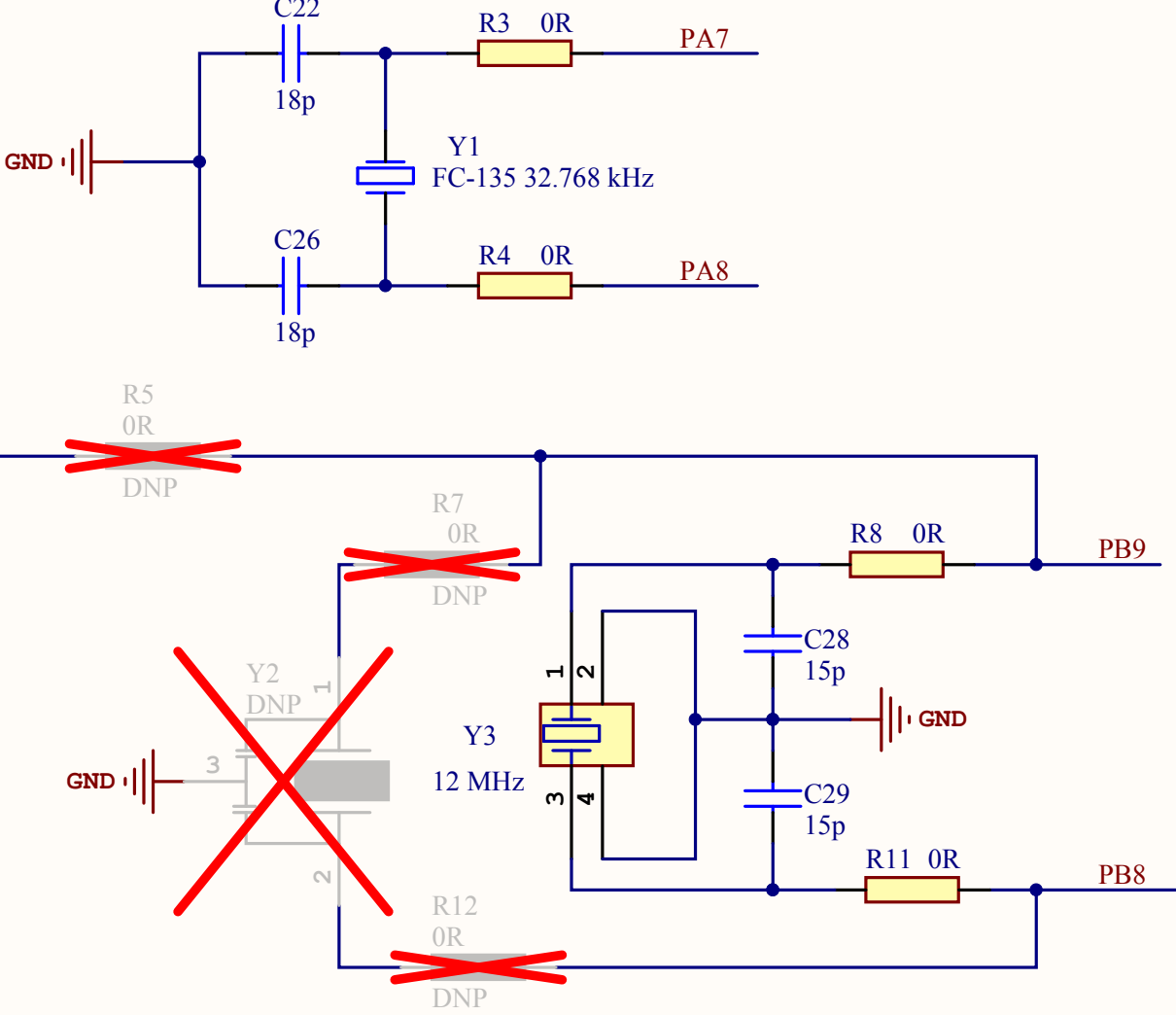
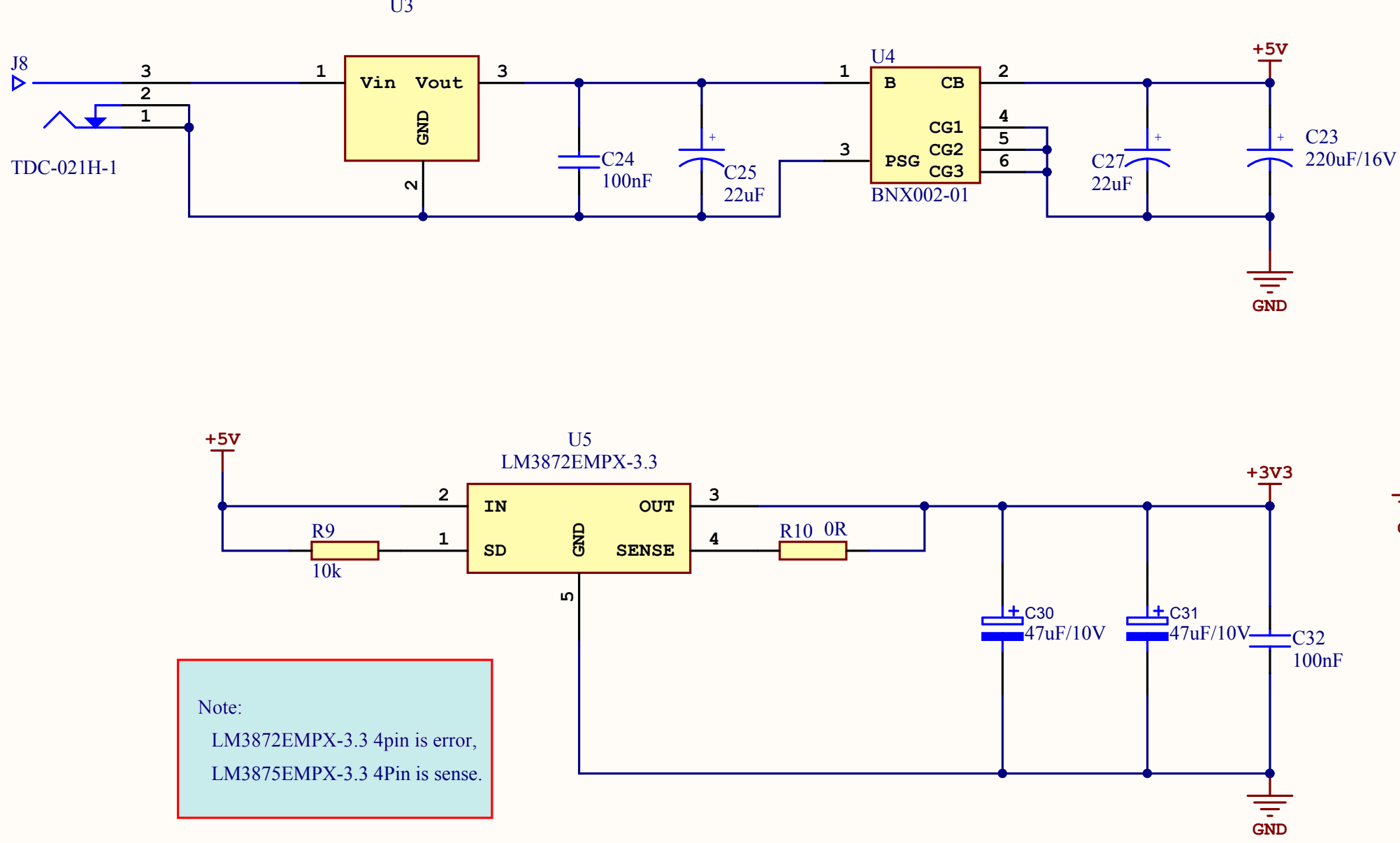
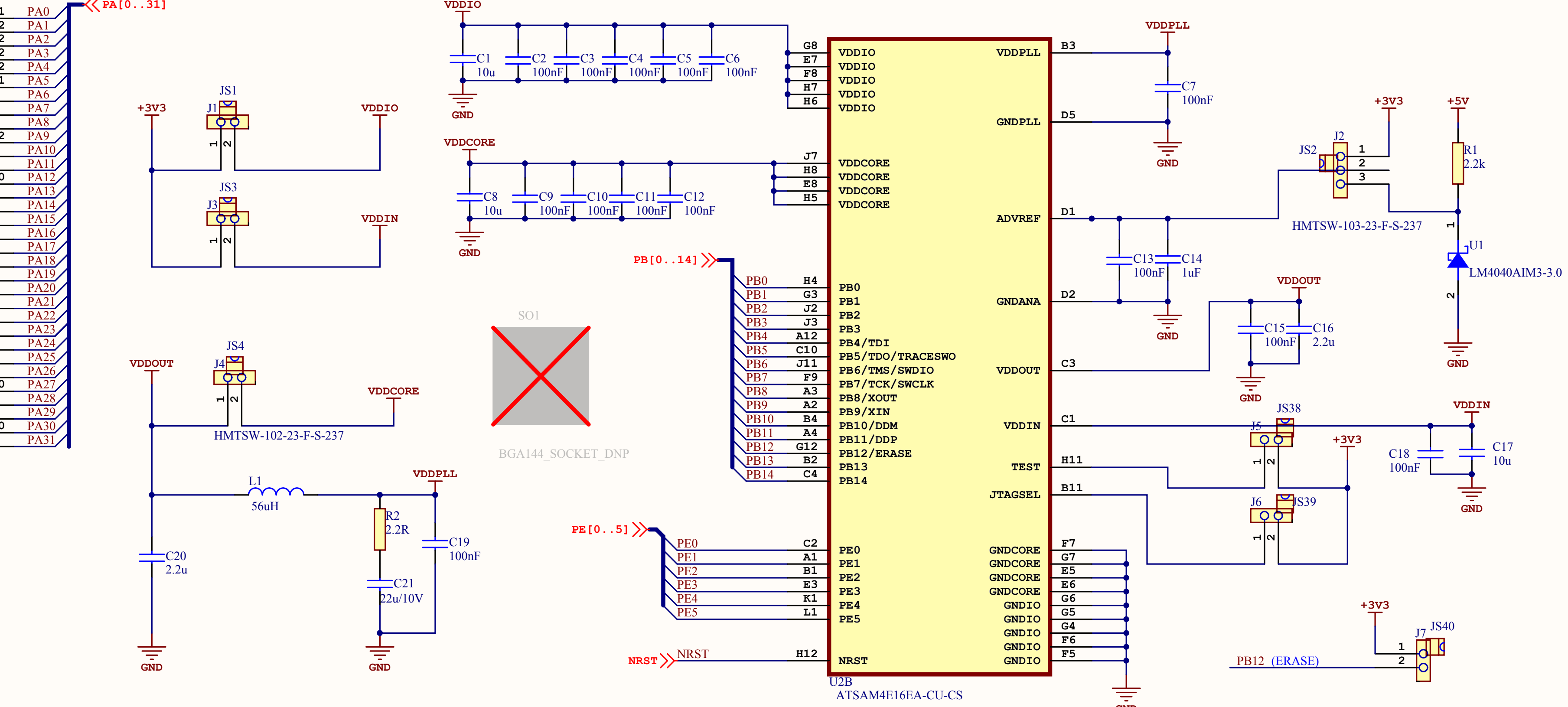
U2A
ATSAM4E16EA-CU-CS




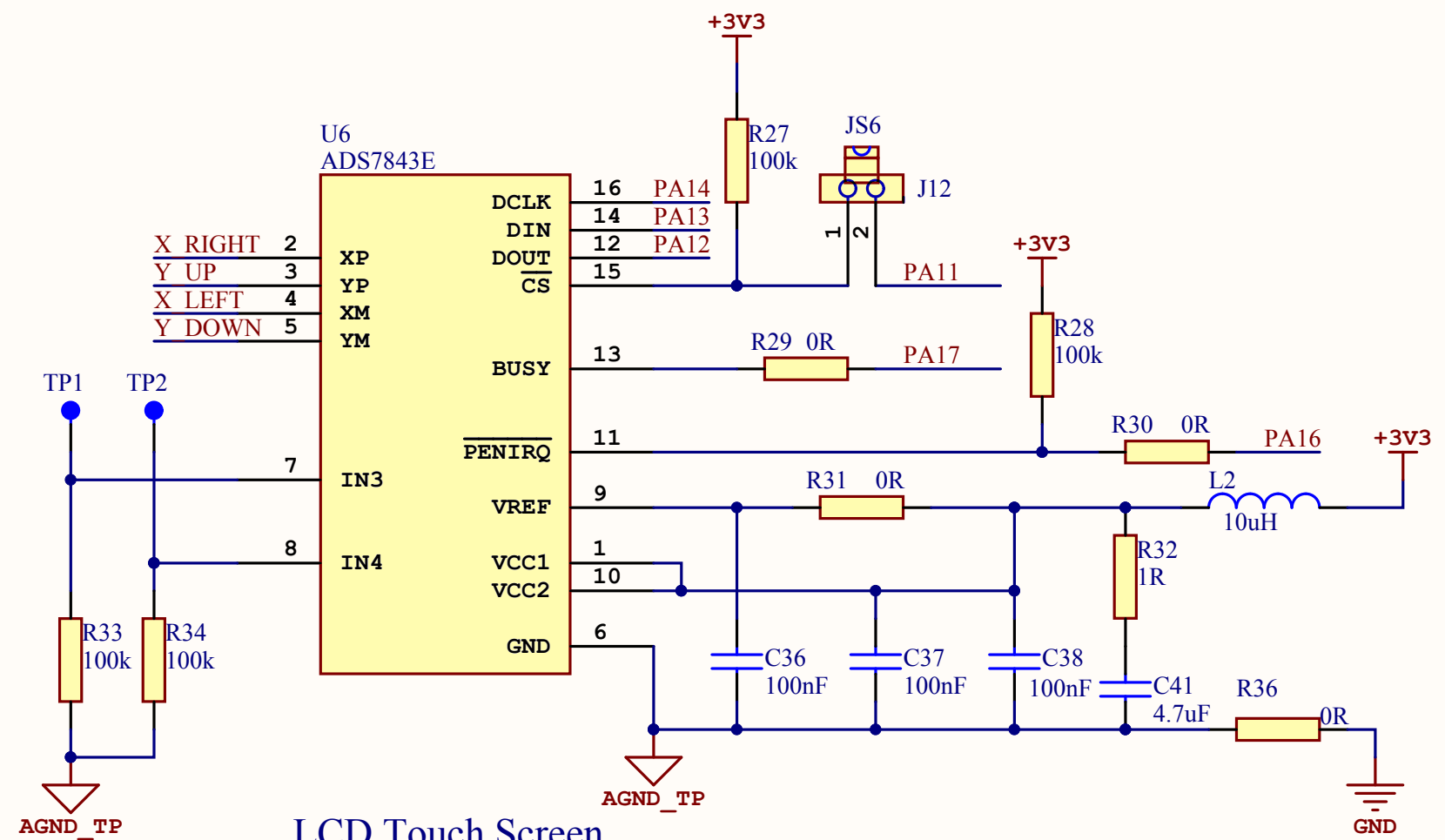
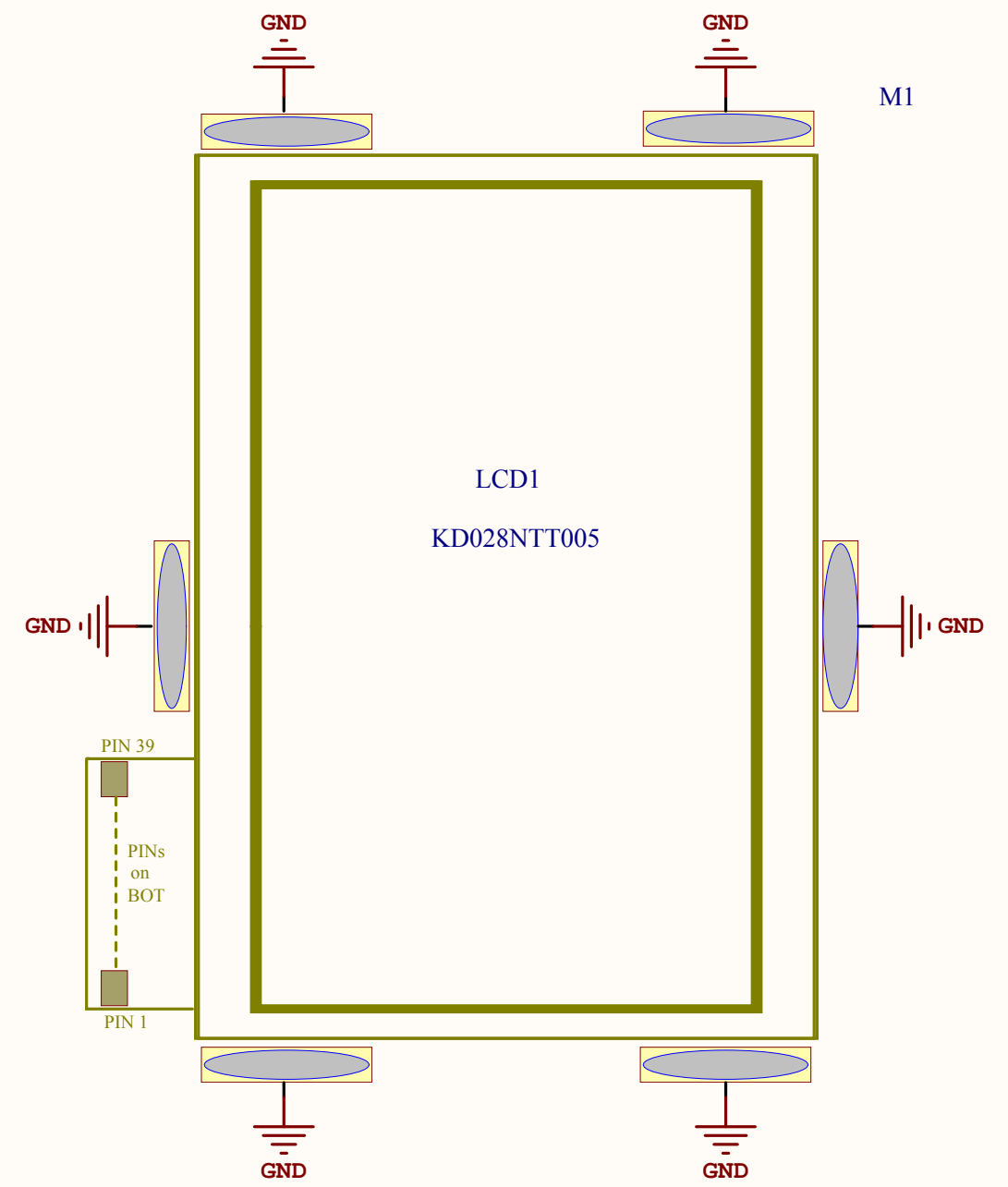
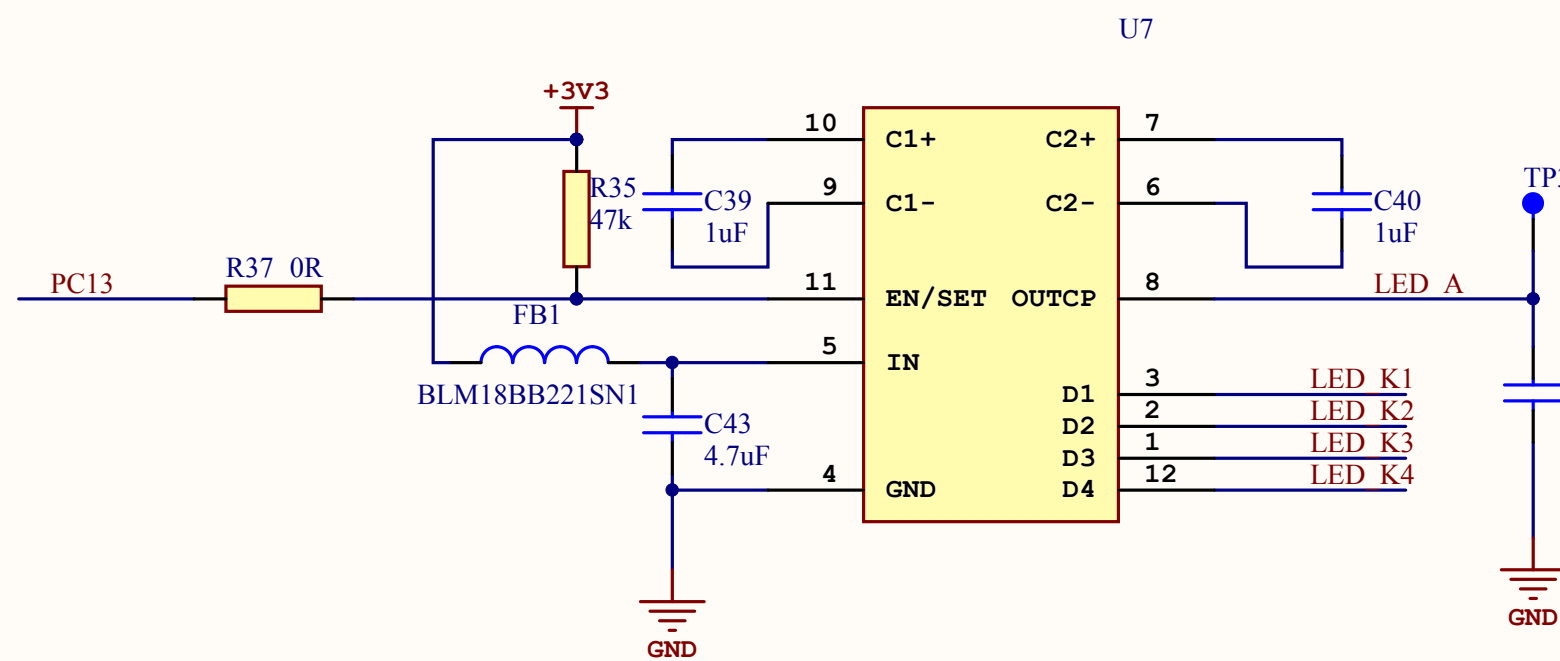
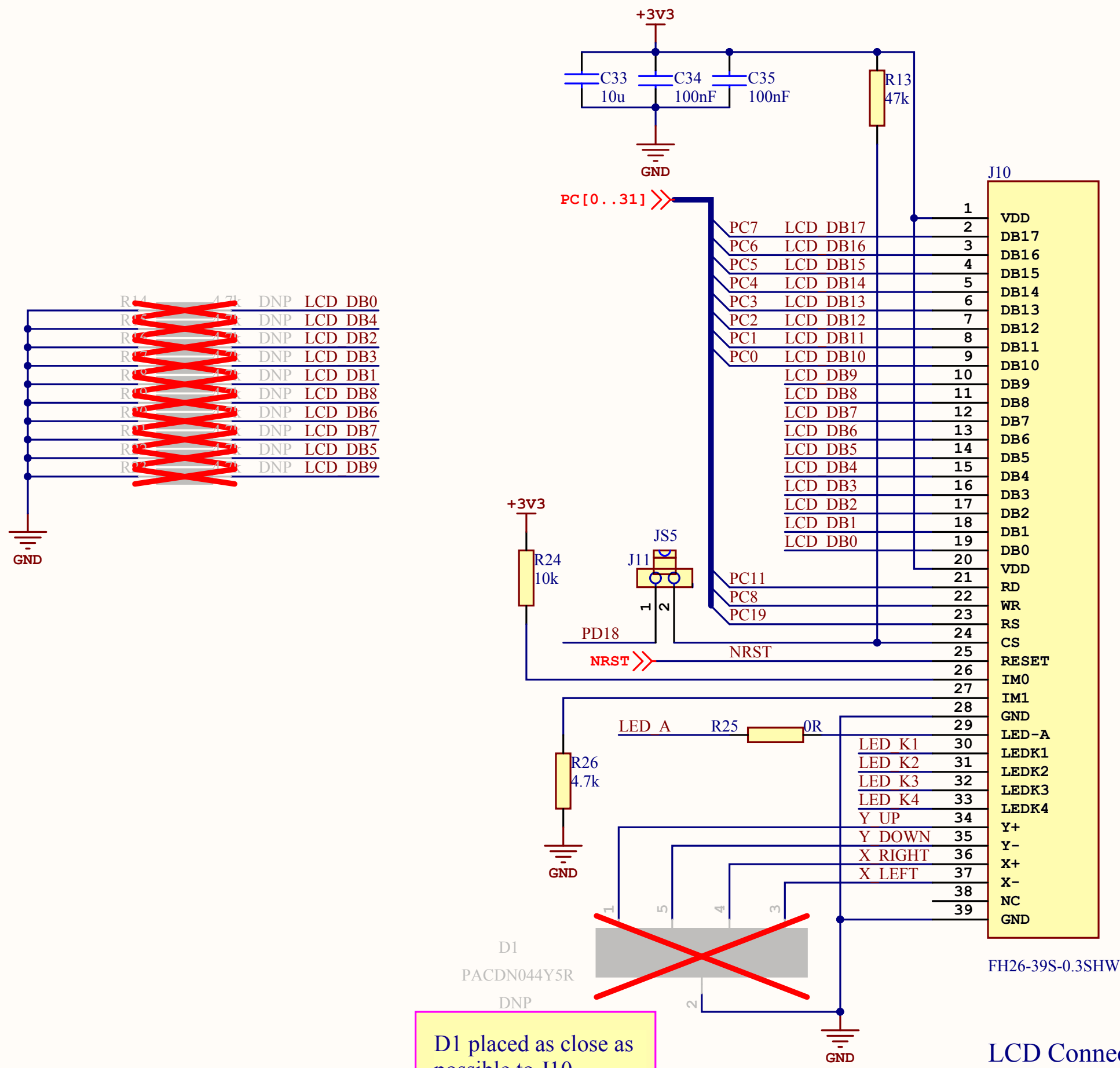
U2C
ATSAM4E16EA-CU-CS




U2D
ATSAM4E16EA-CU-CS



Atmel R&D India		GSK			
10th Flr, C2		MSK			
RMZ , Perungudi		*			
Chennai, India					
Date:		2/28/2014	4:51:27 PM	PAGE:	2 of 8
Document number:		SAM4E-EK		Revision	V3.0
TITLE: SAM4E					
02 - SAM4E.SchDoc					



Atmel R&D India	GSK		 Enabling Unlimited Possibilities		
10th Flr, C2	MSK				
RMZ , Perungudi	*				
Chennai, India					
Date:	2/28/2014	4:51:27 PM	PAGE:	3 of 8	
Document number:	SAM4E-EK		Revision	V3.0	
TITLE: TFT LCD & Touch					
03 - TFT LCD & Touch.SchDoc					

A

B

C

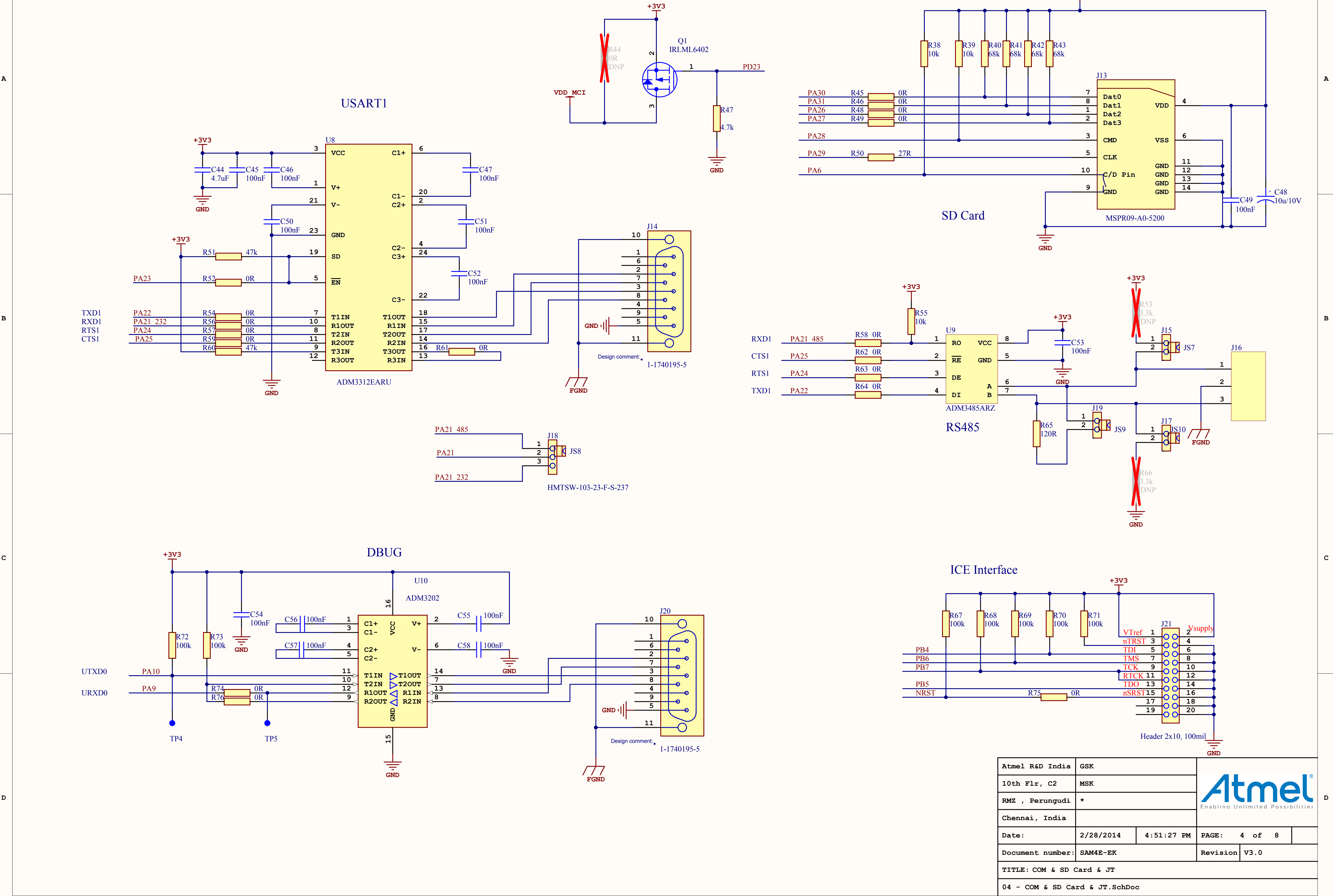
D


A

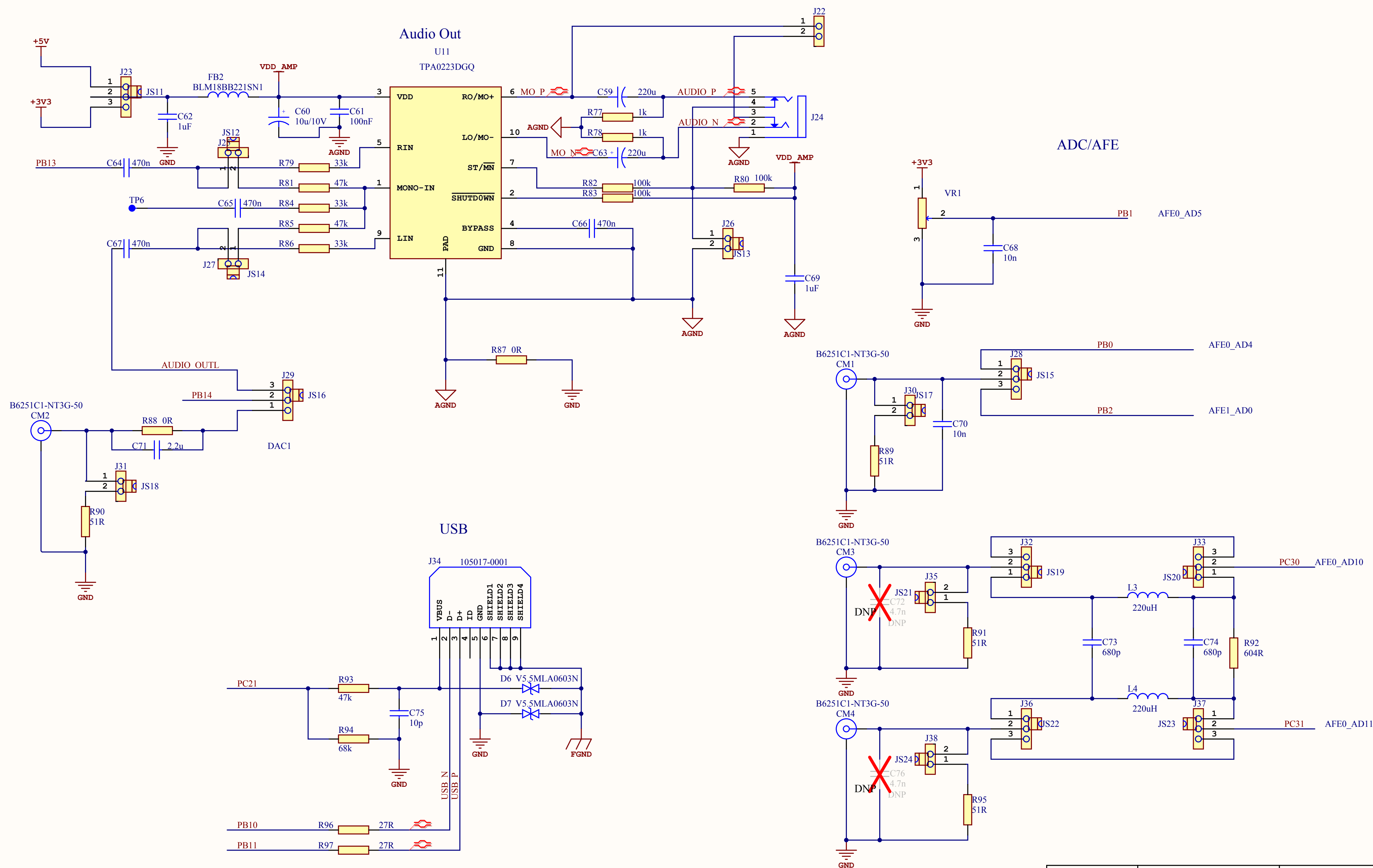
B


C

D



Atmel R&D India		GSK		<div> Enabling Unlimited Possibilities</div>		
10th Flr, C2		MSK				
RMZ , Perungudi		*				
Chennai, India						
Date:		2/28/2014	4:51:27 PM	PAGE: 4 of 8		
Document number:			SAM4E-EK		Revision:	V3.0
TITLE: COM & SD Card & JT						
04 - COM & SD Card & JT.SchDoc						



Atmel R&D India	GSK	 Enabling Unlimited Possibilities	
10th Flr, C2	MSK		
RMZ , Perungudi	*		
Chennai, India			
Date:	2/28/2014	4:51:27 PM	PAGE: 5 of 8
Document number:	SAM4E-EK		Revision V3.0
TITLE: AFE & DAC & USB			
05 - AFE & DAC & USB.SchDoc			

A

A

B

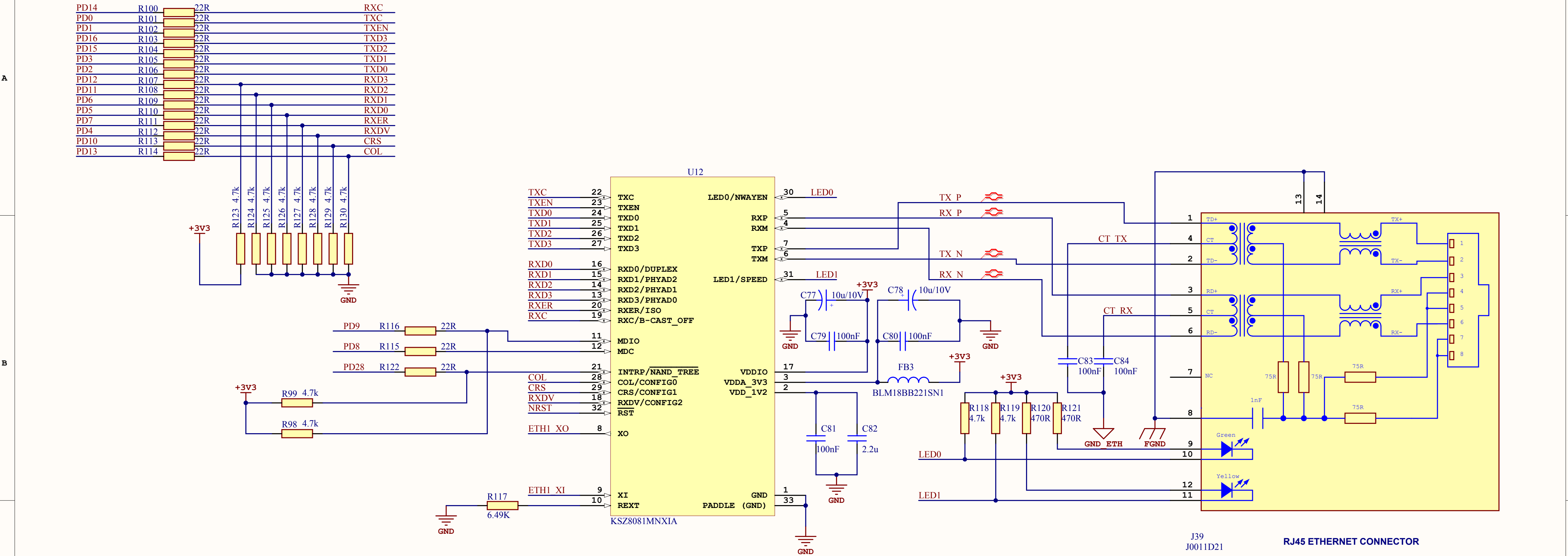
B

C

C

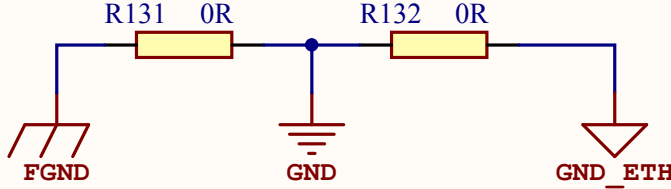
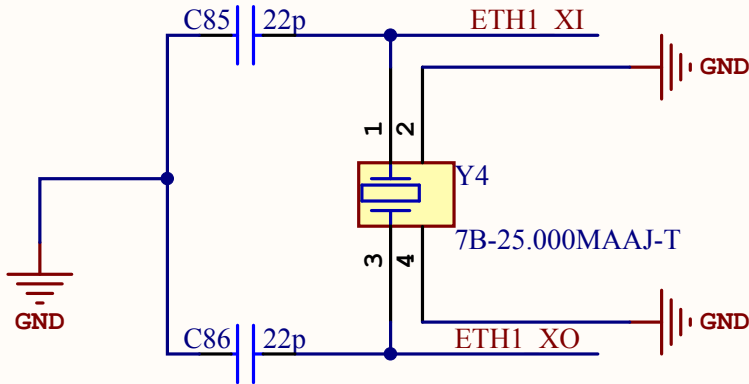
D


D

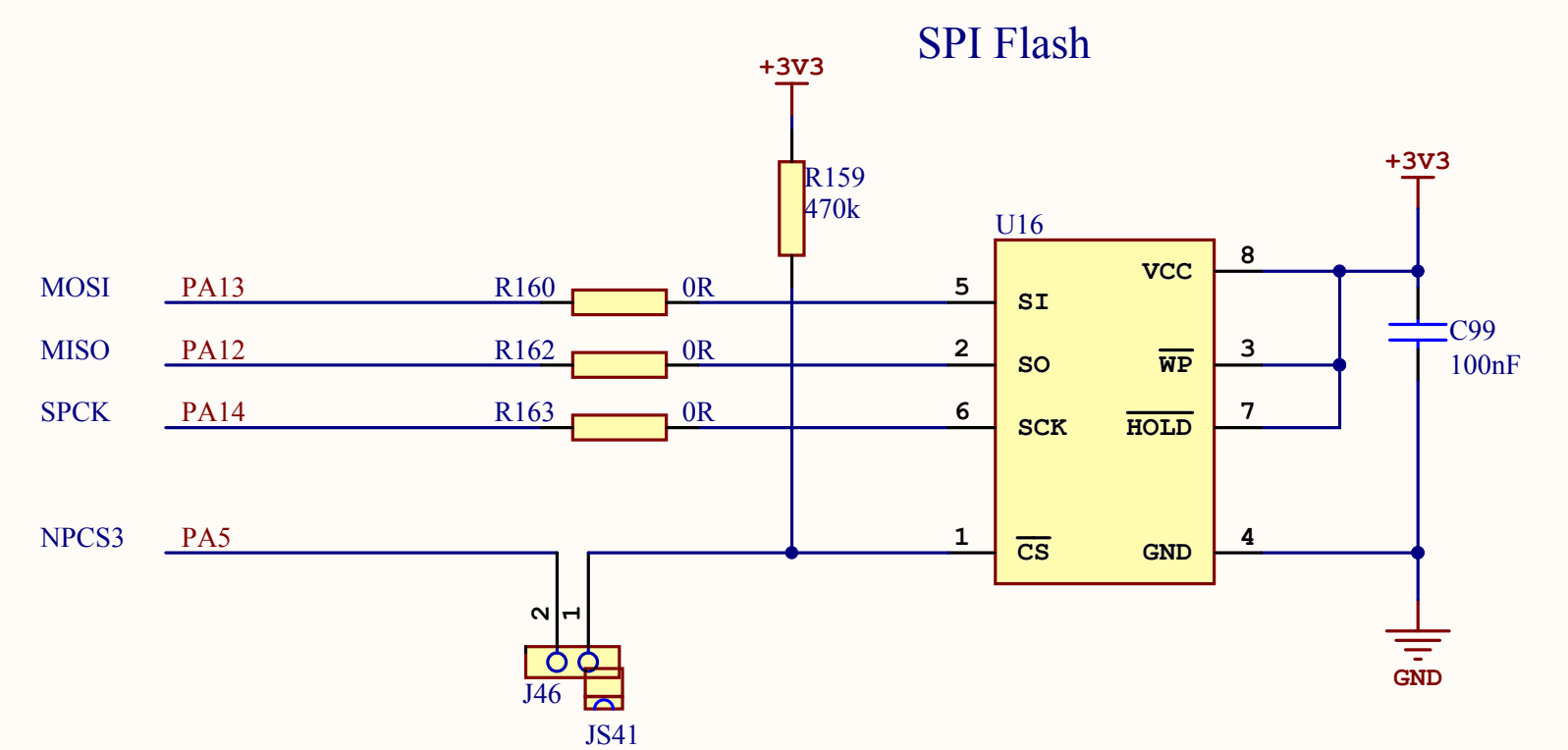
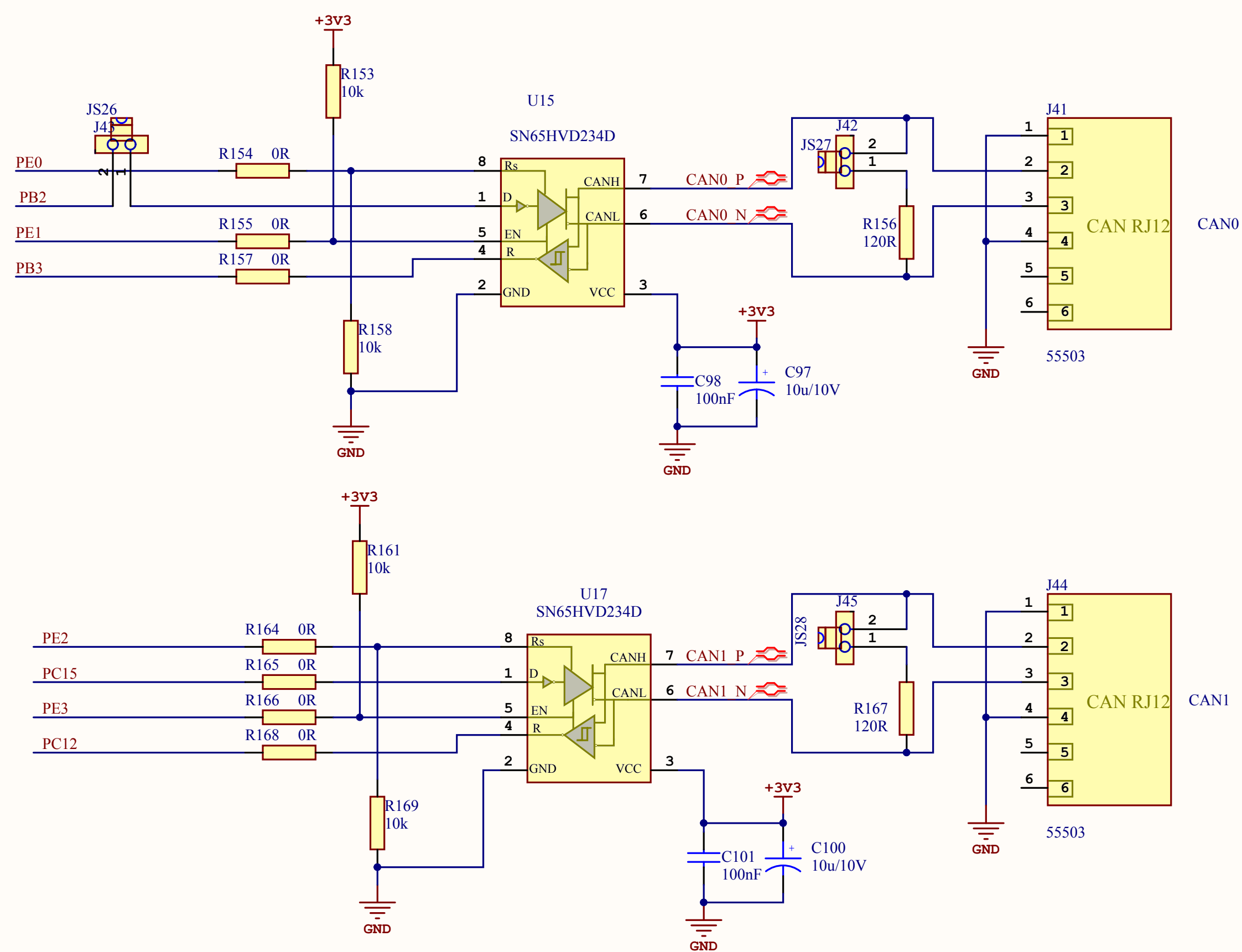
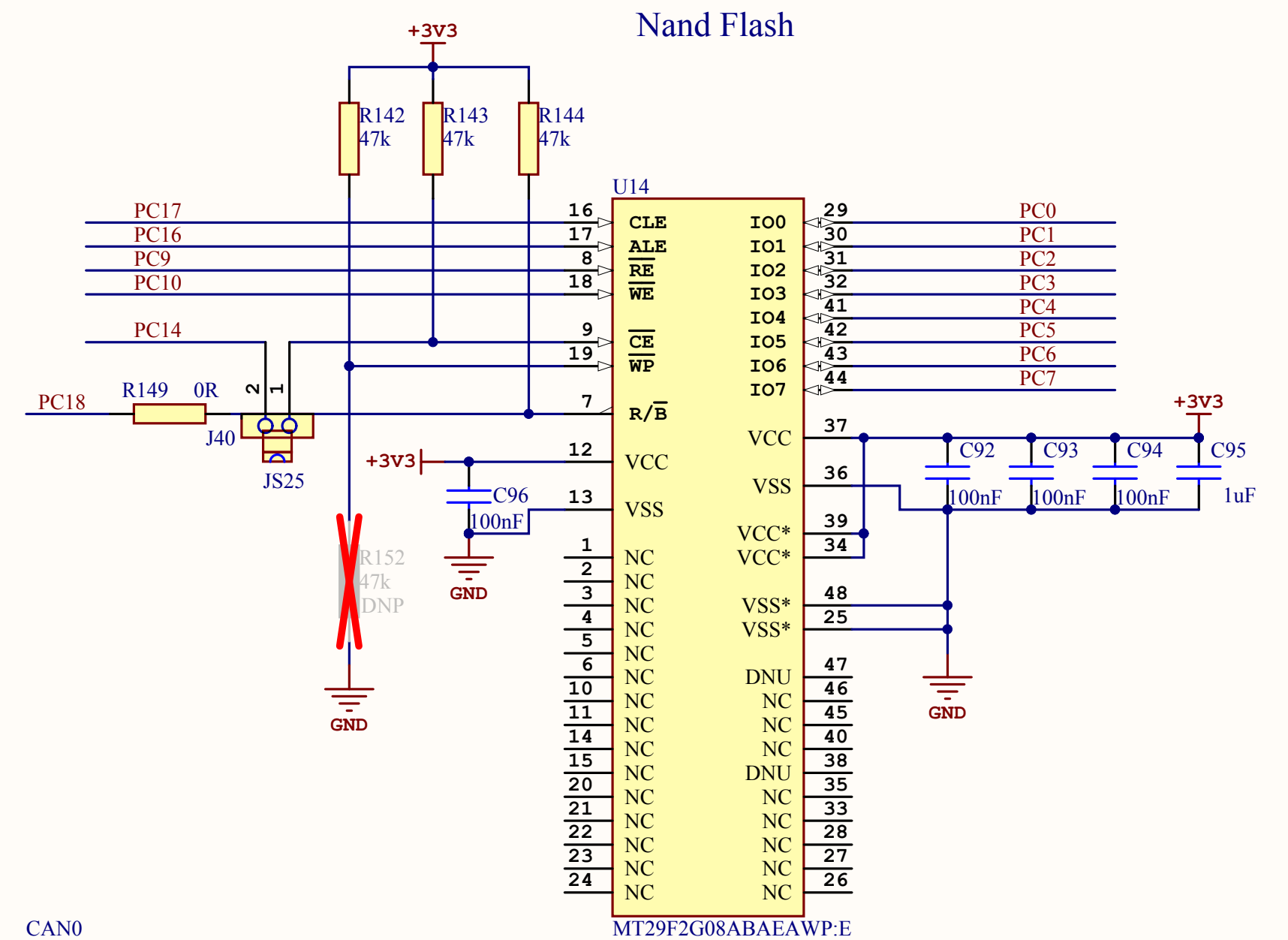
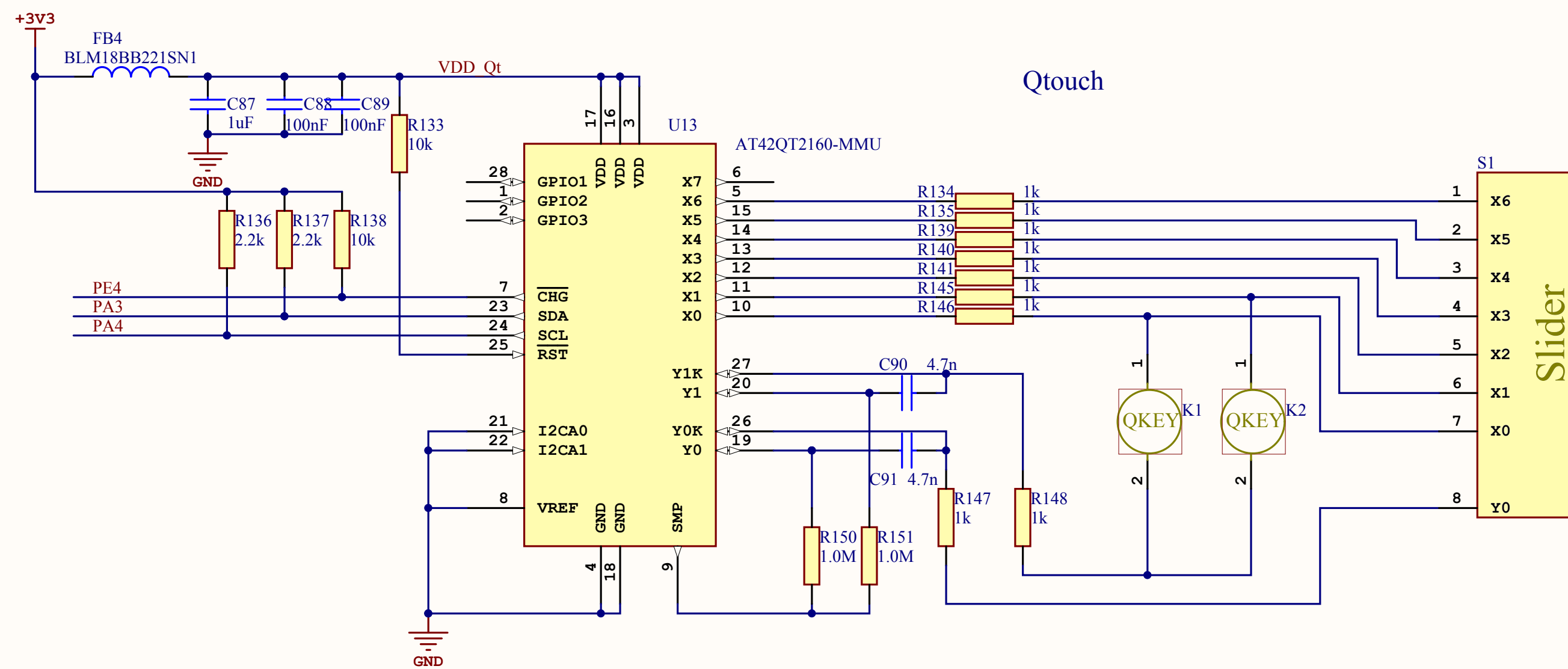



ETH_PHY

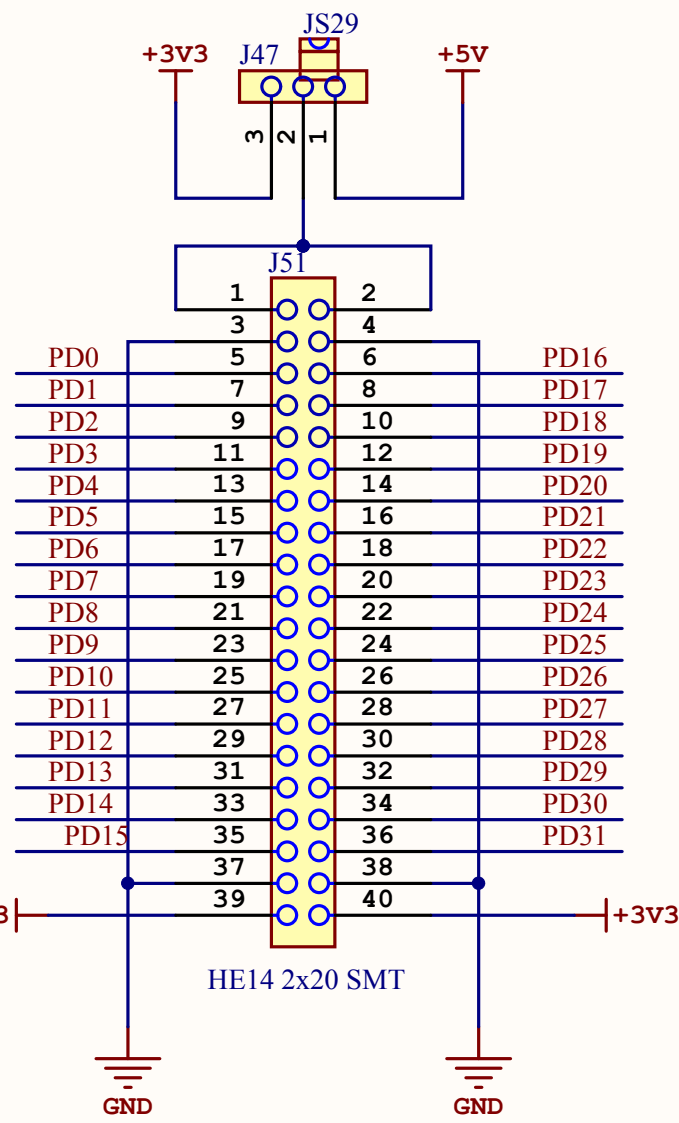
PHY ADD[2:0]: 001
CONFIG[2:0]:000,MODE: MII
Duplex MODE: Half Duplex
Speed MODE: 100Mbps
Enable Auto-Negotiation



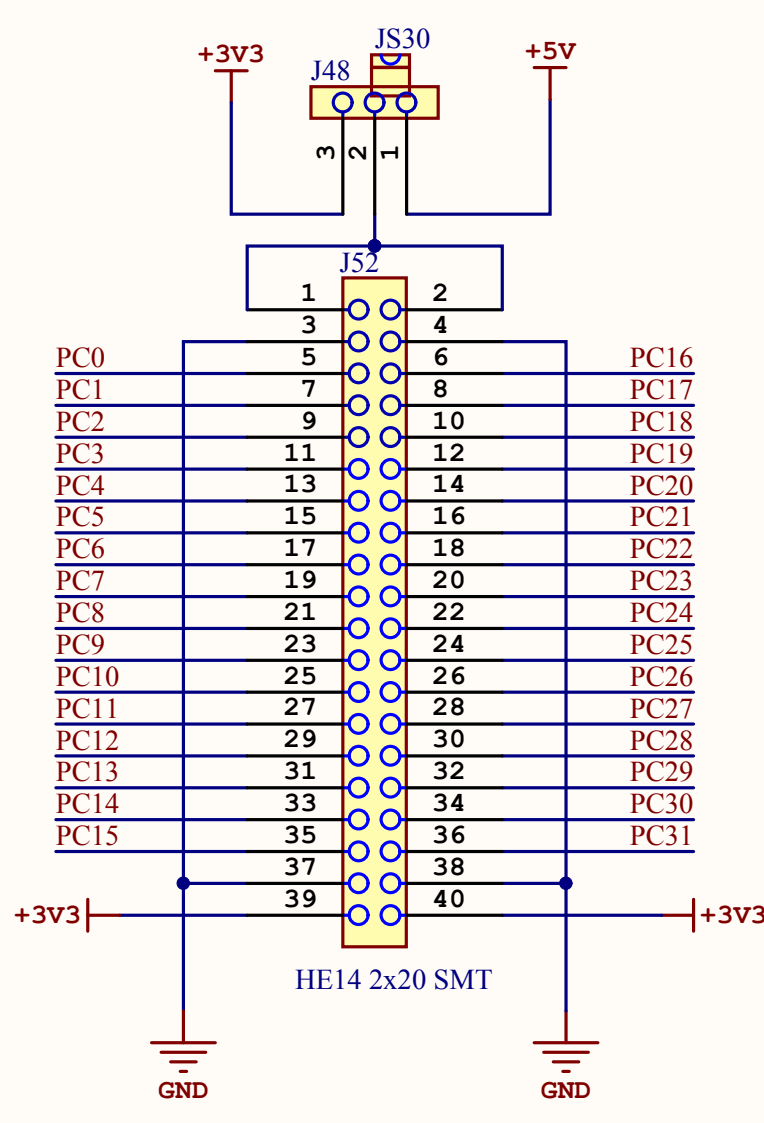
Atmel R&D India		GSK			
10th Flr, C2		MSK			
RMZ , Perungudi		*			
Chennai, India					
Date:	2/28/2014	4:51:27 PM	PAGE:	6 of 8	
Document number:	SAM4E-EK		Revision	V3.0	
TITLE: ETH					
06 - ETH.SchDoc					



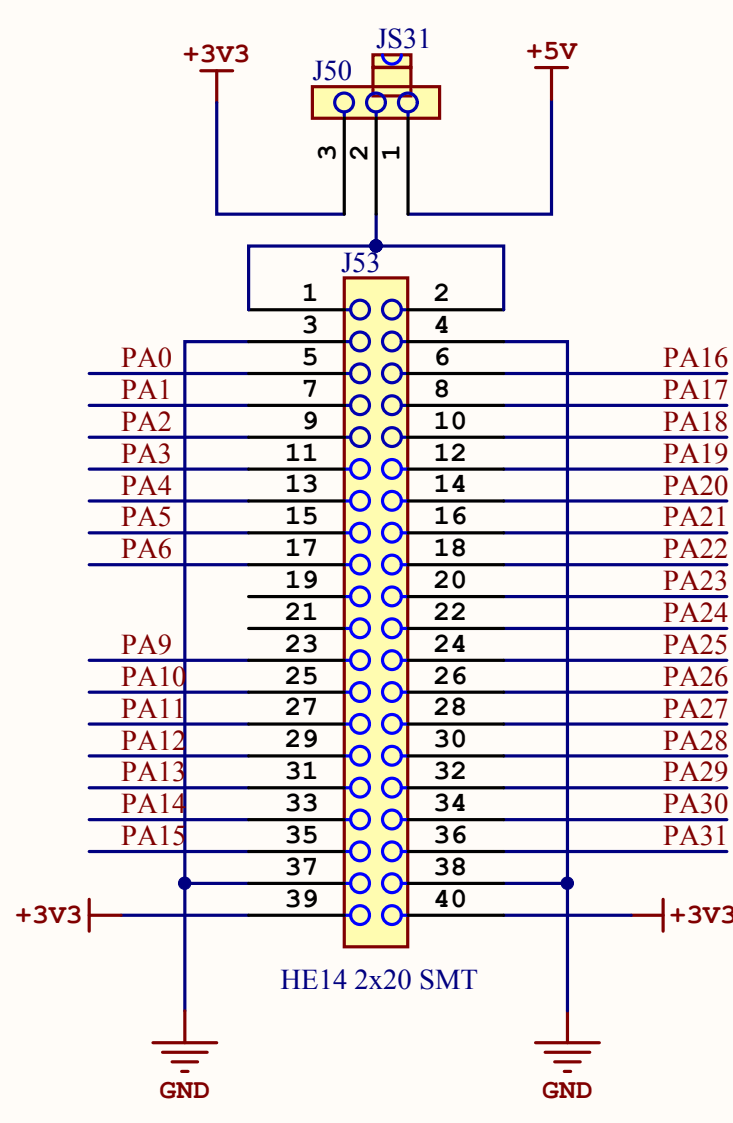
Atmel R&D India	GSK		
10th Flr, C2	MSK		
RMZ , Perungudi	*		
Chennai, India			
Date:	2/28/2014	4:51:28 PM	PAGE: 7 of 8
Document number:	SAM4E-EK		Revision V3.0
TITLE: Qtouch & CAN & Fla			
07 - Qtouch & CAN & Fla.SchDoc			



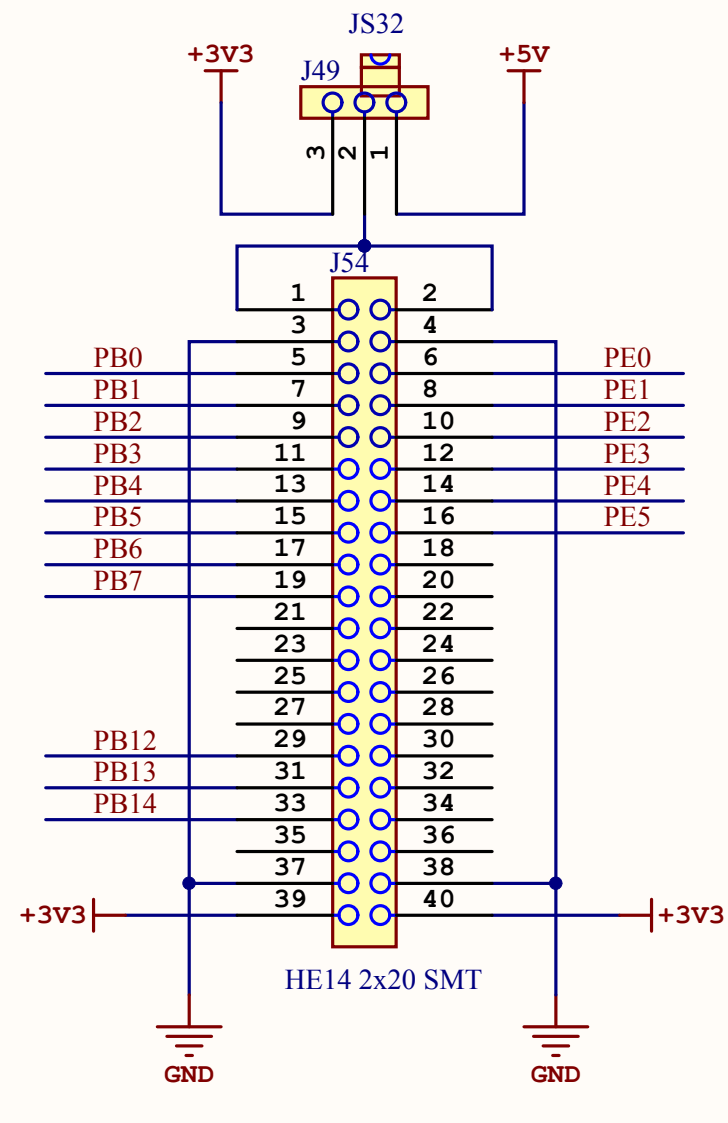
PIOD



PIOC

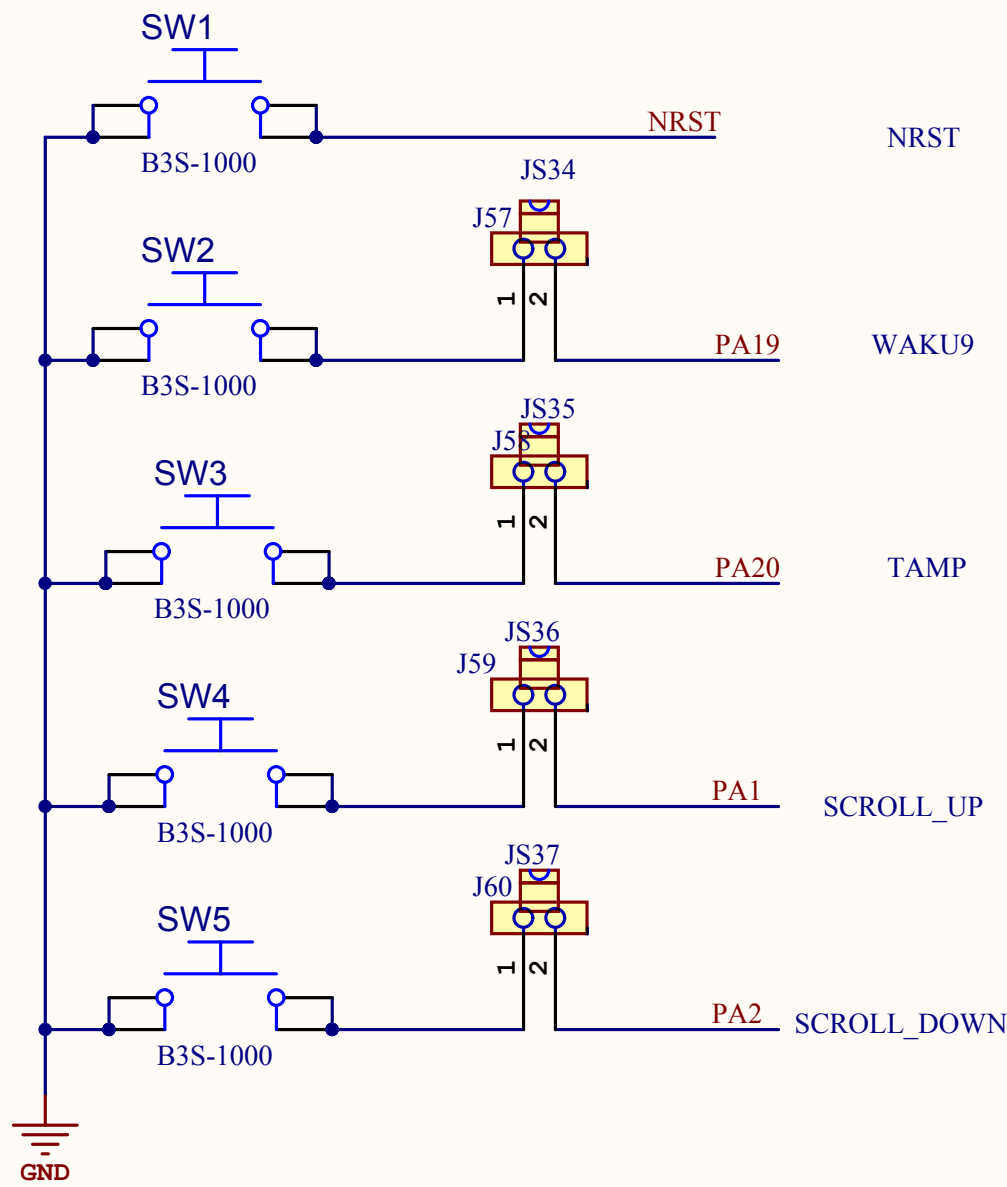


PIOA

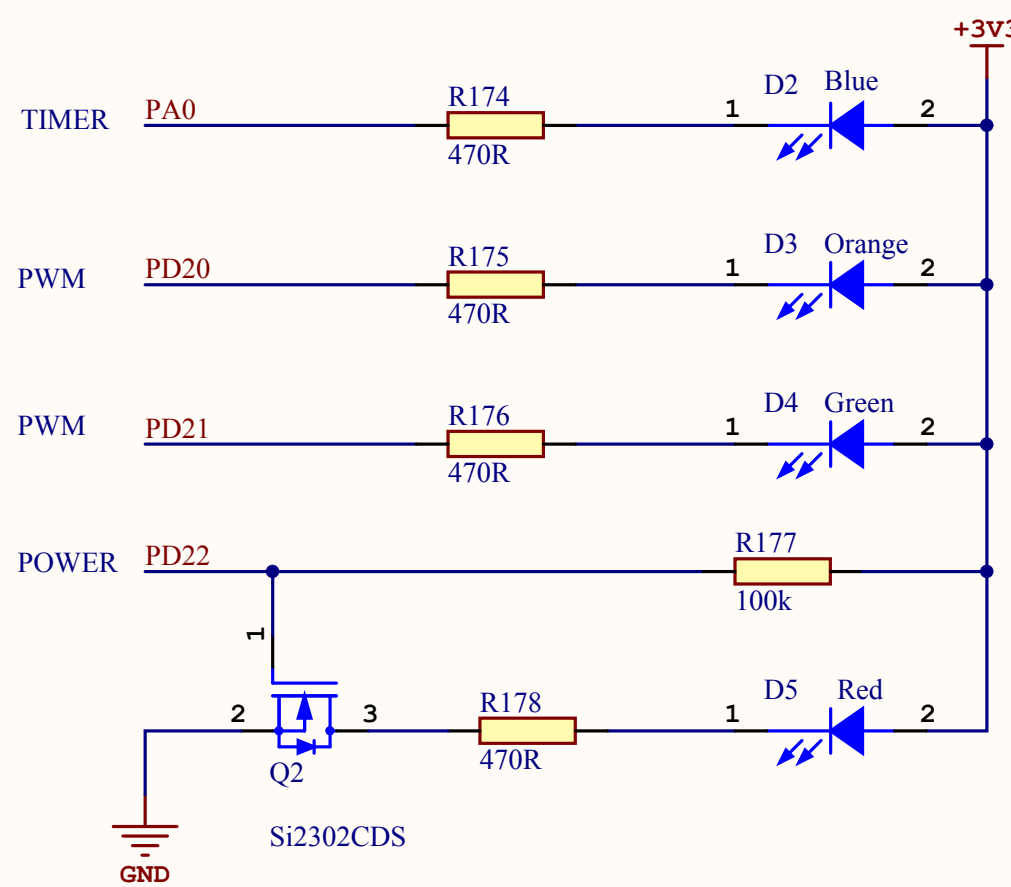


PIOB&E

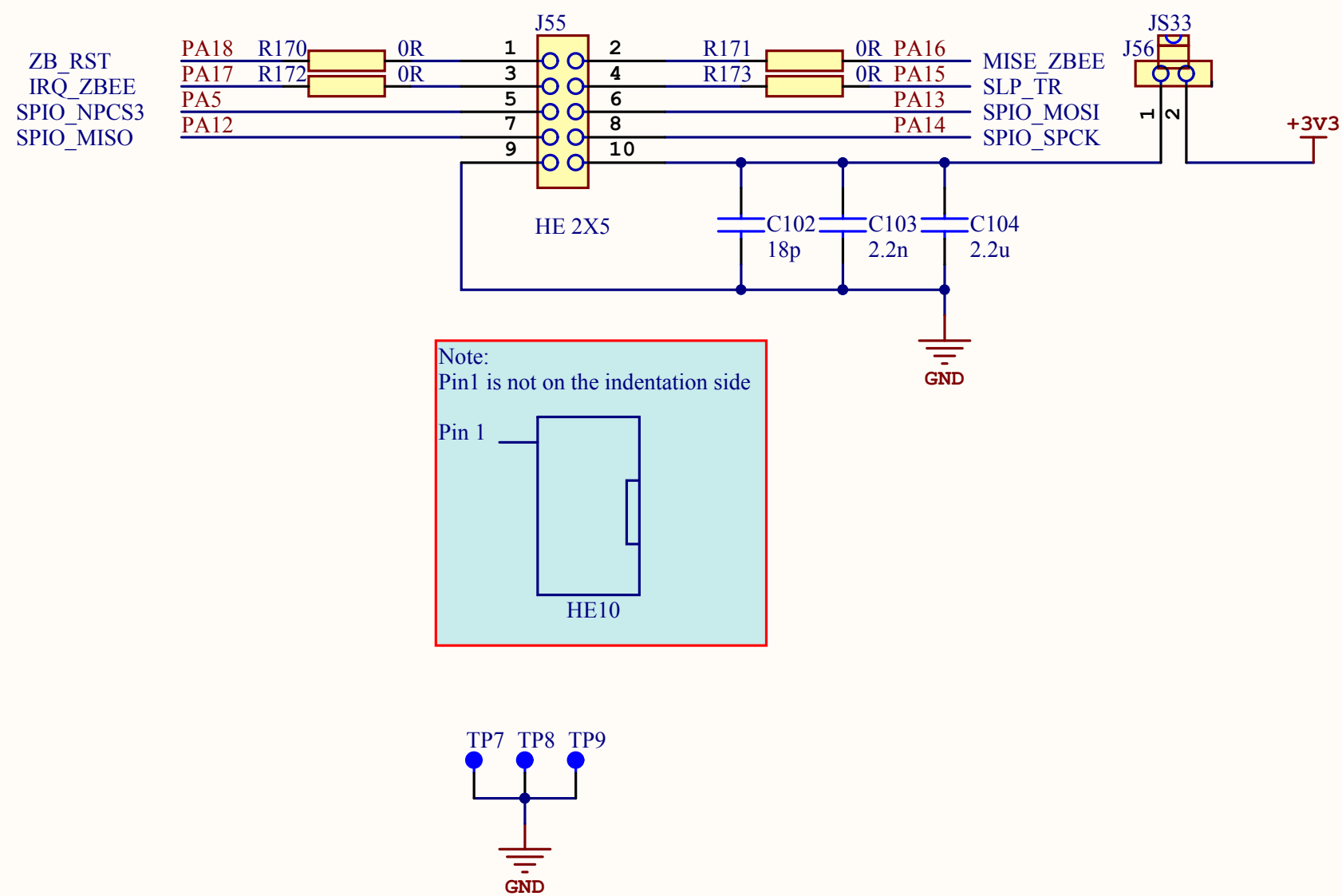
Buttons



Leds

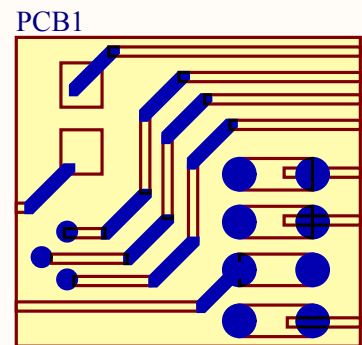
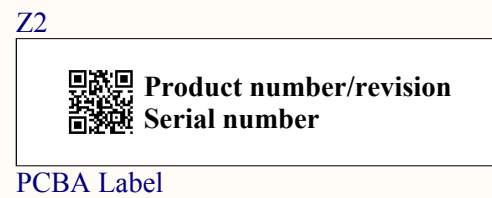
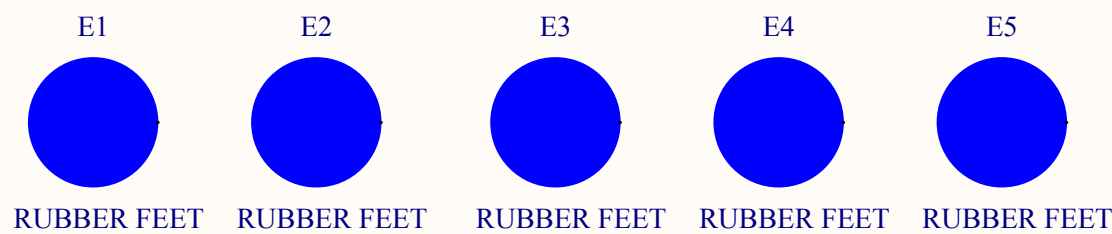
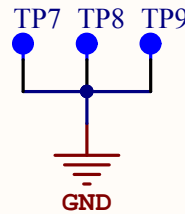


ZIGBEE




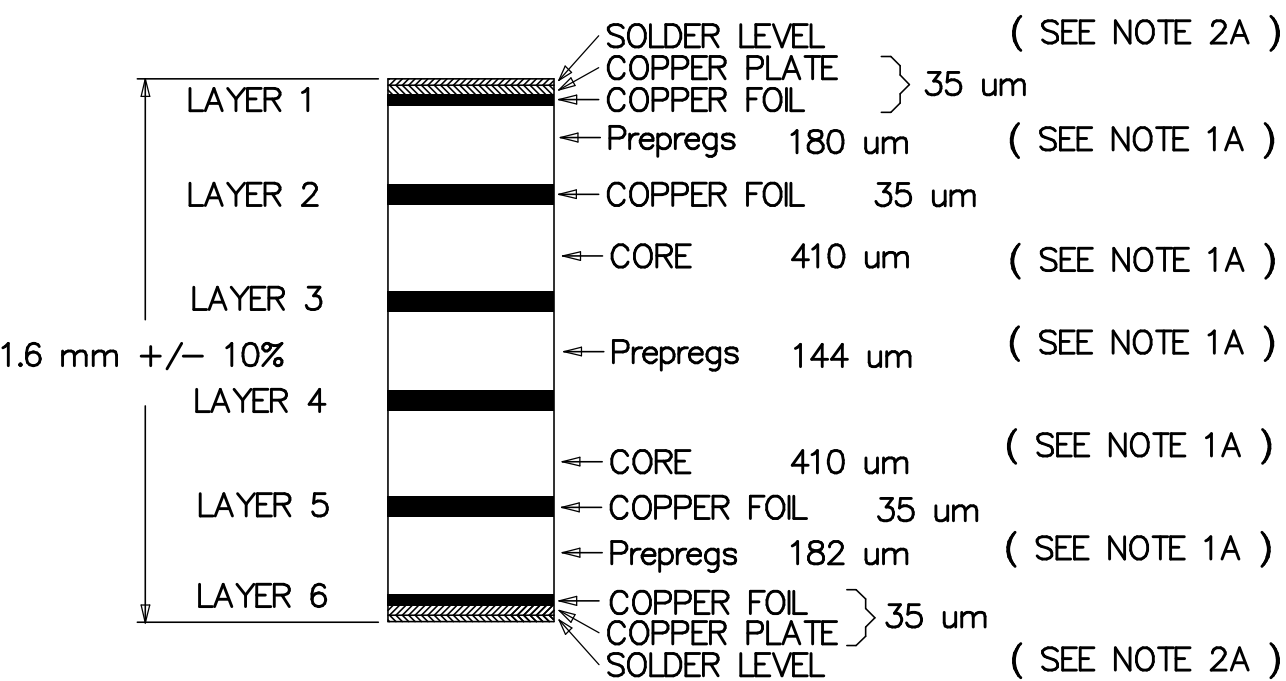
Note:
Pin1 is not on the indentation side

HE10



SAM4E-EK PCB

Atmel R&D India		GSK		 Enabling Unlimited Possibilities	
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RMZ , Perungudi		*			
Chennai, India					
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Document number:	SAM4E-EK			Revision	V3.0
TITLE: I _ O Peripheral					
08 - I_O Peripheral.SchDoc					

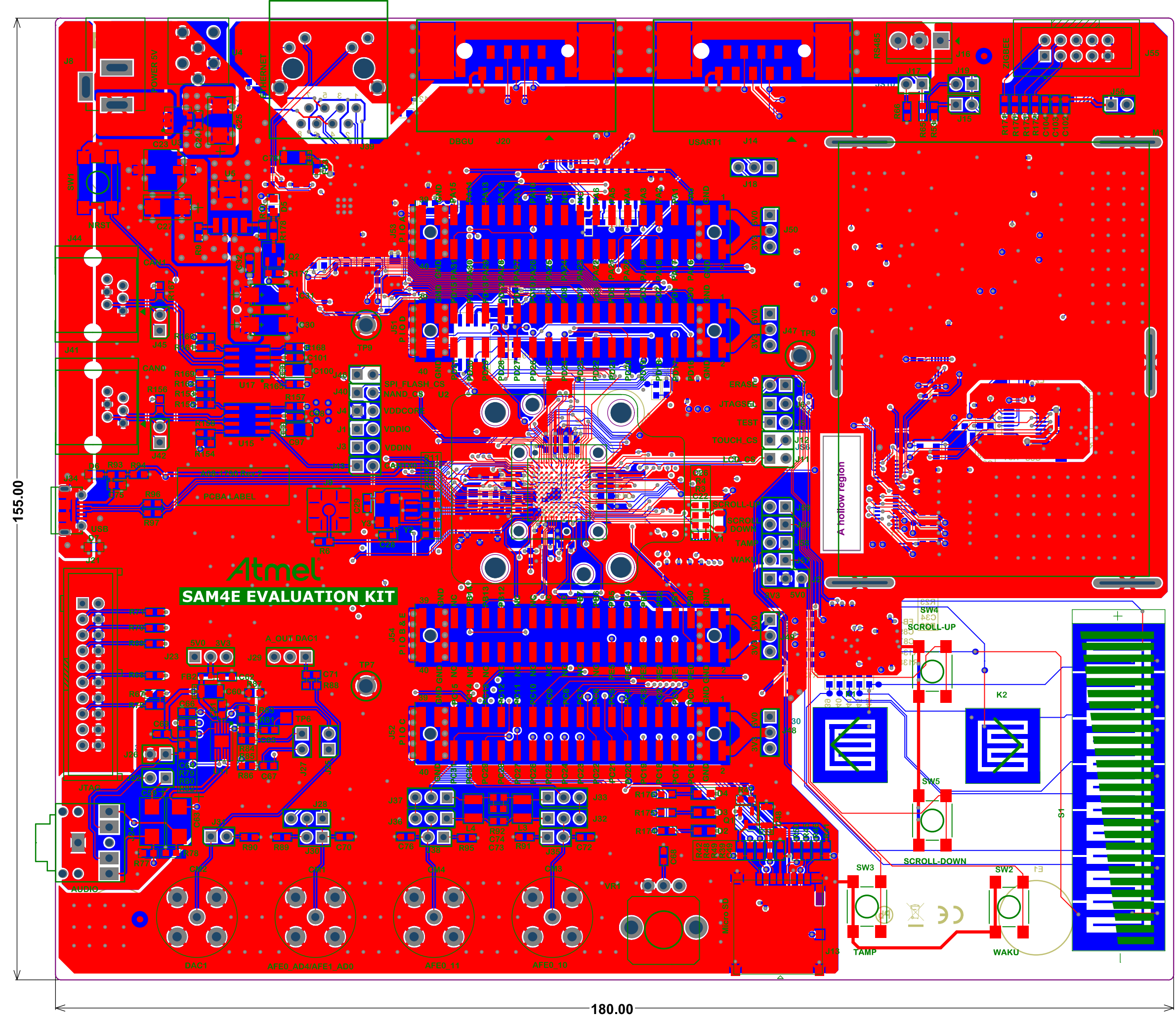


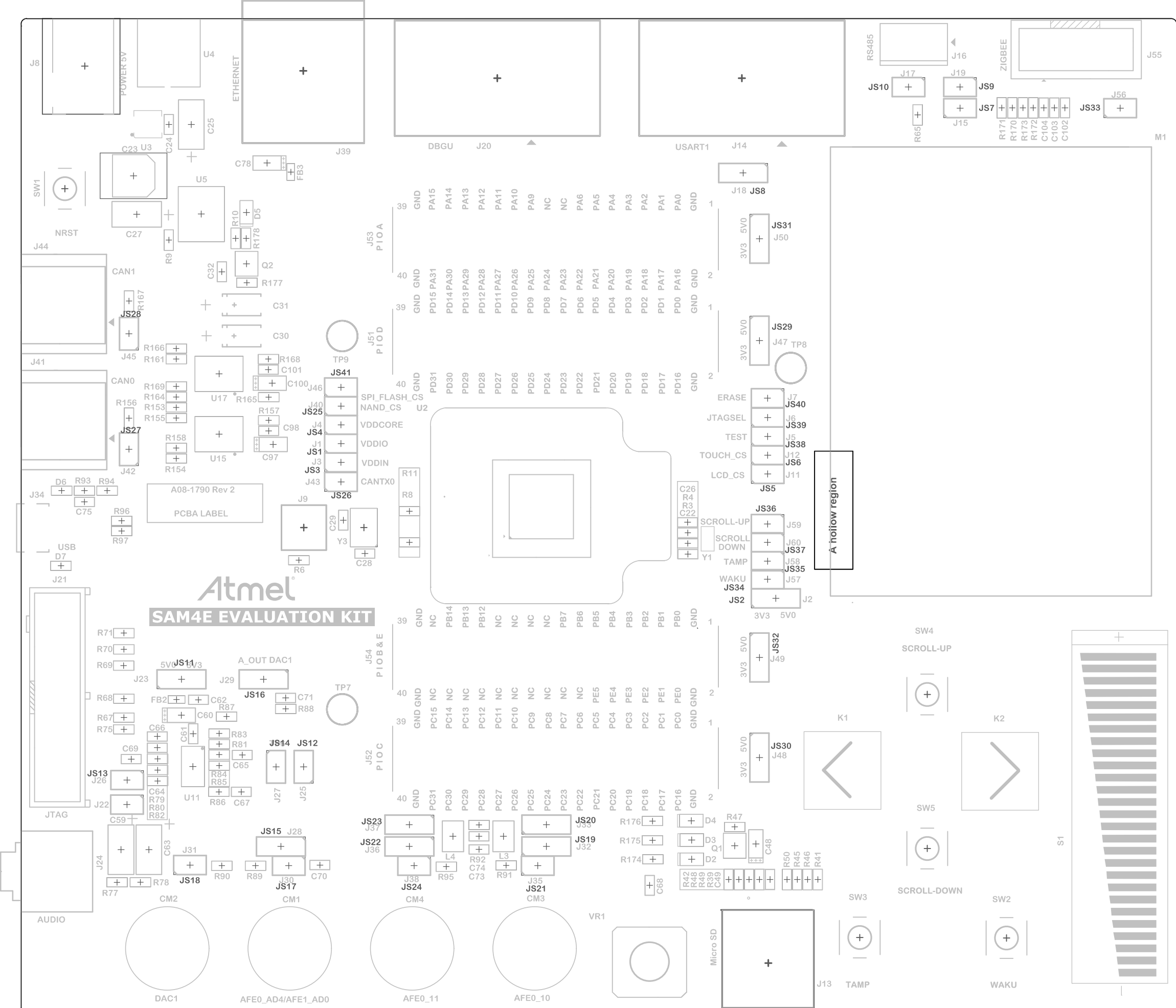
NOTE 1A: DIELECTRIC FR4
2A: SURFACE PROTECTION: Chemical Gold

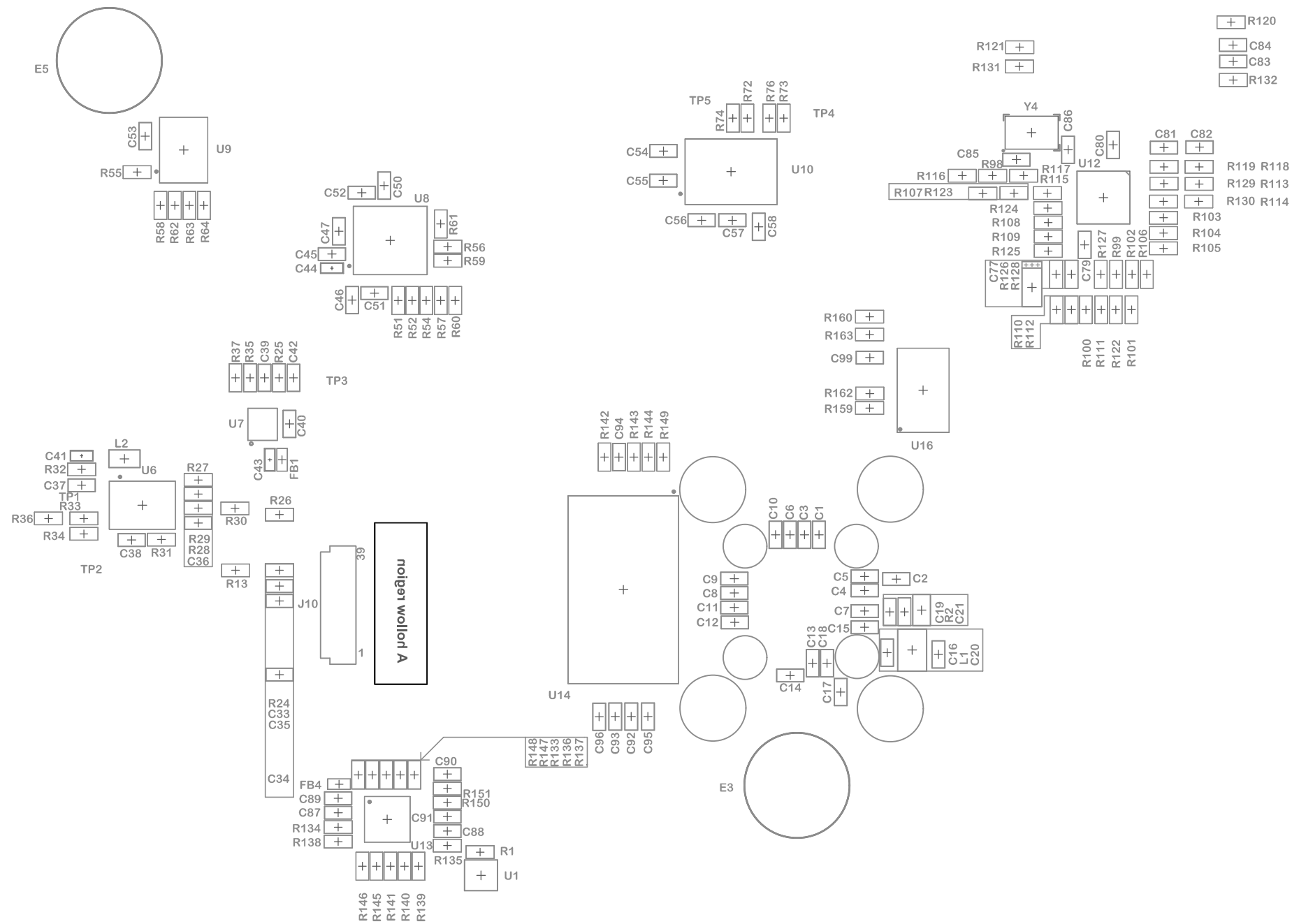
THE BOARD MUST BE RoHS COMPLIANT

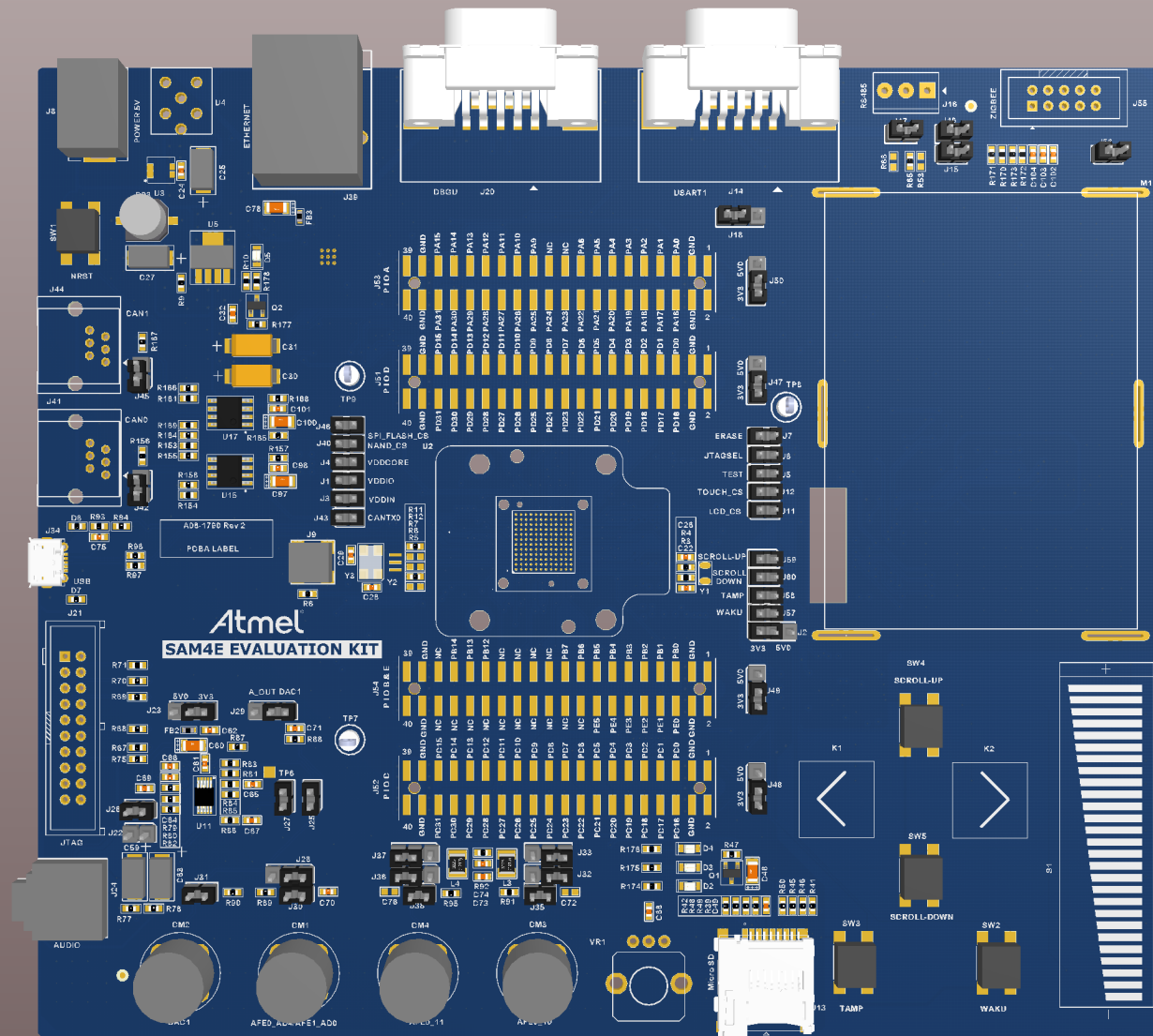
DETAIL A (CROSS-SECTION)

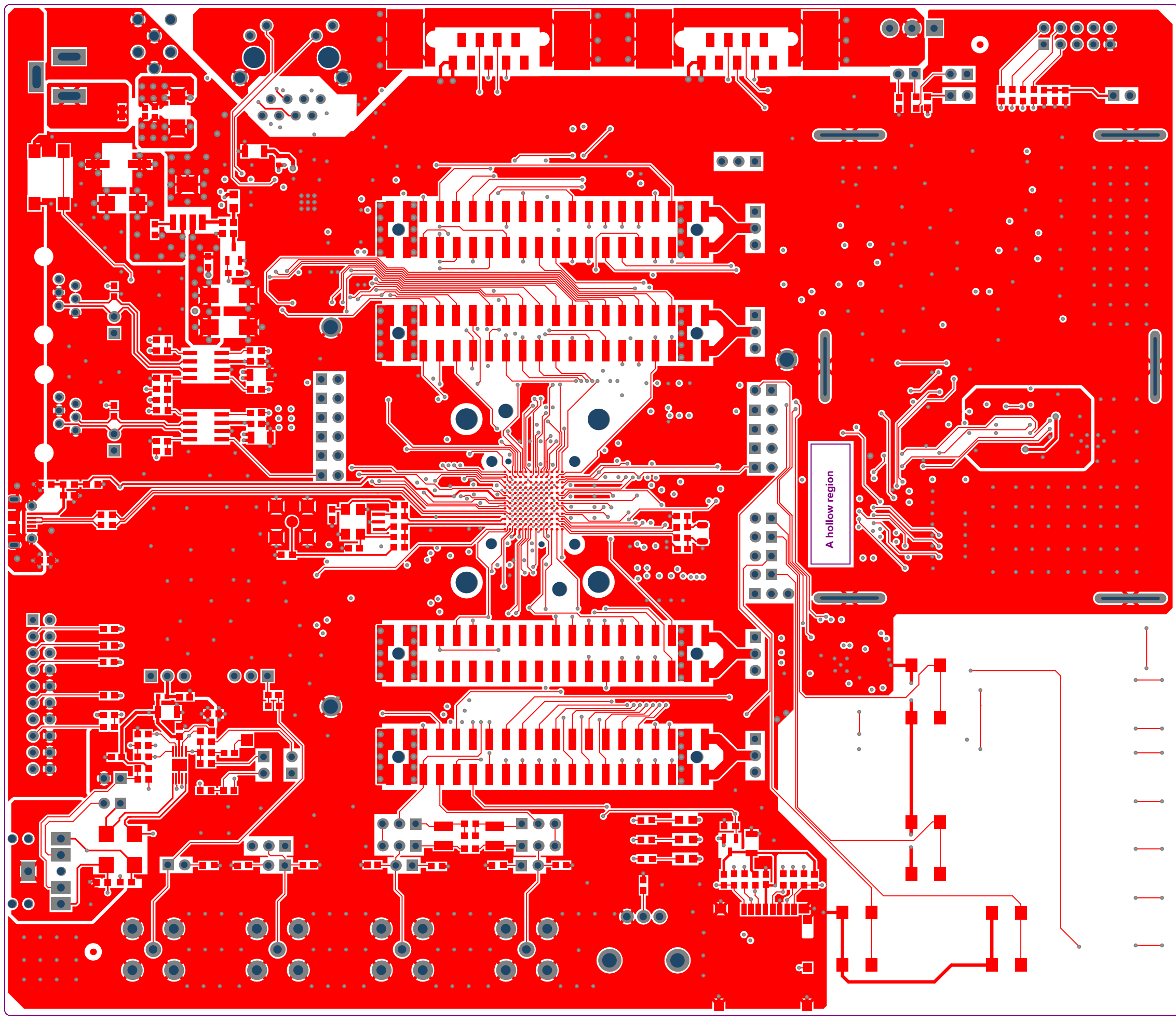
SCALE = NONE

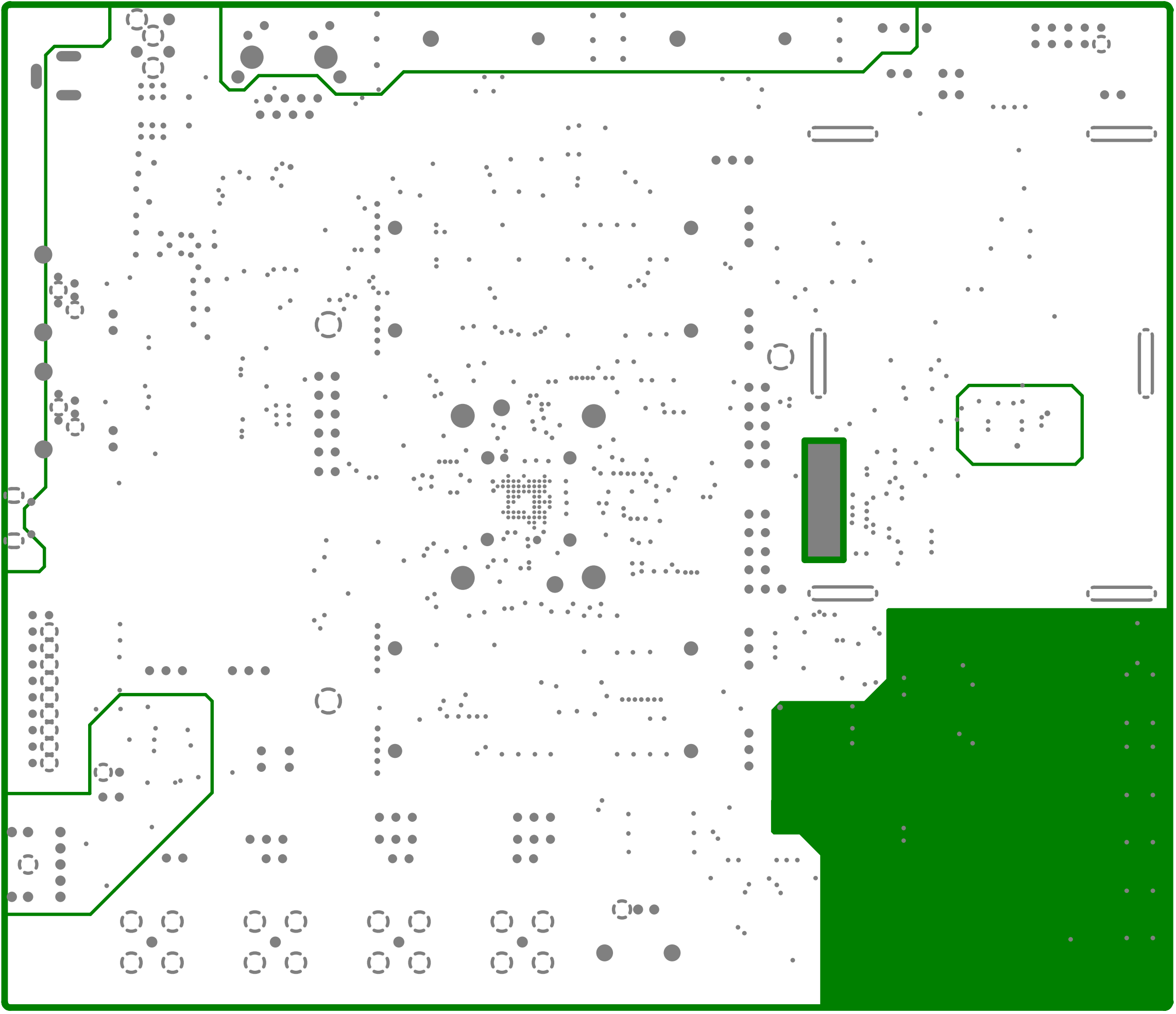


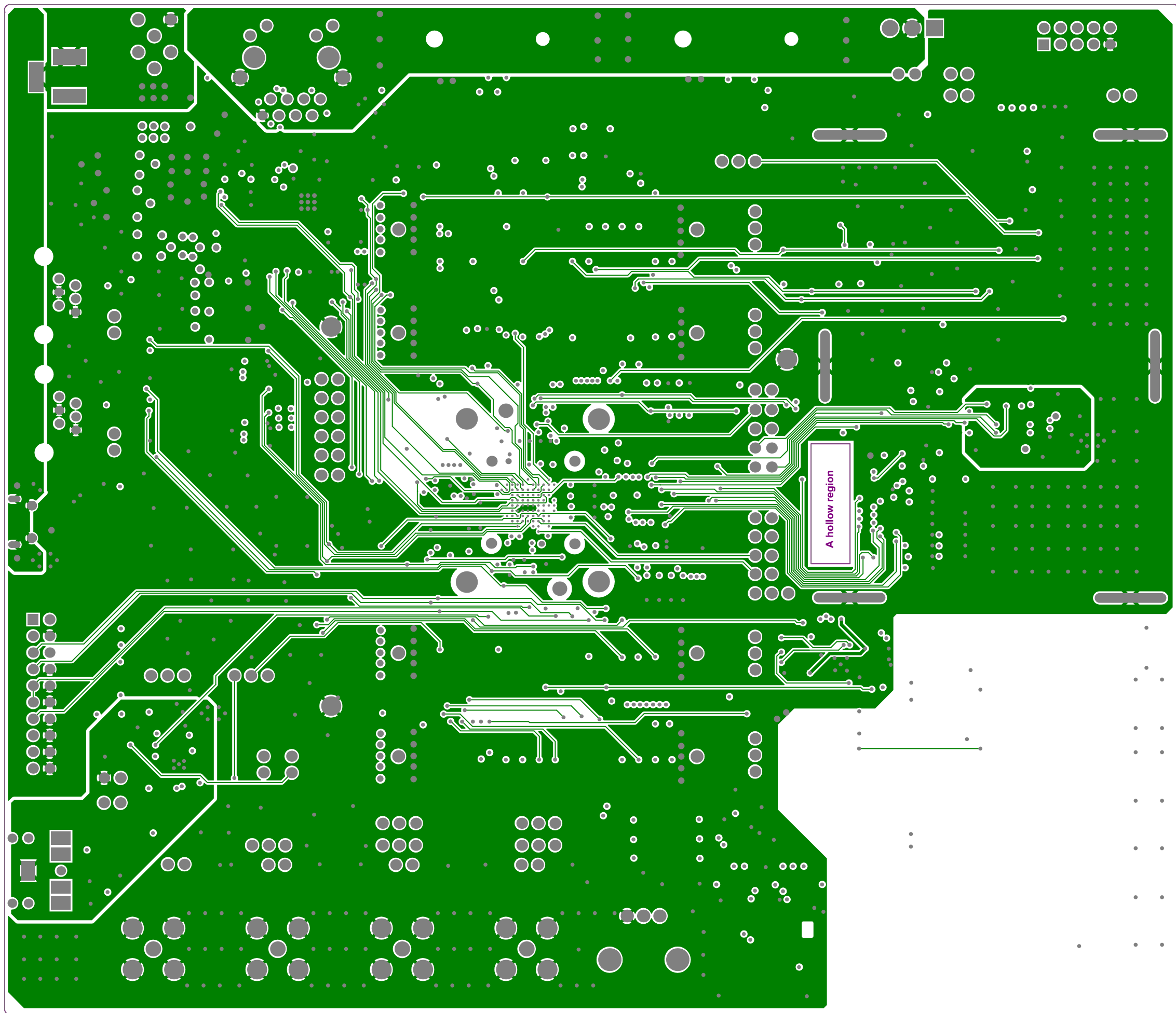


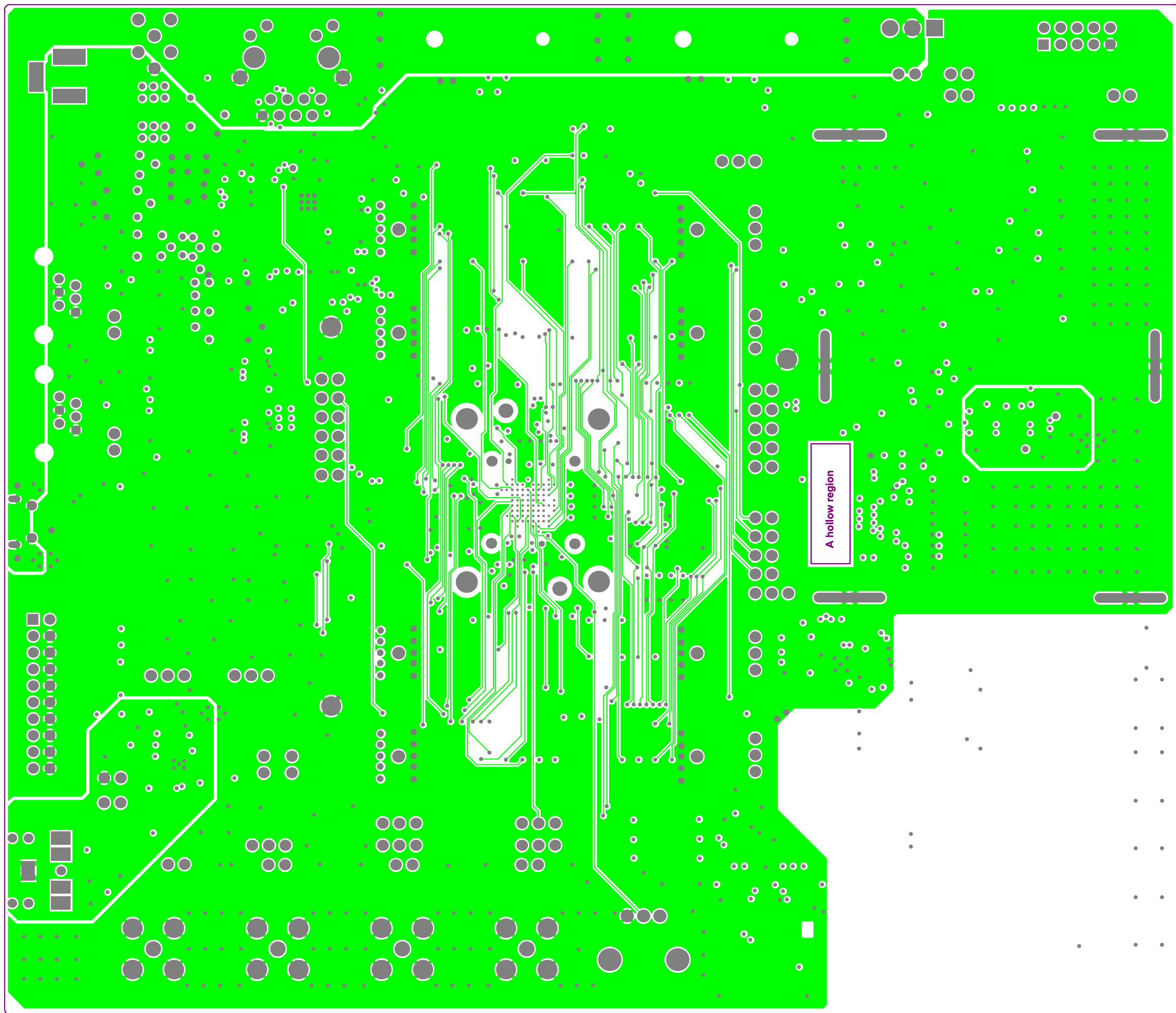


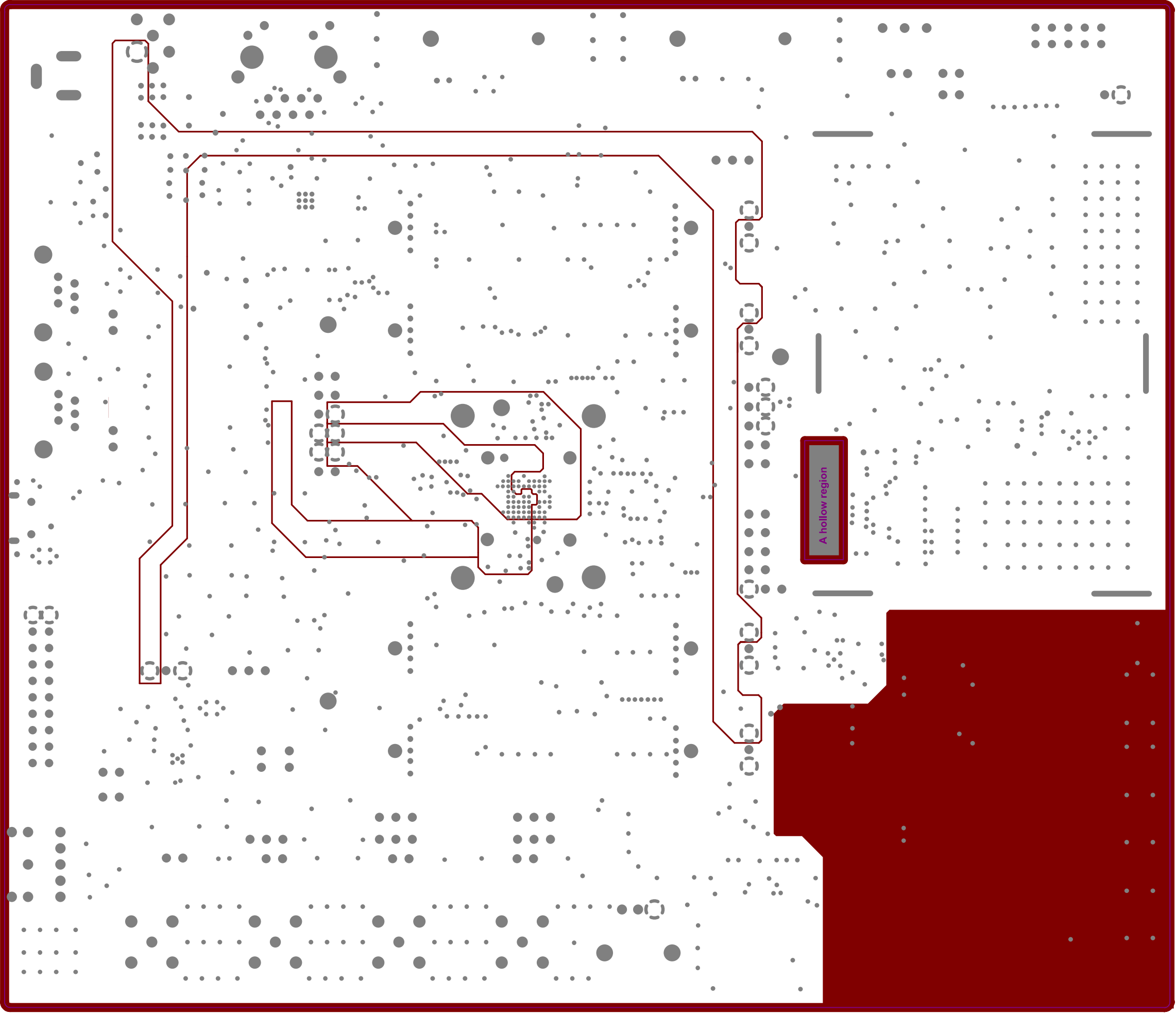


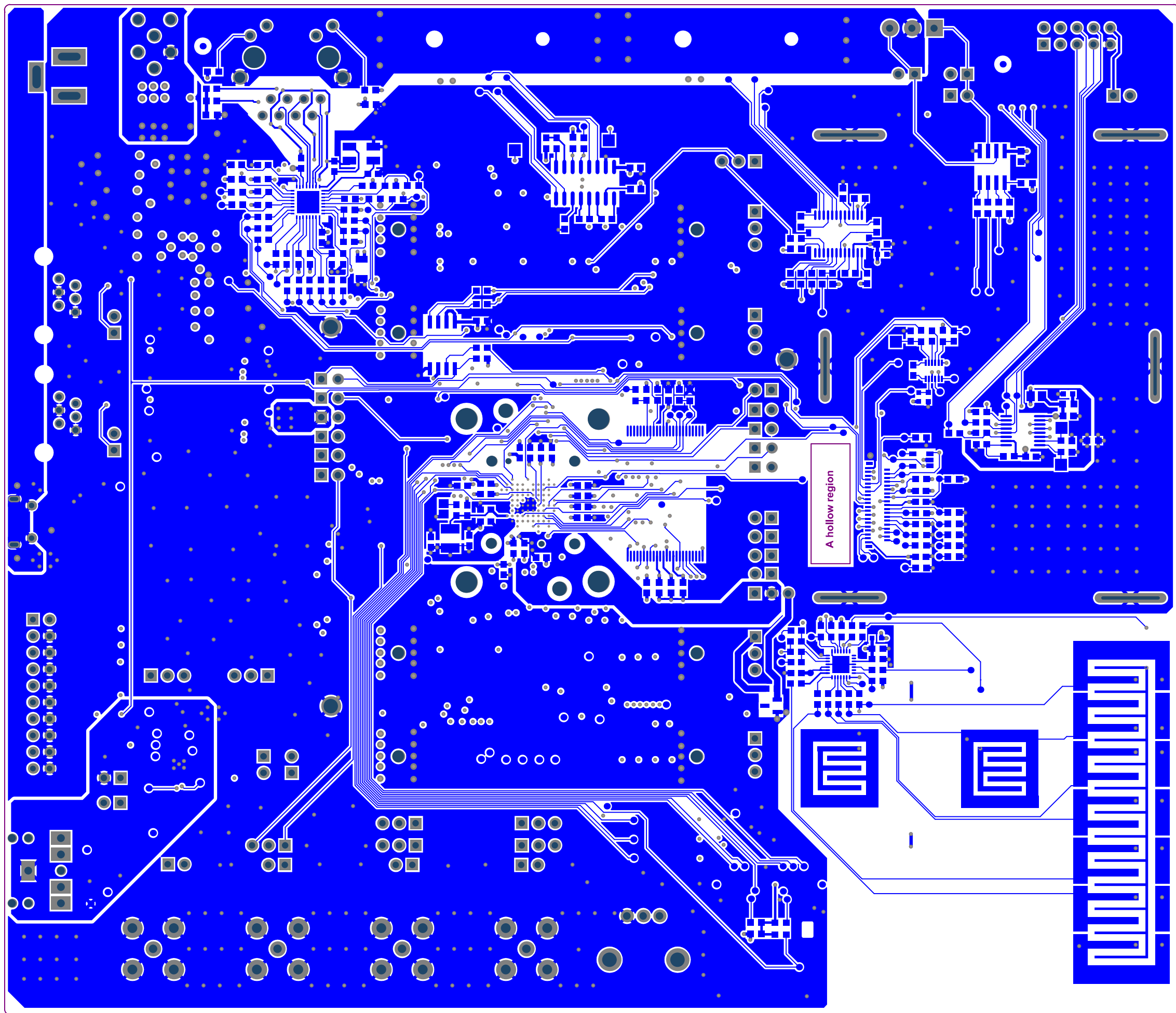












Bill of Materials Print For Variant [Variant of SAM4E-EK_Rev2] of Project [SAM4E-EK_Rev3.PriPcb] (No PCB Document Selected)

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[illegible]