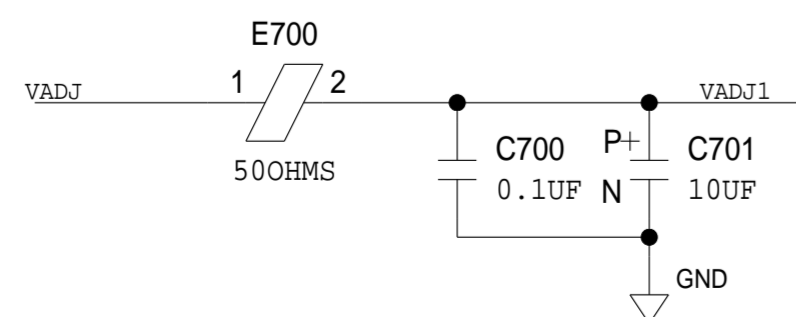



| REVISIONS | | | |
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| AD9671 CUST EVAL PCB | | | |
| AD9671 | | | |
| DESIGN VIEW | | DRAWING NO. | |
| - | | 9671CE01 | |
| PTD ENGINEER | | SIZE | SCALE |
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|  ANALOG DEVICES | SCHEMATIC | | | |
| | AD9671 CUST EVAL PCB AD9671 - | | | |
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| | PTD ENGINEER G. KELSO | SIZE NONE | SHEET 7 | OF 7 |