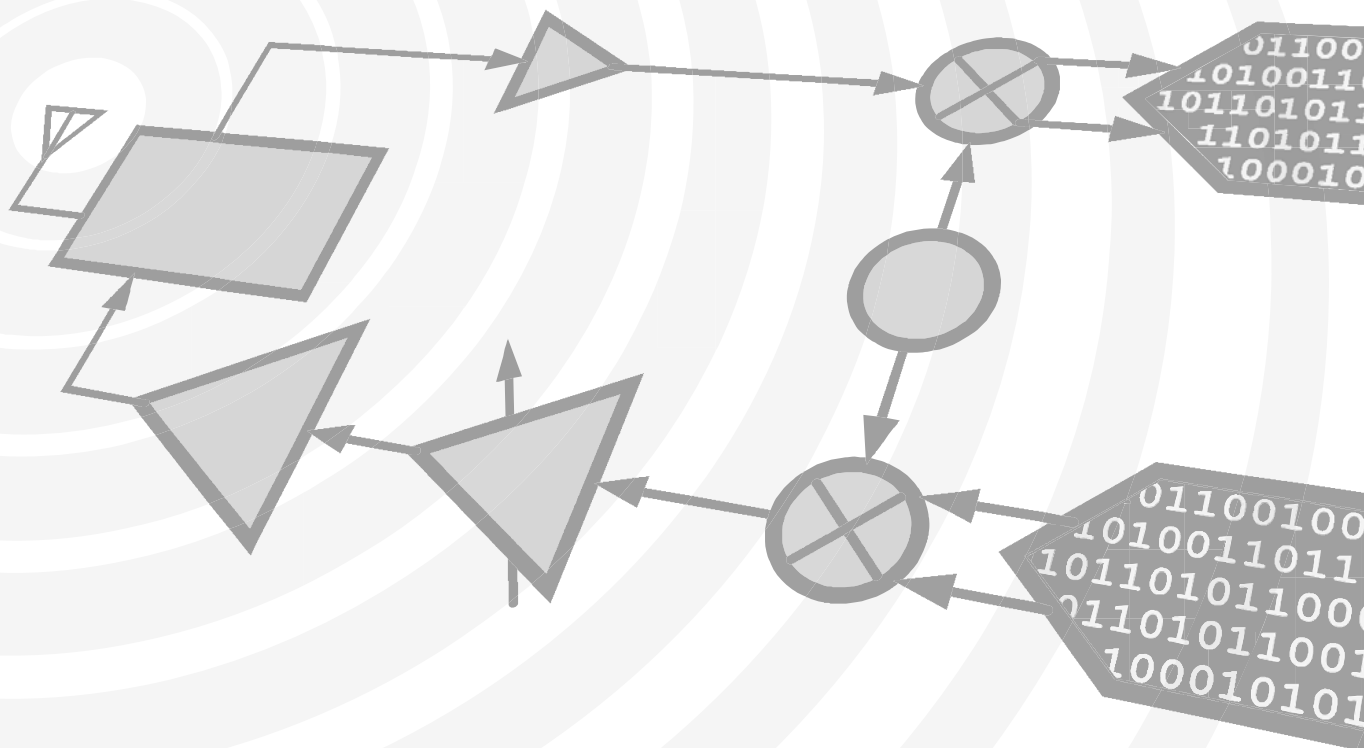




NO CONTENT ON THE ATTACHED DOCUMENT HAS CHANGED



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DIVIDER CONTROL	D1	D0
POWER DOWN	0	0
DIVIDE BY 1	0	1
DIVIDE BY 5	1	0
DIVIDE BY 10	1	1

REF IN

50 Ohm Trace

50 Ohm Co-Planar Trace

VCXO

50 Ohm Co-Planar Trace

V IO

LOCK
DETECT

+5.5V

GND

A RECOMMENDED DESIGN PRACTICE IS TO CONNECT THE REGULATOR ENABLE PIN # 3 THROUGH A 4.7 KOHM RES. TO THE SYSYEM MICROCONTROLLER/FPGA FOR POWER MANAGEMENT CONTROL. SMALL SERIES RESISTORS ARE REQUIRED IN REGULATOR OUTPUT PATHS AS SHOWN



HITTITE MICROWAVE CORPORATION
20 Alpha Rd Chelmsford, MA 01824

TITLE

SCH, EVAL CUSTOMER

PROJECT

DRAWING #:

130-00451-00

DRAWN BY

D. ACEVAL

DATE

11/18/10

SHEET

2

OF

2

CODE ID NO.

1CN88

SIZE

C

REV

C

12-10-2012_15:00