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FAIRCHILD SEMICONDUCTOR®

September 2008

NC7SP38
TinyLogic® ULP 2-Input NAND Gate (Open Drain Output)

Features

- 0.9V to 3.6V V_{CC} supply operation
- 3.6V overvoltage tolerant I/O's at V_{CC} from 0.9V to 3.6V
- t_{PD}:
  - 3.0ns typ. for 3.0V to 3.6V V_{CC}
  - 4.0ns typ. for 2.3V to 2.7V V_{CC}
  - 5.0ns typ. for 1.65V to 1.95V V_{CC}
  - 6.0ns typ. for 1.40V to 1.60V V_{CC}
  - 9.0ns typ. for 1.10V to 1.30V V_{CC}
  - 24.0ns typ. for 0.90V V_{CC}
- Power-off high impedance inputs and outputs
- Static drive (I_{OH}/I_{OL}):
  - ±2.6mA @ 3.00V V_{CC}
  - ±2.1mA @ 2.30V V_{CC}
  - ±1.5mA @ 1.65V V_{CC}
  - ±1.0mA @ 1.40V V_{CC}
  - ±0.5mA @ 1.10V V_{CC}
  - ±20µA @ 0.9V V_{CC}
- Uses patented Quiet Series™ noise/EMI reduction circuitry
- Ultra small MicroPak™ package
- Ultra low dynamic power

General Description

The NC7SP38 is a single 2-Input NAND Gate with open drain output stage from Fairchild's Ultra Low Power (ULP) Series of TinyLogic®. Ideal for applications where battery life is critical, this product is designed for ultra low power consumption within the V_{CC} operating range of 0.9V to 3.6V V_{CC}.

The internal circuit is composed of a minimum of inverter stages including the output buffer, to enable ultra low static and dynamic power.

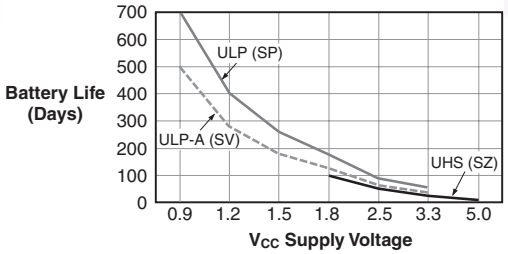
The NC7SP38, for lower drive requirements, is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve best in class speed operation while maintaining extremely low CMOS power dissipation.

Ordering Information

Order Number	Package Number	Package Code Top Mark	Package Description	Supplied As
NC7SP38P5X	MAA05A	P38	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel
NC7SP38L6X	MAC06A	K7	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

All packages are lead free per JEDEC: J-STD-020B standard.

Battery Life vs. V_{CC} Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly.

Battery Life = ($V_{battery}$ x $I_{battery}$ x 0.9) / (P_{device}) / 24hrs/day

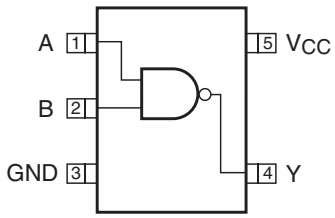
Where, P_{device} = (I_{CC} x V_{CC}) + (C_{PD} + C_L) x V_{CC}^2 x f

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with C_L = 15pF load.

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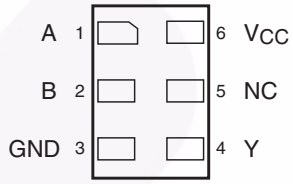
Connection Diagrams

Pin Assignment for SC70



(Top View)

Pad Assignments for MicroPak



(Top Through View)

Pin Description

Pin Names	Description
A, B	Input
Y	Output
NC	No Connect

Logic Symbol

IEEE/IEC



Function Table

$Y = \overline{AB}$

Inputs		Output
A	**B**	**Y**
L	L	*H
L	H	*H
H	L	*H
H	H	L

H = HIGH Logic Level

L = LOW Logic Level

*H = HIGH Impedance Output State (Open Drain)

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Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V_{CC}	Supply Voltage	–0.5V to +4.6V
V_{IN}	DC Input Voltage	–0.5V to +4.6V
V_{OUT}	DC Output Voltage HIGH or LOW State(1) V_{CC} = 0V	 –0.5V to V_{CC} +0.5V –0.5V to +4.6V
I_{IK}	DC Input Diode Current @ V_{IN} < 0V	–50mA
I_{OK}	DC Output Diode Current V_{OUT} < 0V V_{OUT} > V_{CC}	 –50mA +50mA
I_{OH} / I_{OL}	DC Output Source/Sink Current	±50mA
I_{CC} or Ground	DC V_{CC} or Ground Current per Supply Pin	±50mA
T_{STG}	Storage Temperature Range	–65°C to +150°C
T_J	Junction Temperature Under Bias	150°C
T_L	Junction Lead Temperature (Soldering, 10 seconds)	260°C
P_D	Power Dissipation @ +85°C SC70-5 Micropak-6	 150mW 130mW

Recommended Operating Conditions(2)

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating
V_{CC}	Supply Voltage	0.9V to 3.6V
V_{IN}	Input Voltage	0V to 3.6V
V_{OUT}	Output Voltage HIGH or LOW State V_{CC} = 0V	 0V to V_{CC} 0V to 3.6V
I_{OH} / I_{OL}	Output Current in I_{OH} / I_{OL} V_{CC} = 3.0V to 3.6V V_{CC} = 2.3V to 2.7V V_{CC} = 1.65V to 1.95V V_{CC} = 1.40V to 1.60V V_{CC} = 1.10V to 1.30V V_{CC} = 0.9V	 ±2.6mA ±2.1mA ±1.5mA ±1mA ±0.5mA ±20µA
T_A	Free Air Operating Temperature	–40°C to +85°C
$\Delta t / \Delta V$	Minimum Input Edge Rate @ V_{IN} = 0.8V to 2.0V, V_{CC} = 3.0V	10ns/V
θ_{JA}	Thermal Resistance SC70-5 Micropak-6	 425°C/W 500°C/W

Notes:

1. I_O Absolute Maximum Rating must be observed.
2. Unused inputs must be held HIGH or LOW. They may not float.

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DC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)	Conditions	T_A = +25°C Min.	T_A = +25°C Max.	T_A = –40°C to +85°C Min.	T_A = –40°C to +85°C Max.	Units
V_{IH}	HIGH Level Input Voltage	0.90		0.65 x V_{CC}		0.65 x V_{CC}		V
		1.10 ≤ V_{CC} ≤ 1.30		0.65 x V_{CC}		0.65 x V_{CC}		
		1.40 ≤ V_{CC} ≤ 1.60		0.65 x V_{CC}		0.65 x V_{CC}		
		1.65 ≤ V_{CC} ≤ 1.95		0.65 x V_{CC}		0.65 x V_{CC}		
		2.30 ≤ V_{CC} ≤ 2.70		1.6		1.6		
		3.00 ≤ V_{CC} ≤ 3.60		2.1		2.1		
V_{IL}	LOW Level Input Voltage	0.90			0.35 x V_{CC}		0.35 x V_{CC}	V
		1.10 ≤ V_{CC} ≤ 1.30			0.35 x V_{CC}		0.35 x V_{CC}	
		1.40 ≤ V_{CC} ≤ 1.60			0.35 x V_{CC}		0.35 x V_{CC}	
		1.65 ≤ V_{CC} ≤ 1.95			0.35 x V_{CC}		0.35 x V_{CC}	
		2.30 ≤ V_{CC} ≤ 2.70			0.7		0.7	
		3.00 ≤ V_{CC} ≤ 3.60			0.9		0.9	
V_{OL}	LOW Level Output Voltage	0.90	I_{OL} = 20µA		0.1		0.1	V
		1.10 ≤ V_{CC} ≤ 1.30			0.1		0.1	
		1.40 ≤ V_{CC} ≤ 1.60			0.1		0.1	
		1.65 ≤ V_{CC} ≤ 1.95			0.1		0.1	
		2.30 ≤ V_{CC} ≤ 2.70			0.1		0.1	
		3.00 ≤ V_{CC} ≤ 3.60			0.1		0.1	
		1.10 ≤ V_{CC} ≤ 1.30	I_{OL} = 0.5mA		0.30 x V_{CC}		0.30 x V_{CC}	
		1.40 ≤ V_{CC} ≤ 1.60	I_{OL} = 1mA		0.31		0.37	
		1.65 ≤ V_{CC} ≤ 1.95	I_{OL} = 1.5mA		0.31		0.35	
		2.30 ≤ V_{CC} ≤ 2.70	I_{OL} = 2.1mA		0.31		0.33	
		3.00 ≤ V_{CC} ≤ 3.60	I_{OL} = 2.6mA		0.31		0.33	
I_{IN}	Input Leakage Current	0.90 to 3.60	0 ≤ V_I ≤ 3.6V		±0.1		±0.5	µA
I_{OFF}	Power Off Leakage Current	0	0 ≤ (V_I, V_O) ≤ 3.6V		0.5		0.5	µA
I_{CC}	Quiescent Supply Current	0.90 to 3.60	V_I = V_{CC} or GND		0.9		0.9	µA

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AC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)	Conditions	T_A = +25°C			T_A = –40°C to +85°C		Units	Figure Number
				Min.	Typ.	Max.	Min.	Max.		
t_{PZL}, t_{PLZ}	Propagation Delay	0.9	C_L = 10pF, R_U = 5,000Ω, R_D = 5,000Ω		24				ns	Figure 1 Figure 2
		1.10 ≤ V_{CC} ≤ 1.30		4.0	9	18.7	3.5	30.9		
		1.40 ≤ V_{CC} ≤ 1.60		2.0	6	12.4	1.5	13.9		
		1.65 ≤ V_{CC} ≤ 1.95		1.5	5	9.6	1.0	12.1		
		2.30 ≤ V_{CC} ≤ 2.70		1.0	4	9.0	0.8	10.0		
		3.00 ≤ V_{CC} ≤ 3.60		1.0	3	8.7	0.5	9.0		
		0.90	C_L = 15pF, R_U = 5,000Ω, R_D = 5,000Ω		27				ns	Figure 1 Figure 2
		1.10 ≤ V_{CC} ≤ 1.30		5.0	10	20.0	4.5	33.9		
		1.40 ≤ V_{CC} ≤ 1.60		3.0	7	13.3	2.5	16.0		
		1.65 ≤ V_{CC} ≤ 1.95		2.0	5	10.3	2.0	12.6		
		2.30 ≤ V_{CC} ≤ 2.70		1.5	4	9.4	1.0	10.2		
		3.00 ≤ V_{CC} ≤ 3.60		1.0	3	9.1	0.5	9.7		
		0.90	C_L = 30pF, R_U = 5,000Ω, R_D = 5,000Ω		34				ns	Figure 1 Figure 2
		1.10 ≤ V_{CC} ≤ 1.30		6.0	12	24.0	5.0	43.0		
		1.40 ≤ V_{CC} ≤ 1.60		4.0	8	16.0	3.0	18.0		
		1.65 ≤ V_{CC} ≤ 1.95		2.0	6	12.0	2.0	14.0		
		2.30 ≤ V_{CC} ≤ 2.70		1.0	5	11.0	1.0	12.0		
		3.00 ≤ V_{CC} ≤ 3.60		0.8	4	10.0	0.5	11.0		
C_{IN}	Input Capacitance	0			2.0				pF	
C_{OUT}	Output Capacitance	0			4.0				pF	
C_{PD}	Power Dissipation Capacitance	0.9 to 3.60	V_I = 0V or V_{CC}, f = 10MHz		6				pF	

AC Loading and Waveforms

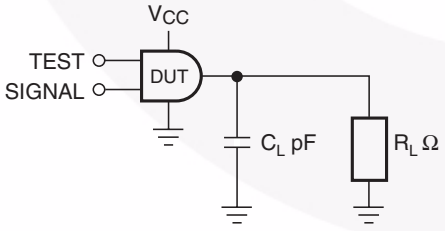


Figure 1. AC Test Circuit

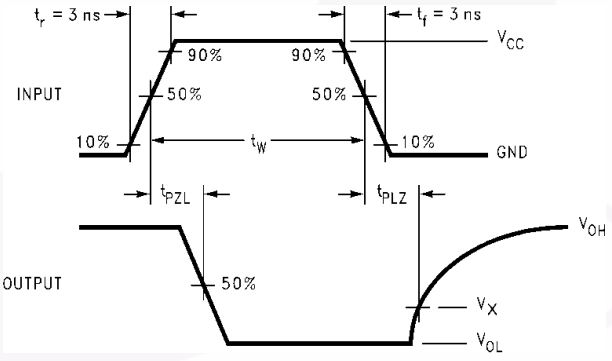


Figure 2. AC Waveforms

Symbol	V_{CC}					
	3.3V ± 0.3V	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V	0.9V
V_{mi}	1.5V	V_{CC}/2	V_{CC}/2	V_{CC}/2	V_{CC}/2	V_{CC}/2
V_x	V_{OL} + 0.3V	V_{OL} + 0.15V	V_{OL} + 0.15V	V_{OL} + 0.1V	V_{OL} + 0.1V	V_{OL} + 0.1V

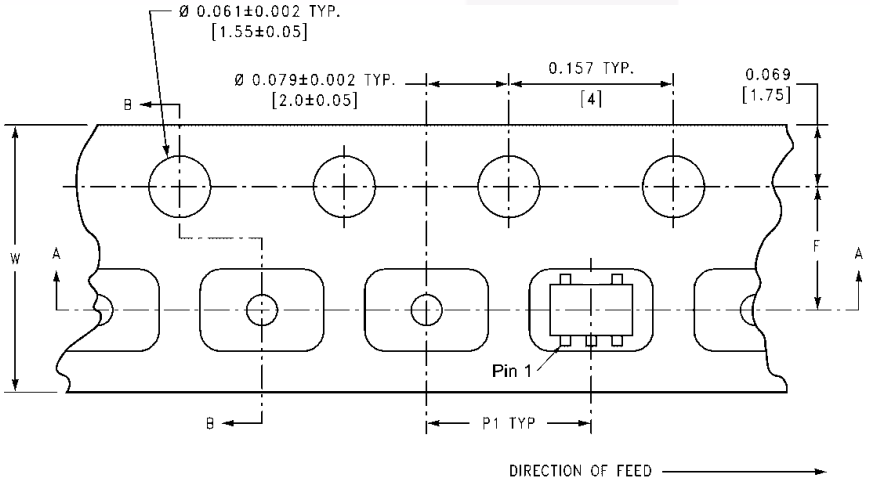
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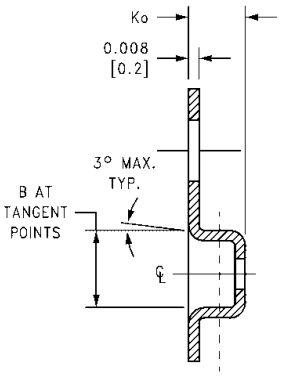
Tape and Reel Specification

Tape Format for SC70

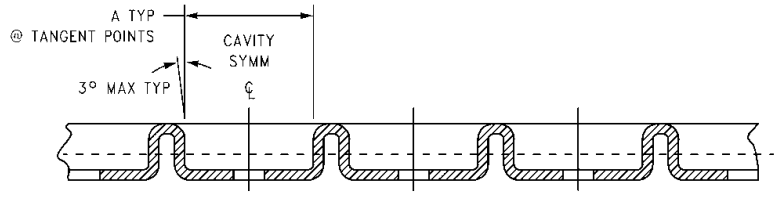
Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
P5X	Leader (Start End)	125 (typ.)	Empty	Sealed
	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ.)	Empty	Sealed

Tape Dimension inches (millimeters)

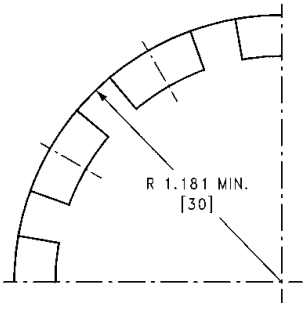




SECTION B-B



SECTION A-A



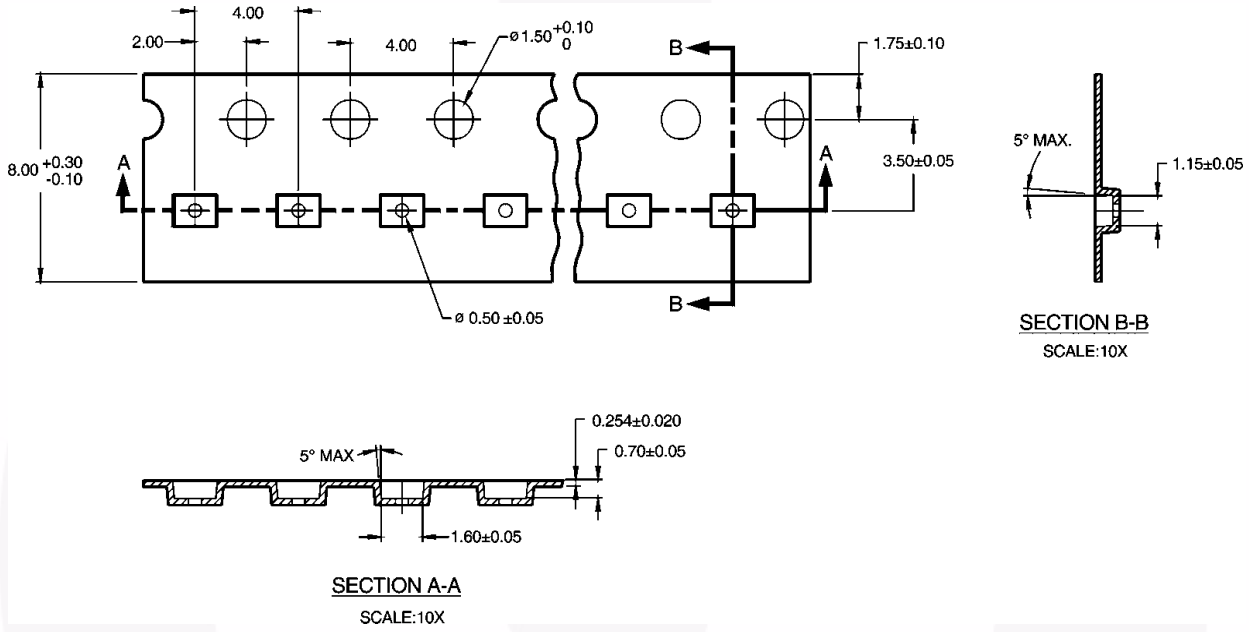
BEND RADIUS NOT TO SCALE

Tape and Reel Specification (Continued)

Tape Format for MicroPak

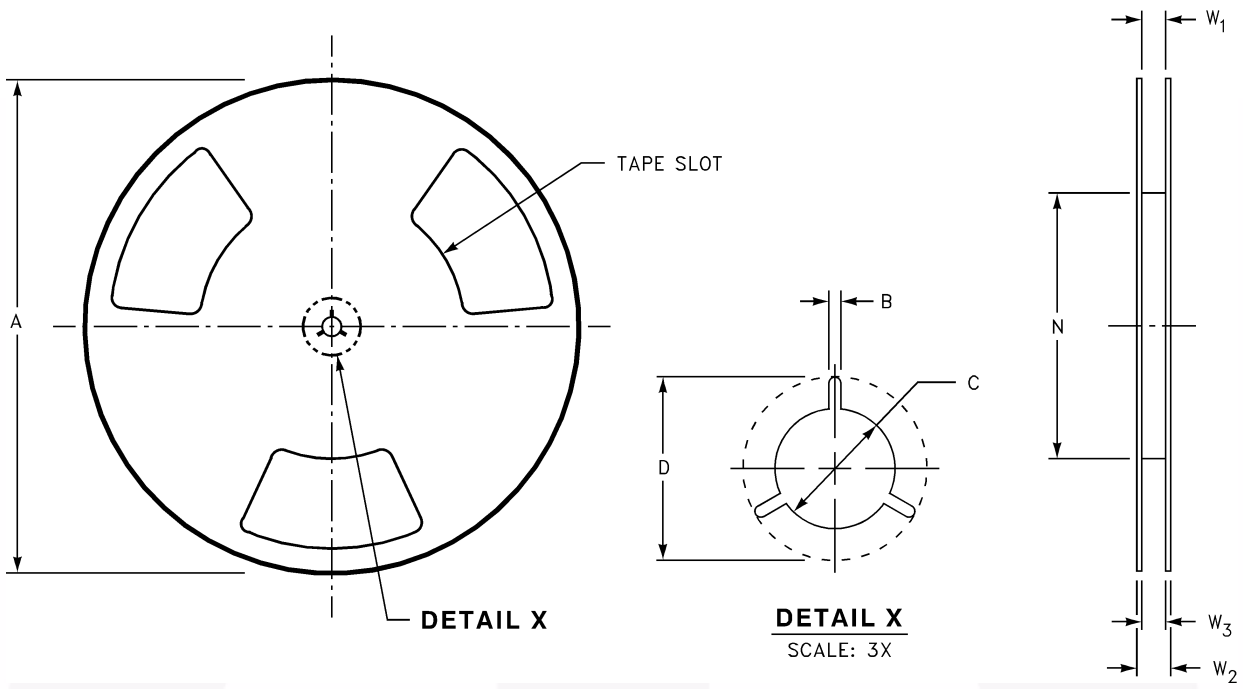
Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
L6X	Leader (Start End)	125 (typ.)	Empty	Sealed
	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (typ.)	Empty	Sealed

Tape Dimension millimeters



Tape and Reel Specification (Continued)

Reel Dimension for MicroPak inches (millimeters)



Tape Size	A	B	C	D	N	W_1	W_2	W_3
8mm	7.0 (177.8)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.331 +0.059/–0.000 (8.40 +1.50/–0.00)	0.567 (14.40)	W1 +0.078/–0.039 (W1 +2.00/–1.00)

Physical Dimensions

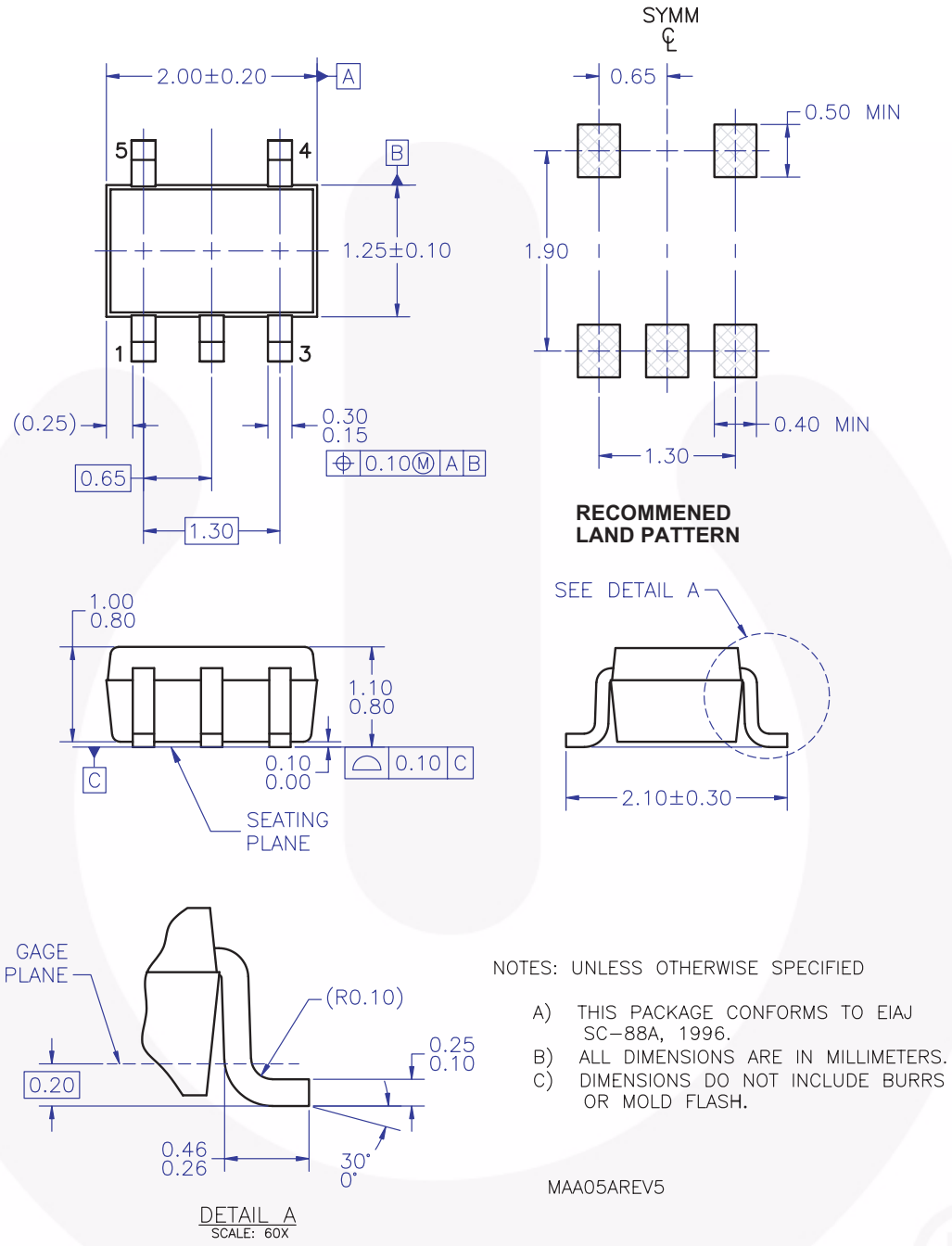
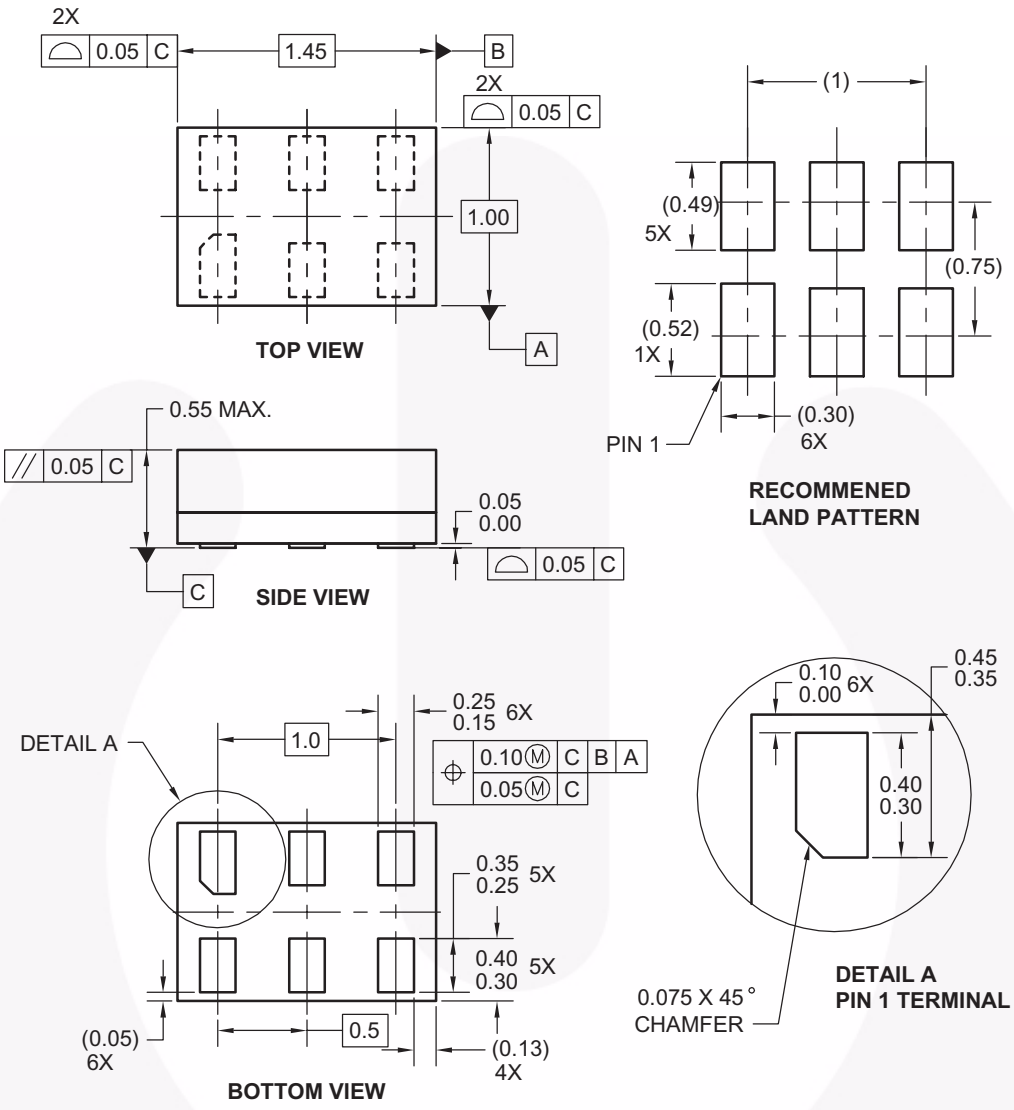


Figure 3. 5-Lead SC70, EIAJ SC-88a, 1.25mm Wide

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Physical Dimensions (Continued)



Notes:
1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

Figure 4. 6-Lead MicroPak, 1.0mm Wide

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Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

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Definition of Terms

Datasheet Identification	Product Status	Definition
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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