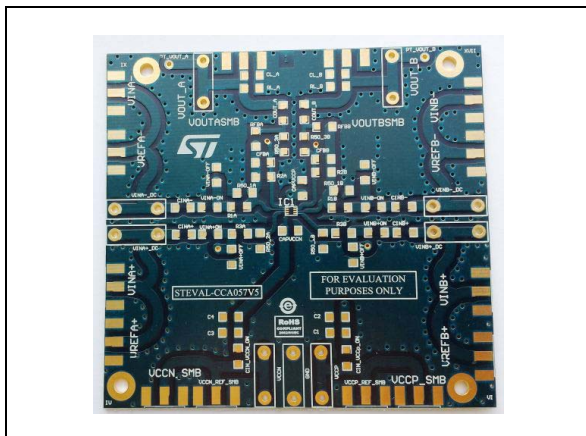


Bare PCB evaluation board for the dual operational amplifier family in a DFN8 package

Data brief



Description

The STEVAL-CCA057V5 evaluation board is designed to help customers quickly prototype new dual op amp circuits in a DFN8 package and reduce design time.

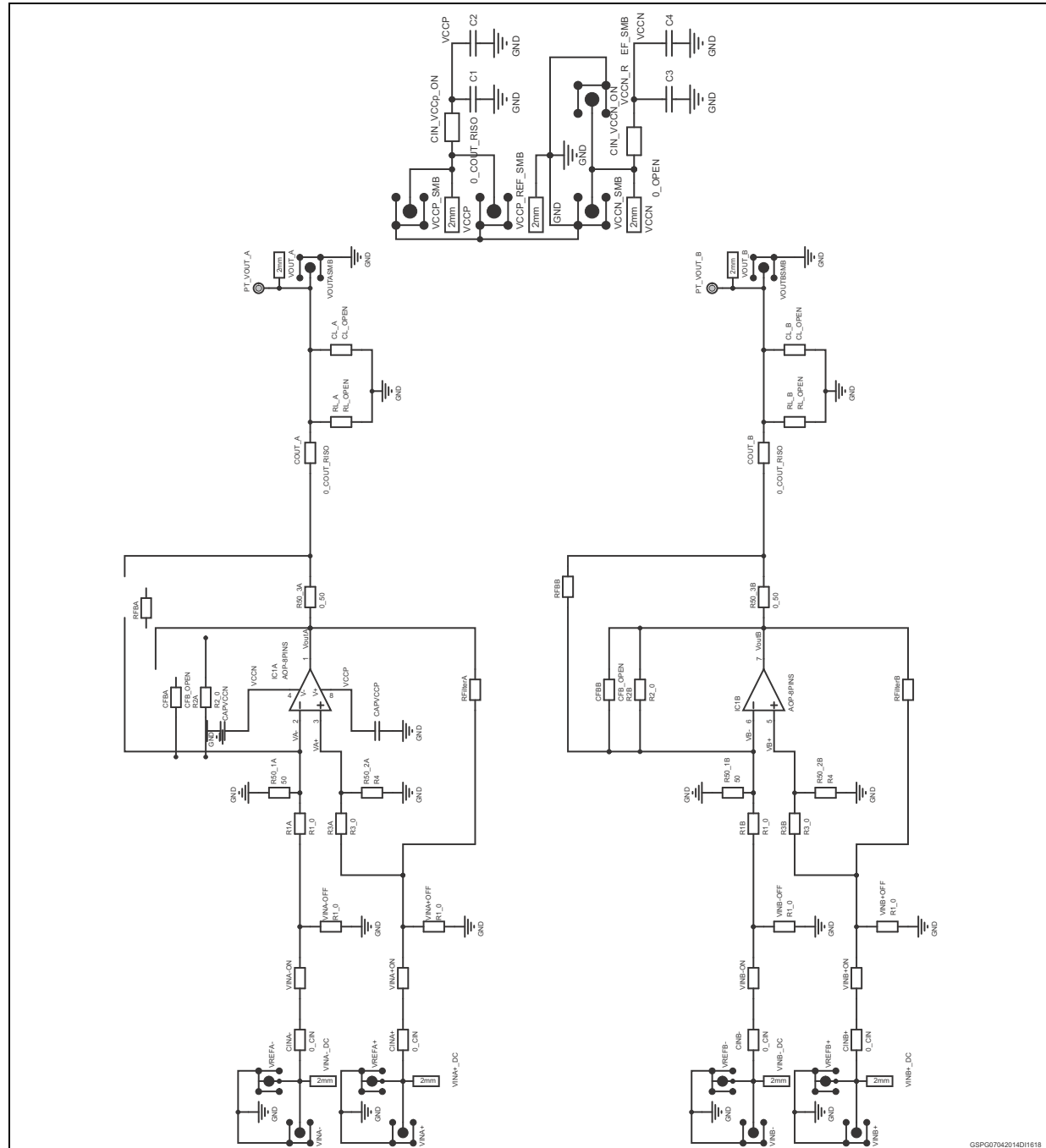
It can be used with almost any STMicroelectronics dual op amp in various configurations and applications.

The STEVAL-CCA057V5 is a bare board. There are no components or amplifiers soldered on the board. The components must be ordered separately.

Features

- The board with its components allow it to be configured as:
 - Sallen-key filter
 - Instrument amplifier
 - AC-coupled circuit
 - Out-of-loop compensation circuit
 - In loop compensation circuit
 - Transimpedance amplifier
 - Gain amplifier
 - Numerous other possible configurations
- RoHS compliant

Figure 1. STEVAL-CCA057V5 circuit schematic



2 Revision history

Table 1. Document revision history

Date	Revision	Changes
07-Aug-2014	1	Initial release.

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