Americas
Atlanta - 678-957-9614
Austin - 512-257-3370
Boston - 774-760-0087
Chicago - 630-285-0071
Cleveland - 216-447-0464
Dallas - 972-818-7423
Detroit - 248-848-4000
Houston - 281-894-5983
Indianapolis - 317-773-8323
Los Angeles - 949-462-9523
New York - 631-435-6000
Phoenix - 480-792-7200
San Jose - 408-735-9110
Toronto - 905-673-0699

Europe
Austria - Wels - 43-7242-2244-39
Denmark - Copenhagen - 45-4450-2828
France - Paris - 33-1-69-53-63-20
Germany - Dusseldorf - 49-2129-3766400
Germany - Munich - 49-89-627-144-0
Germany - Pforzheim - 49-7231-424750
Italy - Milan - 39-0331-742611
Italy - Venice - 39-049-7625286
Netherlands - Drunen - 31-416-690399
Poland - Warsaw - 48-22-3325737
Spain - Madrid - 34-91-708-08-90
Sweden - Stockholm - 46-8-5090-4654
UK - Wokingham - 44-118-921-5800

Asia/Pacific
Australia - Sydney - 61-2-9868-6733
China - Beijing - 86-10-8569-7000
China - Chengdu - 86-28-8665-5511
China - Chongqing - 86-23-8980-9588
China - Dongguan - 86-769-8702-9880
China - Hangzhou - 86-571-8792-8115
China - Hong Kong SAR - 852-2943-5100
China - Nanjing- 86-25-8473-2460
China - Qingdao - 86-532-8502-7355
China - Shanghai - 86-21-5407-5533
China - Shenyang - 86-24-2334-2829
China - Shenzhen - 86-755-8864-2200
China - Wuhan - 86-27-5980-5300
China - Xiamen - 86-592-2388138
China - Xian - 86-29-8833-7252
China - Zhuhai - 86-756-3210040
India - Bangalore - 91-80-3090-4444
India - New Delhi - 91-11-4160-8631
India - Pune - 91-20-3019-1500
Japan - Osaka - 81-6-6152-7160
Japan - Tokyo - 81-3-6880-3770
Korea - Daegu - 82-53-744-4301
Korea - Seoul - 82-2-554-7200
Malaysia - Kuala Lumpur - 60-3-6201-9857
Malaysia - Penang - 60-4-227-8870
Philippines - Manila - 63-2-634-9065
Singapore - 65-6334-8870
Taiwan - Hsin Chu - 886-3-5778-366
Taiwan - Kaohsiung - 886-7-213-7828
Taiwan - Taipei - 886-2-2508-8600
Thailand - Bangkok - 66-2-694-1351

01/27/15



Microchip Technology Inc. • 2355 West Chandler Blvd. • Chandler, AZ 85224-6199
www.microchip.com

The Microchip name and logo, the Microchip logo, and MPLAB are registered trademarks of Microchip Technology Inc. in the U.S.A. and other countries. PICtail is a trademark of Microchip Technology Inc. in the U.S.A. and other countries. All other trademarks mentioned herein are property of their respective companies. © 2015, Microchip Technology Incorporated, Printed in the U.S.A. All Rights Reserved. 6/15

DS50002360A

PIC24FJ256GB410 Plug-In Module (PIM) Information Sheet

Overview

The PIC24FJ256GB410 Plug-in Module is designed to demonstrate the capabilities of the PIC24FJ256GB410 family using the Explorer 16 and LCD Explorer Demonstration Boards. Most of the pins from the device are mapped directly to the PIM connector (100-Pin ICE). The exceptions are those pins that are remapped to provide remappable functionality to the pins in the PICtail™ Plus socket. Table 1 below shows the exceptions of the PIC24FJ256GB410 pins that are not directly connected to the ICE connector.

Pin Mapping

There are several device pins that don't have a one-to-one connection to the ICE connector. Some are hard-wired to a certain location, while others are mapped to the connector through jumpers.

Table 1 summarizes the device pins that don't have one-to-one connections.

Table 1: Pin Mapping Exceptions (No Direct Connections)

Pin Function	Device Pin	PIM Pin	Jumper
SEG12/RP16/USBID/RF3	51	95	N/A
VBUS/RF7	54	1	J2
SEG50/OCM1C/CTED3/RG15	1	1	J2
D-/RG3	56	89	J4
SEG46/PMD9/RG1	89	89	J4
D+/RG2	57	90	J5
SEG49/PMD8/RG0	90	90	J5
COM4/SEG47/SCK4/PMD10/RF1	88	14	J6
COM4/SEG47/SCK4/PMD10/RF1	88	88	J6
SEG1/AN21/C1INC/C2INC/C3INC/RP27/DAC1/PMA2/PMALU/RG9	14	51	J7
SEG1/AN21/C1INC/C2INC/C3INC/RP27/DAC1/PMA2/PMALU/RG9	14	14	J7
SEG59/CTED11/PMA16/RG14	95	N/C	N/A

Jumpers

Table 2 is the jumper table on the silkscreen of the PIM. This table uses schematic net names rather than the fill pin function.

Table 2: Jumpered Pins

Jumper	Pin 1	Pin 2	Pin 3
J2	SEG50/RG15	PIM_1	VBUS/RF7
J4	PMD9/RG1	PIM_89	D-/RG3
J5	PMD8/RG0	PIM_90	D+/RG2
J6	PIM_88	COM4/PMD10/RF1	PIM_14
J7	PIM_14	RG9	PIM_51

PIC24FJ256GB410 Plug-In Module (PIM) Information Sheet

Schematic Revision 1.1

