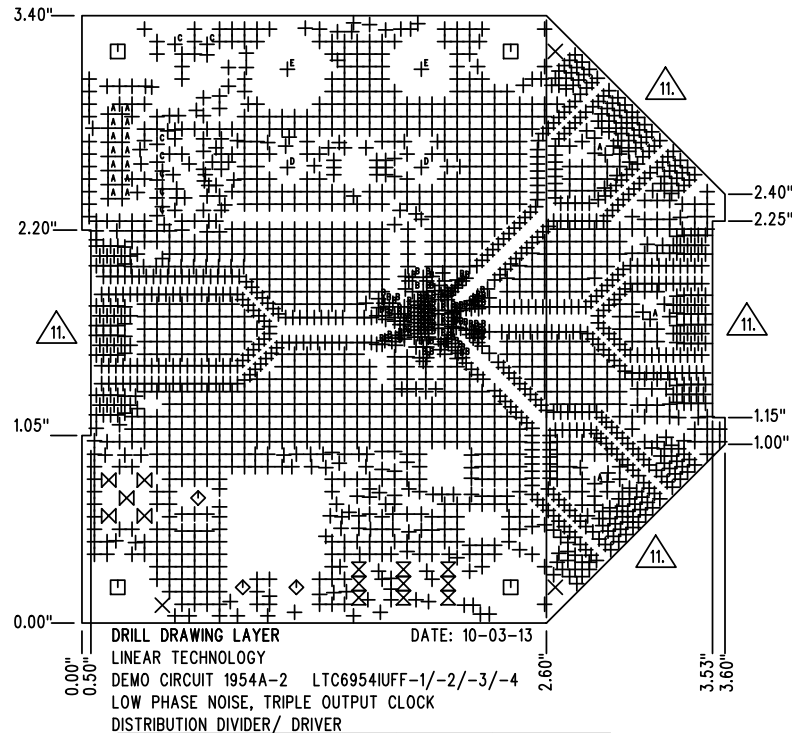
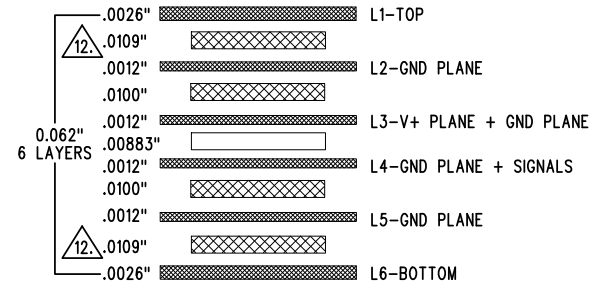


SHOWN FROM TOP SIDE



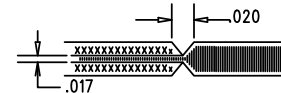
SIZE	QTY	SYM	PLATED	TOL
0.012	1926	+	YES	+/-0.003"
0.07	3	X	NO	+/-0.003"
0.19	4	□	NO	+/-0.003"
0.064	3	◇	YES	+/-0.003"
0.035	9	⊗	YES	+/-0.003"
0.055	5	⊗	YES	+/-0.003"
0.04	17	+ ^A	YES	+/-0.003"
0.006	120	+ ^B	YES	+/-0.003"
0.025	7	+ ^C	YES	+/-0.003"
0.094	2	+ ^D	YES	+/-0.003"
0.215	2	+ ^E	YES	+/-0.003"

LAYER STRUCTURE



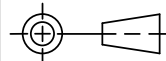
NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2
LAYER 6-5, USING 0.02" TRACE.
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES. KIM T.
APP ENG. MICHEL A.

SCALE = NONE

LINEAR TECHNOLOGY

1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
LOW PHASE NOISE, TRIPLE OUTPUT CLOCK
DISTRIBUTION DIVIDER/ DRIVER

SIZE N/A IC NO. LTC6954IUFF-1/-2/-3/-4 REV 2
DEMO CIRCUIT 1954A

FILENAME: DC1954A-2.PCB

SHT 1 OF 1