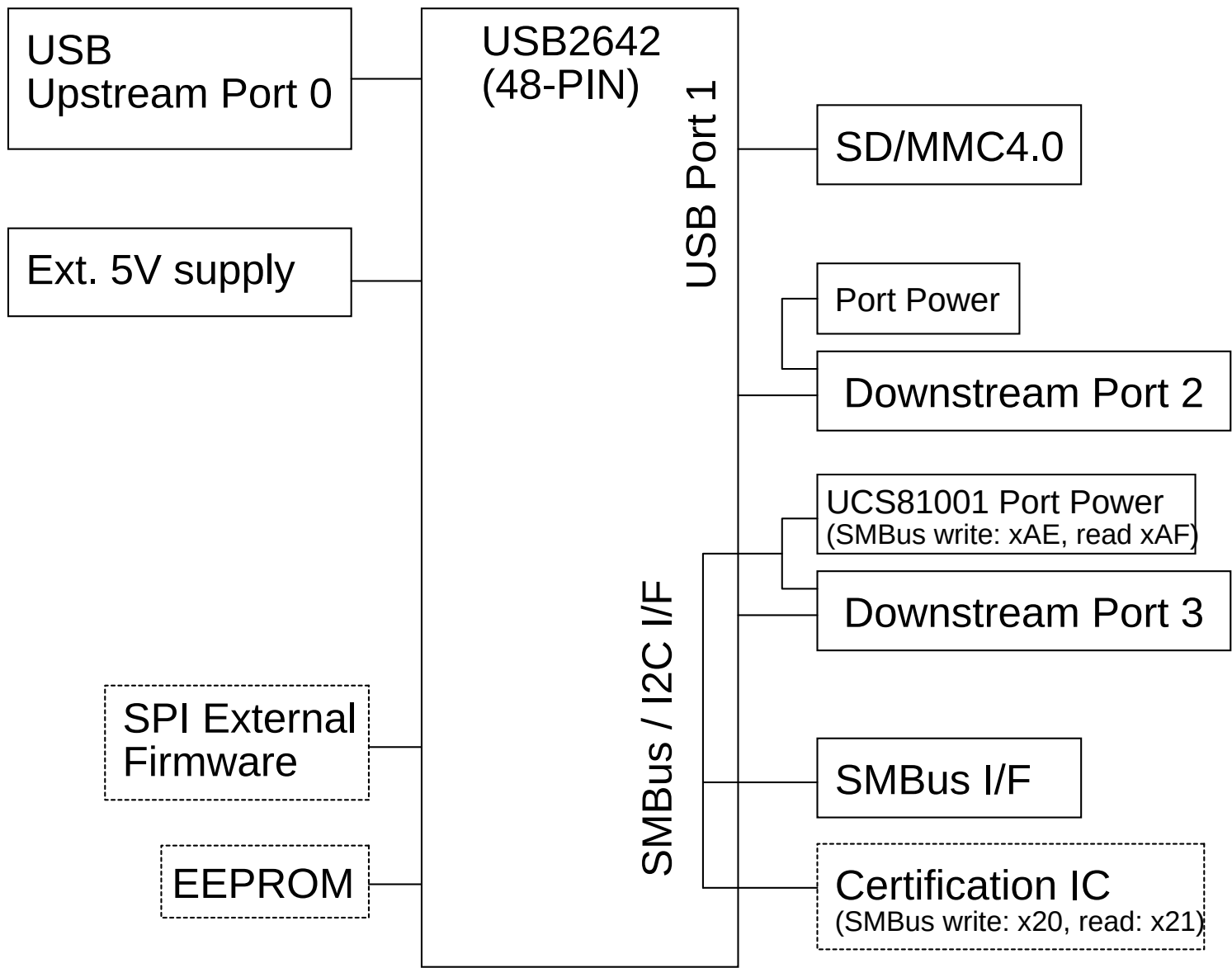


USB2642QFN48 Evaluation Board

Evaluation Board for USB2642, 48 pin QFN

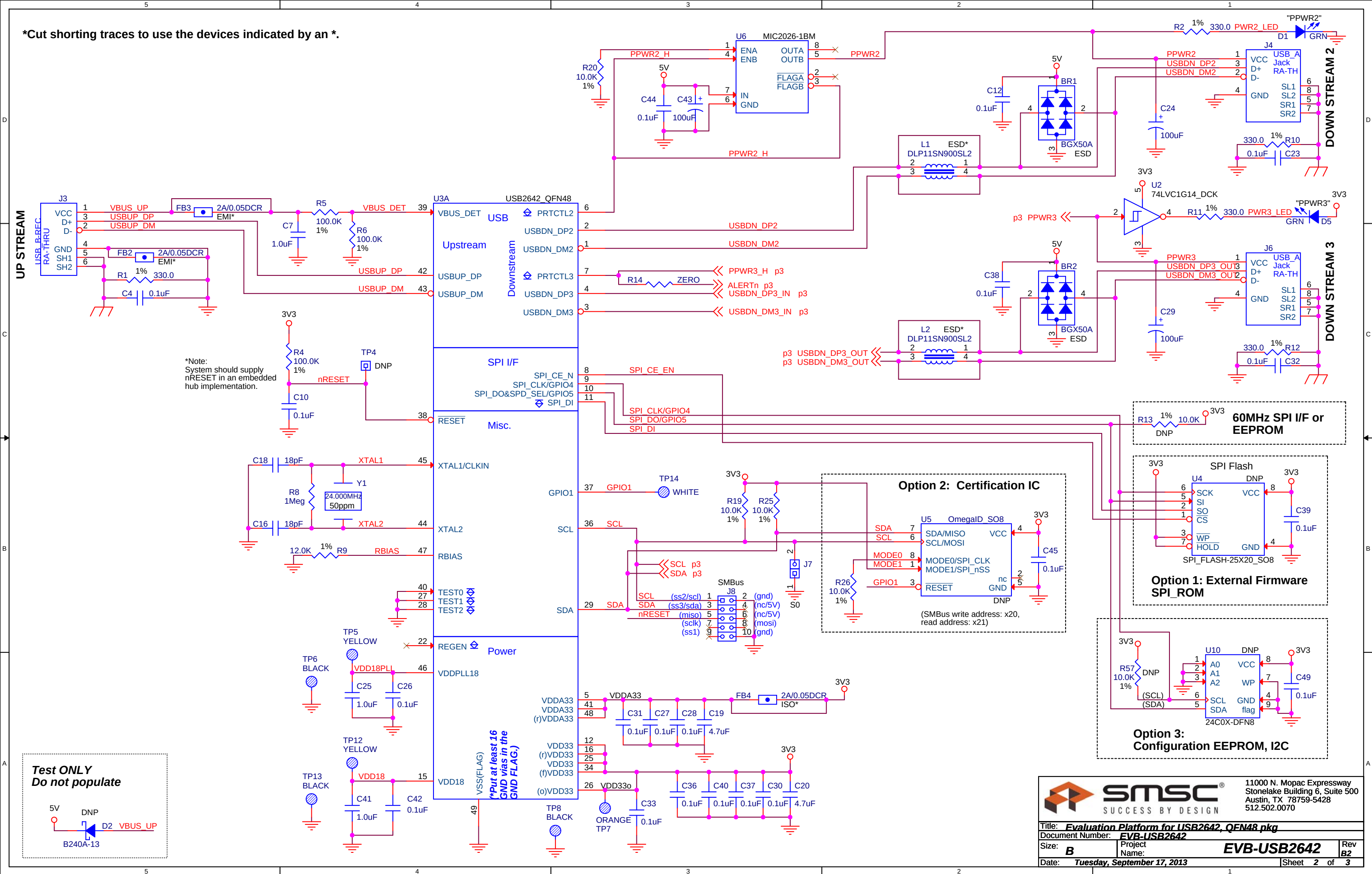
Functional Block Diagram



Revision History

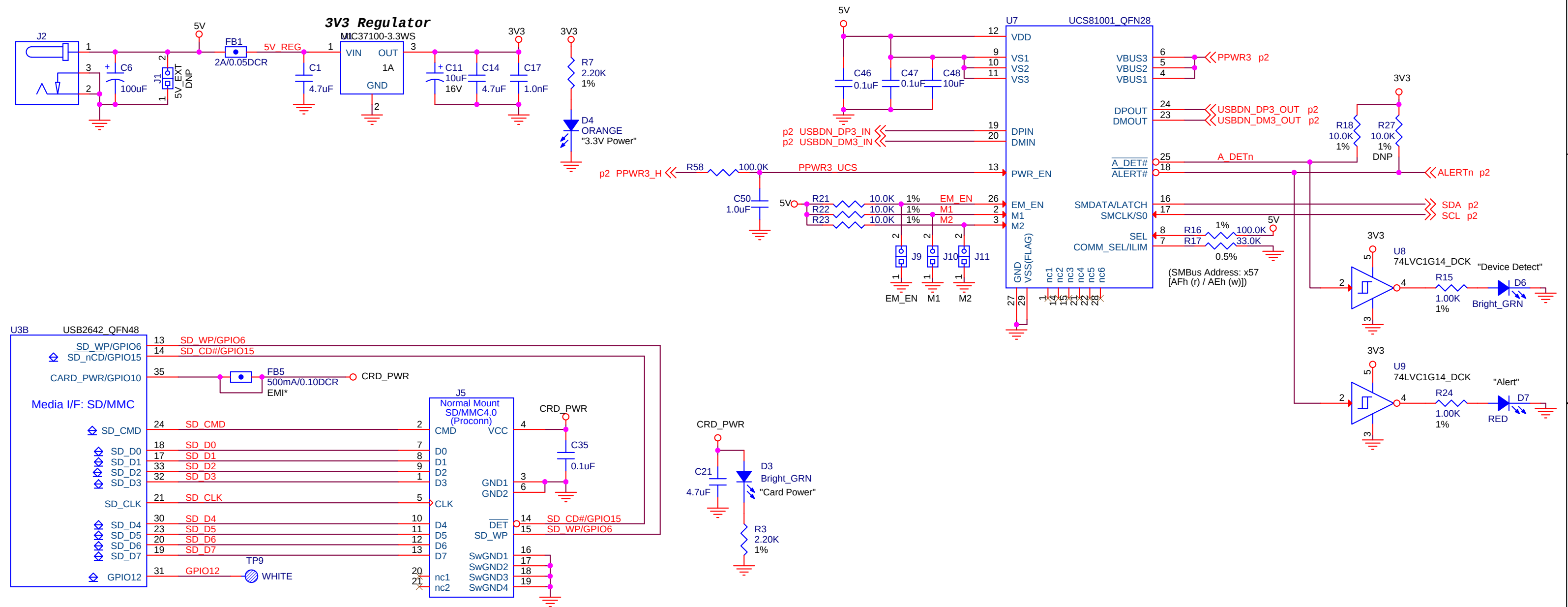
Rev.	Date	Revision Summary	Author
00A	20110621	Initial schematic based off of Tyche_A	J. Hancock
01A	20110701	Made all resistors 1%, replaced 3.3V regulator with MIC37100, removed VBUS_UP and REGEN DNP resistors, removed I2C reference as configuration option, and added SMBus addresses to slave devices.	J. Hancock
02A	20110708	Changed P RTPWR3 LED circuit.	J. Hancock
A	20110718	Renumbered and released.	J. Hancock
A1	20111116	Shorted pins 25 and 26 together (VDD33).	J. Hancock
0B	20111221	Depopulated pull-up resistor on ALERTn, populated 00hm on ALERTn to USB chip, added RC circuit to PPWR3_H between USB and UCS chip, added EEPROM, and wired ESD diodes to 5V instead of VBUS on downstream ports.	J. Hancock
B	20120105	Released.	J. Hancock
B1	20120307	Updated SMBus address for UCS chip. Updated UCS p/n.	J. Hancock
B2	20120605	Changed UCS part number to UCS81001 and changed cap on UCS PWR_EN to 1uF. Updated Block Diagram.	J. Hancock

***Cut shorting traces to use the devices indicated by an *.**



***Cut shorting traces to use the devices indicated by an *.**

Note:
Jumper Selections marked with an asterisk (*) are default settings.



Optional Devices

