

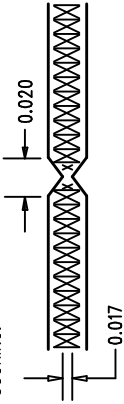
SIZE	QTY	SYM	PLTD
95	5	+	PLTD
35	3	X	PLTD
40	1	□	NPLTD
70	2	⊗	NPLTD
10	147	⊗	PLTD
15	2	A	PLTD
187	4	B	NPLTD

NOTES : Unless Otherwise Specified

1. FAB PER IPC-A-600.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON ALL LAYERS.
THICKNESS .062 +/- .006 TOTAL OF 2 LAYERS.
FLAMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER. .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/- .003
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
WHITE TIN IMMERSION (OMIKRON) OR LEAD FREE

SOLDER BOTH SIDES.

6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
8. SCORING:



FAB DRAWING

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1 2 PLACE ±.01 3 PLACE ±.005 INTERPRET DIM AND TOL PER ASME Y14.5M - 1994		APPROVALS		LINEAR TECHNOLOGY 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
		PCB DESIGNER J. WU	DATE 1/25/07	TITLE: High Speed ADC Clock Source	
ENGINEER C. Mayott		DATE 1/25/07	SIZE A		REV. 1
CHECK		DC1216A		SHT 1 of 1	
SCALE = NONE					