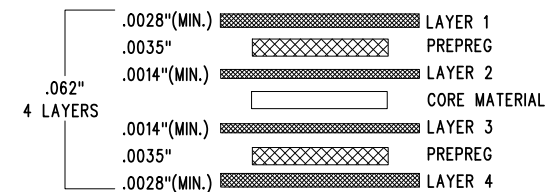


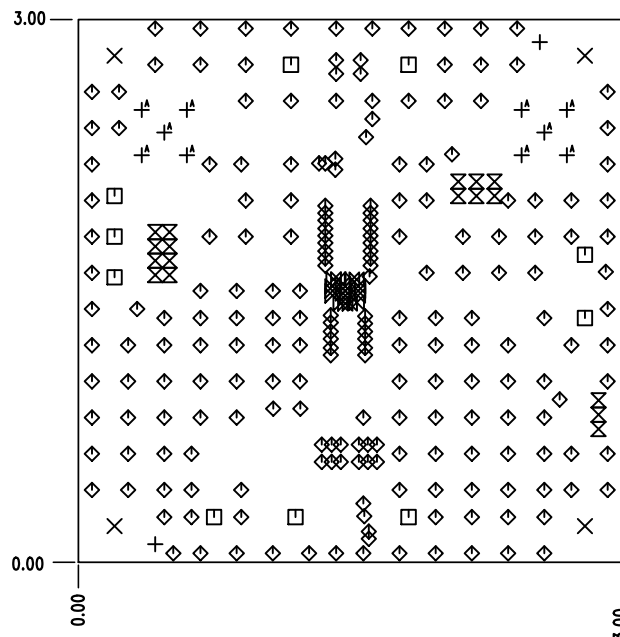
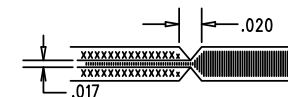
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	4	PRODUCTION	SY	04-25-18

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
U1 SOLDER MASK SIZE IS 14 MIL AND PAD SIZE IS 18 MIL.
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.008	19	⊗	YES	+/-0.003
0.01	217	◇	YES	+/-0.003
0.035	17	⊗ ^A	YES	+/-0.003
0.056	10	+ ^A	YES	+/-0.003
0.07	2	+	NO	+/-0.003
0.095	10	□	YES	+/-0.003
0.187	4	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS PCB DES. LT APP ENG. SY		ANALOG DEVICES POWER BY LINEAR www.analog.com FOR ADI CUSTOMER USE ONLY	
TITLE: FABRICATION DRAWING STEP DOWN SILENT SWITCHER μMODULE REGULATOR		SIZE N/A		IC NO. LTM8074EY DEMO CIRCUIT 2753A	
SCALE = NONE		FILENAME: DC2753A-4.PCB		REV 4	
				SHT 1 OF 1	