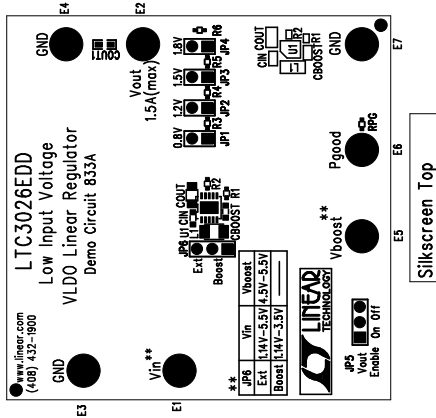


REVISIONS			
REV	DESCRIPTION	APPR	DATE



### NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
3. JUMPER SHUNTS INSTALLATION:  
JP5: On JP6: Boost

## ASSY DRAWING

APPROVALS		LINEAR TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
DRAWN	INIT	DATE			
CHECK					
DESIGN	JUNE WU	10/15/04			
ENGR	Tom Gross	10/15/04			
TITLE: LTC3026EDD, Low Input Voltage VLDO Linear Regulator			SIZE	A	REV. A
SCALE = NONE			DC833A		
DES-230719					SHT 1 of 1