

LAN89218 Mini Development Card

Assy 6519

PCB Revision B

Schematic Revision 1.0

Design Details

Board:

Assy 6519

Chip:

SMSC LAN89218

Board Form Factor:

Larger than 2.346" x 2.300"

Assembly:

100-Pin TQFP

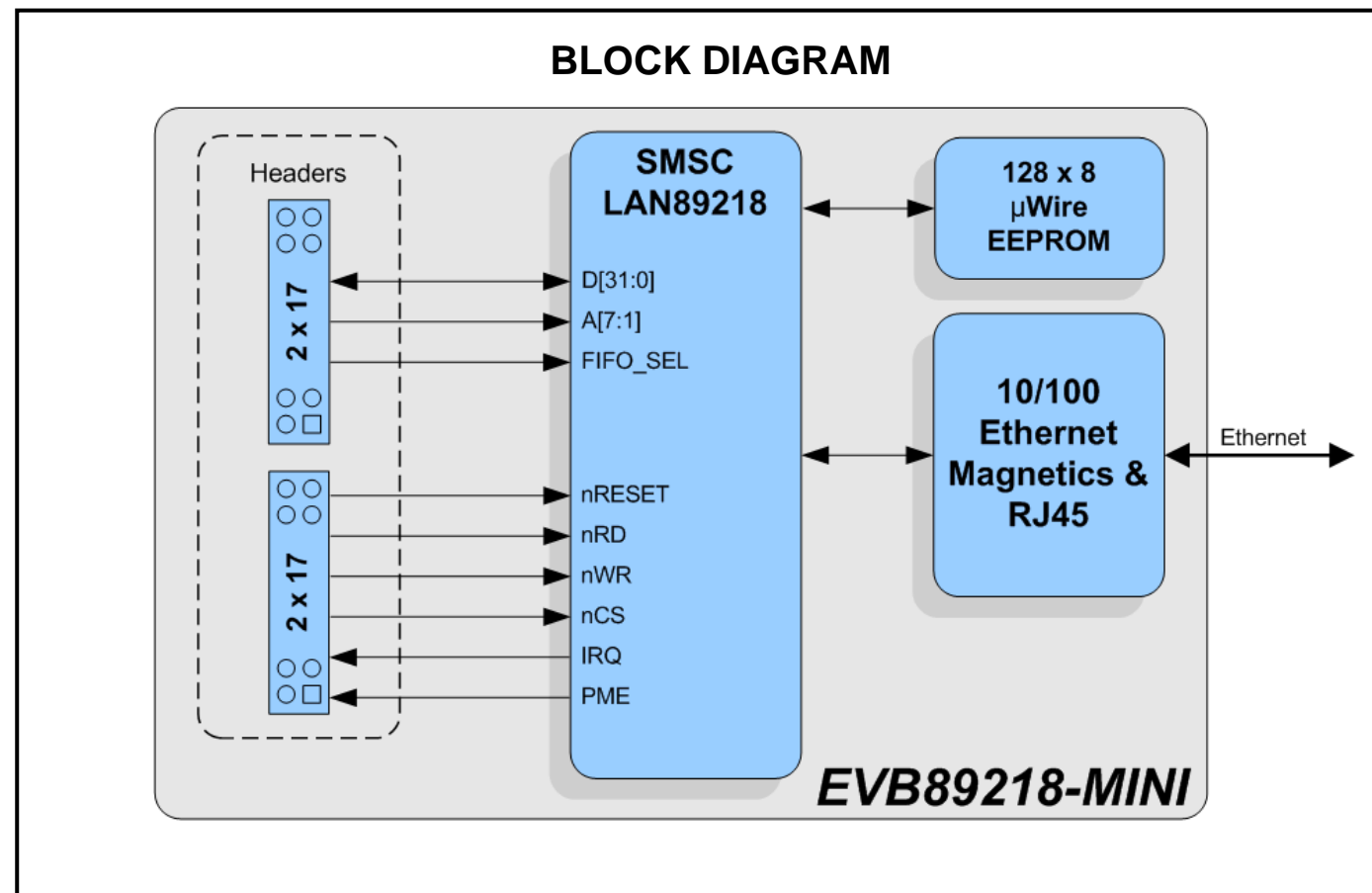
Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

Important Note: Some support components shown in this design example may not be Automotive-Grade. Please be sure to check the manufacturer's datasheet(s) for detailed specifications and corresponding part numbers.

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LAN89218	3

Revision History

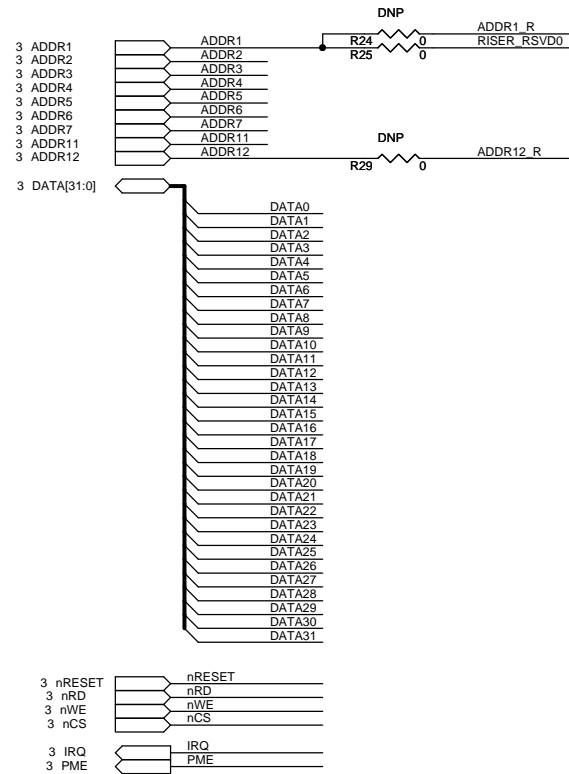
Rev 1.0:



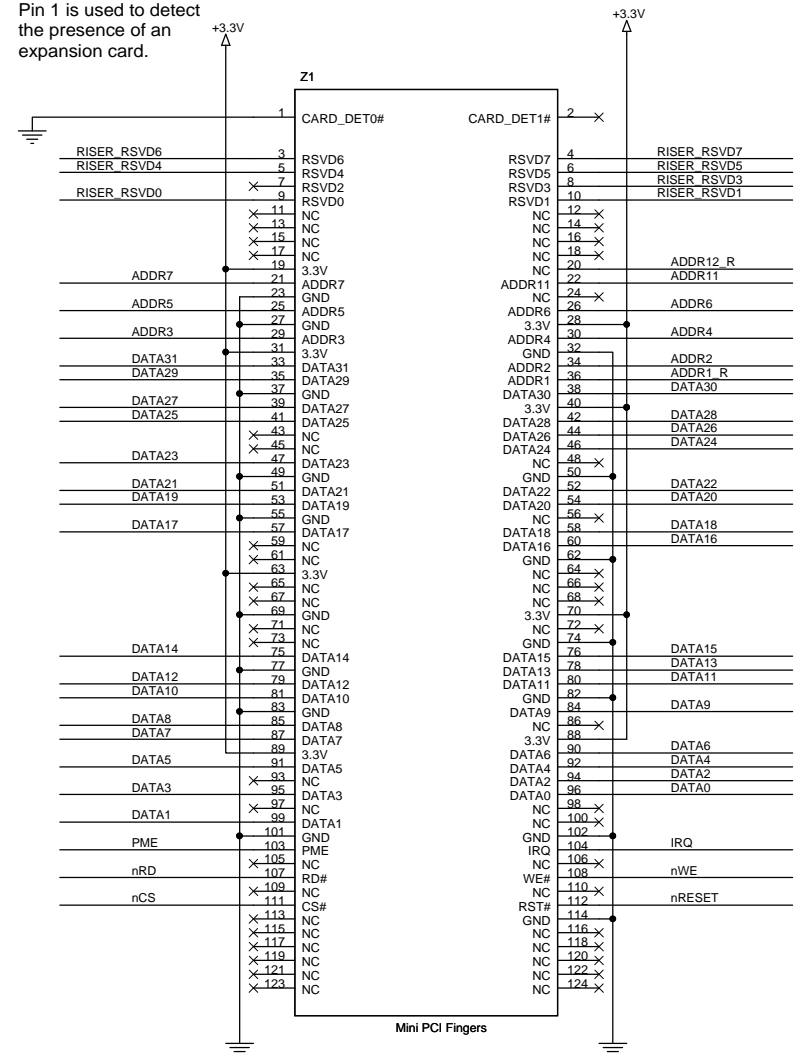
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Local Bus Expansion Slot

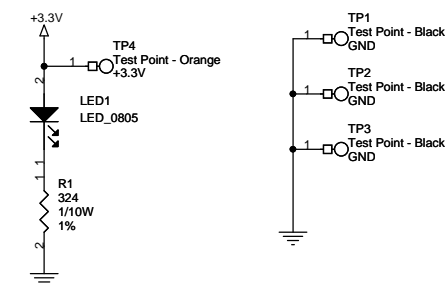
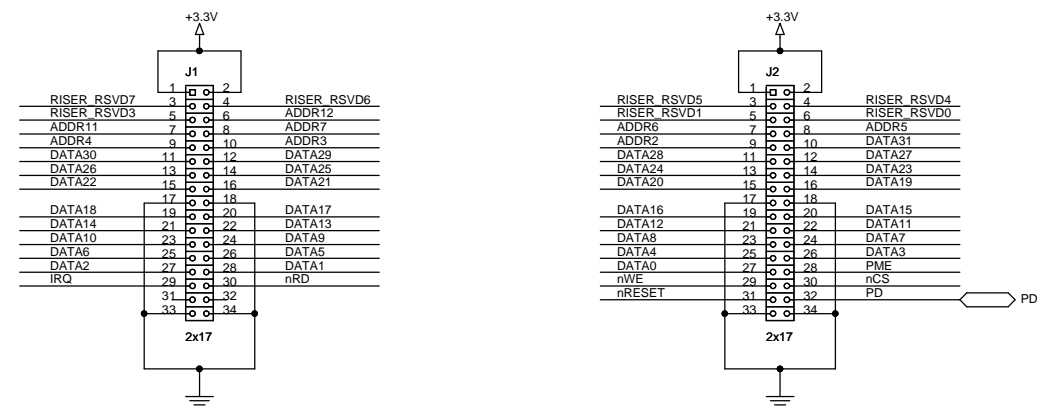
NOTE:
 ADDR1 is connected to RISER_RSVD0 (Pin 9 on Z1) by default.
 Remove R25 and populate R24 to connect ADDR1 to ADDR1_R (Pin 36 on Z1)



NOTE:
 Pin 1 is used to detect the presence of an expansion card.



Local Bus Test Points



SMSC
 SUCCESS BY DESIGN

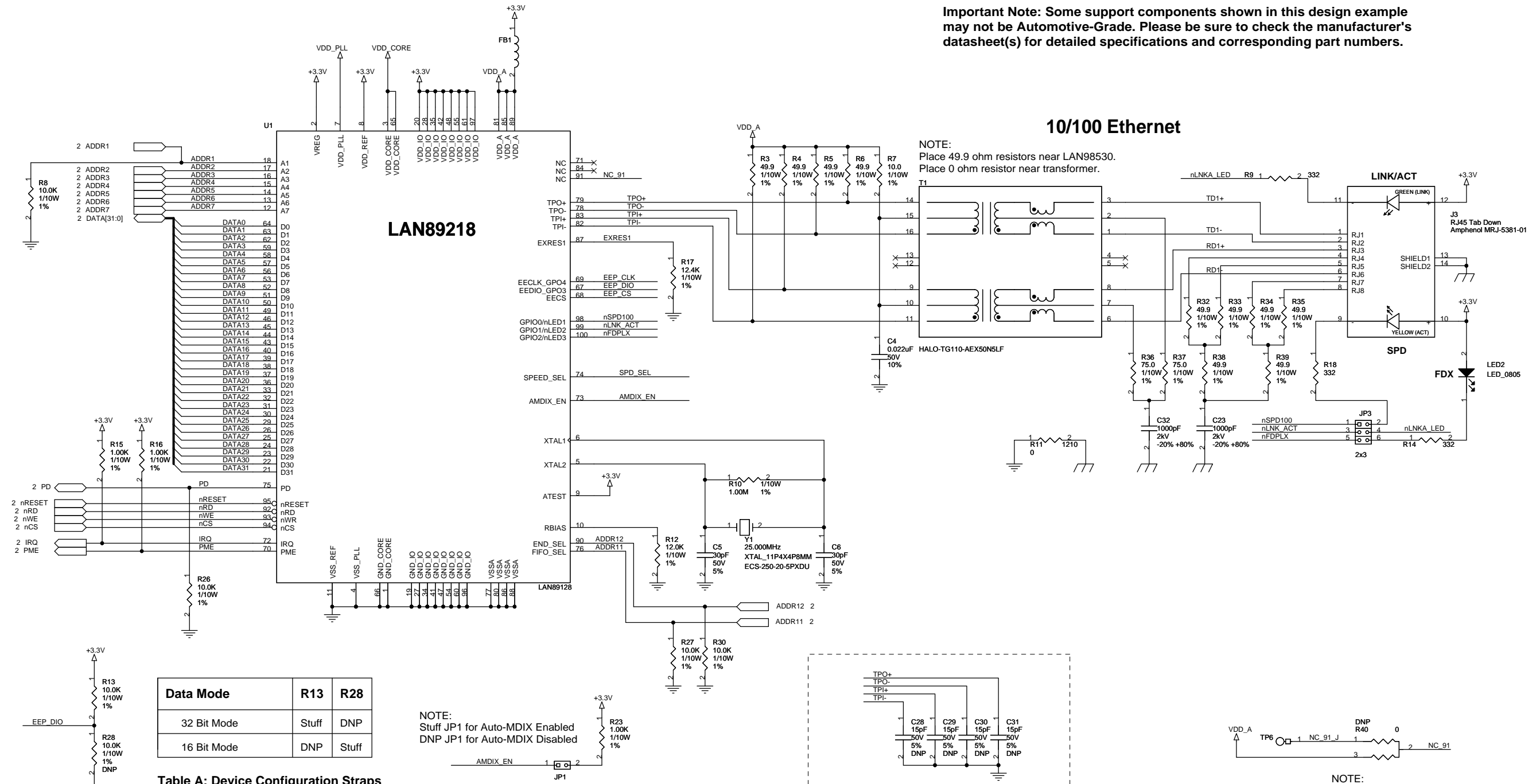
Title: **Expansion Slot Edge Fingers**

Size	Engineer	Assembly No.	PCB Rev	Schematic Rev
C	J.M.	6519	B	1.0
Date:	Tuesday, February 14, 2012			Sheet 2 of 3

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10/100 Ethernet

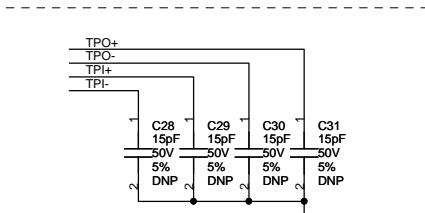
NOTE:
Place 49.9 ohm resistors near LAN98530.
Place 0 ohm resistor near transformer.



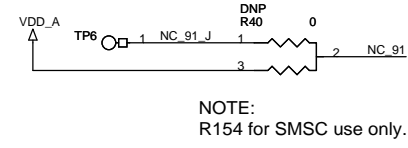
Data Mode	R13	R28
32 Bit Mode	Stuff	DNP
16 Bit Mode	DNP	Stuff

Table A: Device Configuration Straps

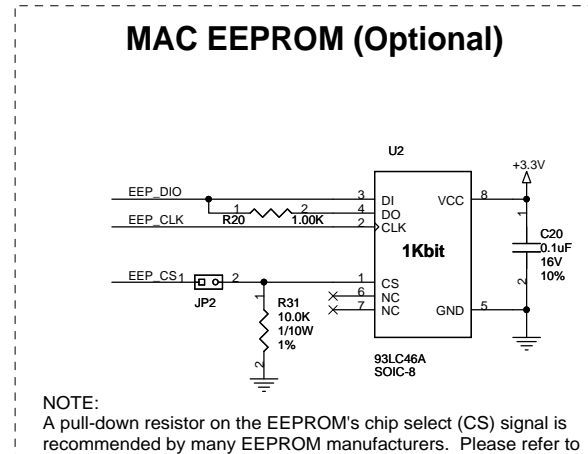
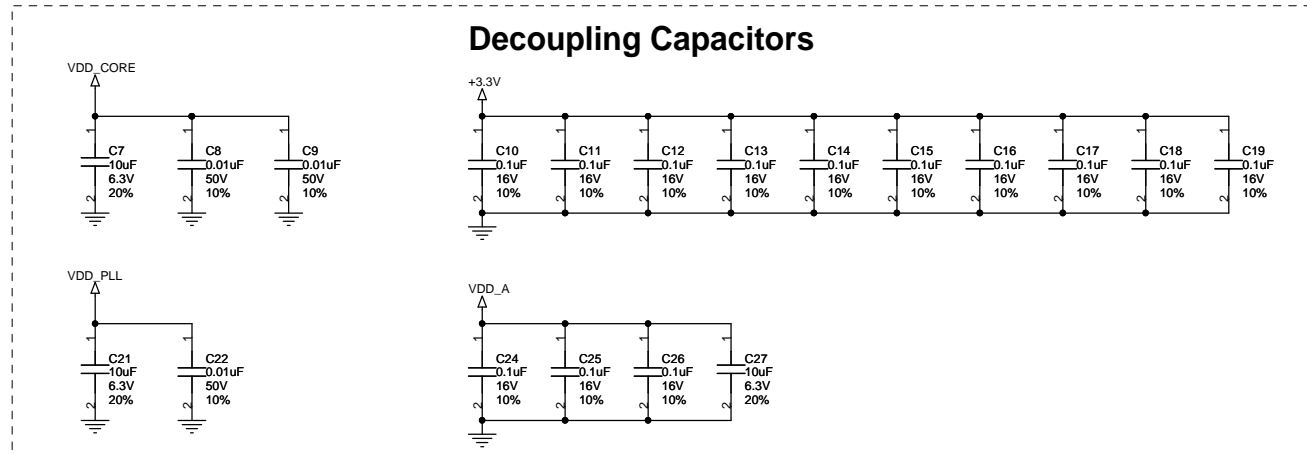
NOTE:
Stuff JP1 for Auto-MDIX Enabled
DNP JP1 for Auto-MDIX Disabled



IMPORTANT NOTE: Capacitors C28 – C31 are optional and are not present on the Mini Development Board. These capacitors are required for operation in an EMI constrained environment. When used, these capacitors must be placed close to the transformer.



NOTE:
Stuff R19 for 10Mbps, Half-Duplex, Auto Neg. Disabled
DNP R19 for 100Mbps, Half-Duplex, Auto Neg. Enabled



NOTE:
A pull-down resistor on the EEPROM's chip select (CS) signal is recommended by many EEPROM manufacturers. Please refer to the EEPROM manufacturer's data sheet for further information.