

5A, Low Noise, Programmable Output, 85mV Dropout Linear Regulator with Analog Margin

DESCRIPTION

Demonstration circuit 1434 is an adjustable 5A linear regulator featuring LT3071. LT3071 is a low dropout voltage, UltraFast™ transient response linear regulator. The device supplies up to 5A of output current with a typical dropout voltage of 85mV. A 0.01μF reference bypass capacitor decreases output voltage noise to 25μV_{RMS}. The LT3071's high bandwidth permits the use of low ESR ceramic capacitors.

Output voltage is digitally selectable in 50mV increments over a 0.8V to 1.8V range. An analog margining function allows the user to adjust system output voltage over a continuous ±10% range. To maintain proper performance and regulation, the BIAS supply voltage must be higher than the IN supply voltage. For a given V_{OUT}, the BIAS voltage must satisfy the following conditions:

$$2.2 \leq V_{\text{BIAS}} \leq 3.6\text{V and } V_{\text{BIAS}} \geq (1.25 \cdot V_{\text{OUT}} + 1\text{V}).$$

For V_{OUT} ≤ 0.95V, the minimum bias voltage is limited to 2.2V.

The IC incorporates a unique tracking function to control buck regulator powering the LT3071's input. This tracking function drives the buck regulator to maintain the LT3071's input voltage to V_{OUT} + 300mV, minimizing power dissipation. It has internal protection includes UVLO, reverse-current protection, precision current limiting with power fold-back and thermal shutdown.

The LT3071 regulator is available in a thermally enhanced 28-lead, 4mm×5mm QFN package.

The LT3071 datasheet gives a complete description of the part, operation and application information. The datasheet must be read in conjunction with this quick start guide for demo circuit 1434.

Design files for this circuit board are available. Call the LTC factory.

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Performance Summary (T_A = 25°C)

PARAMETER	CONDITION	VALUE
Minimum Input Voltage	V _{OUT} =1.2V, I _{OUT} = 5A	1.32V
Maximum Input Voltage		3V
Minimum Bias Voltage	V _{OUT} =1.2V	2.5V
Maximum Bias Voltage		3.6V
Output Voltage V _{OUT}	JP1=LOW, JP2=HIGH, JP3=HIGH	1.2V ±1%
Maximum Output Current	V _{IN} - V _{out} < 0.5V	5A

QUICK START PROCEDURE

Demonstration circuit 1434 is easy to set up to evaluate the performance of the LT3071. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:

1. Place JP5 on the ON position.

2. Use JP1, JP2, JP3 and MAGRA voltage to set the desired output voltage.
3. With power off, connect the input power supply to Vin and GND.
4. Connect the bias voltage to Vbias and GND.
5. Turn on the power at the input and the bias pin.
7. Once the proper output voltage is established, adjust the load within the operating range and observe the output voltage regulation, efficiency and other parameters.

NOTE . Make sure that the input voltage does not exceed 3V and the bias voltage is within its limit.

6. Check for the proper output voltage.

NOTE . If there is no output, temporarily disconnect the load to make sure that the load is not set too high.

NOTE . Make sure that the power dissipation is limited below the thermal limit.

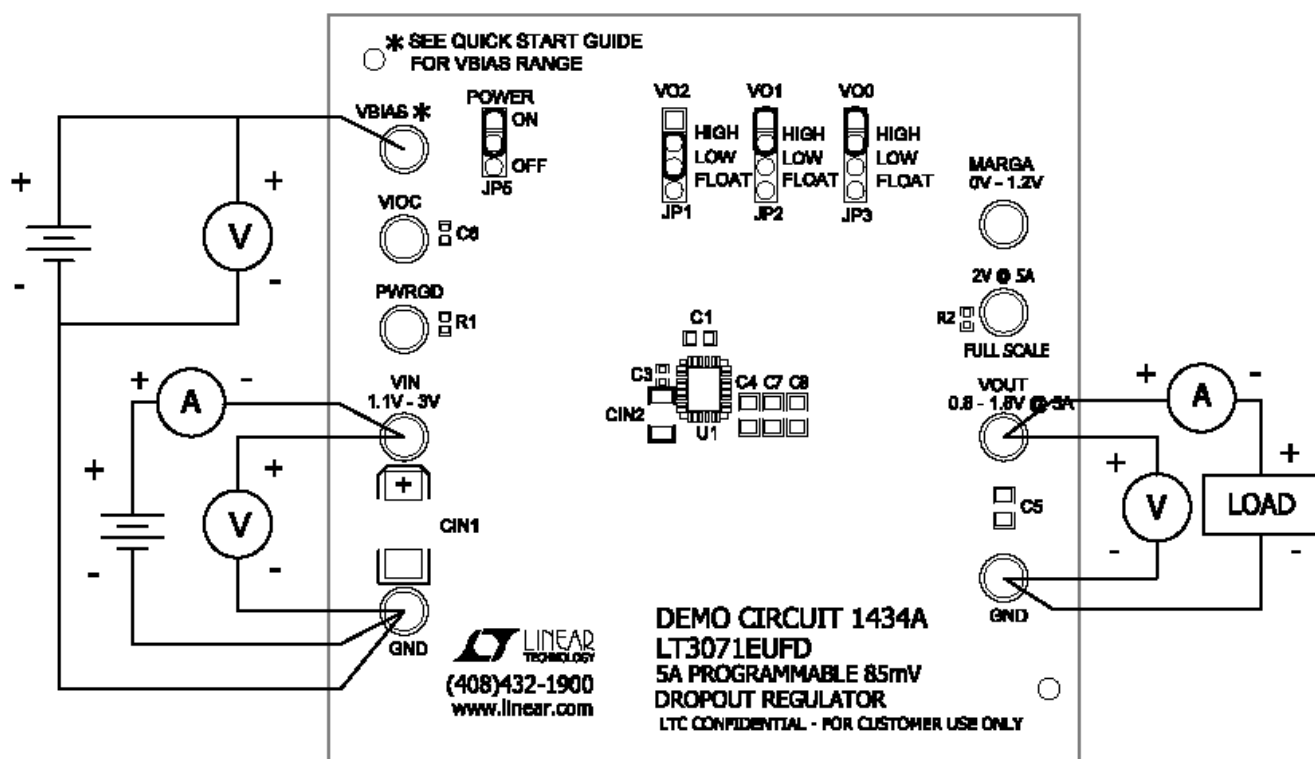
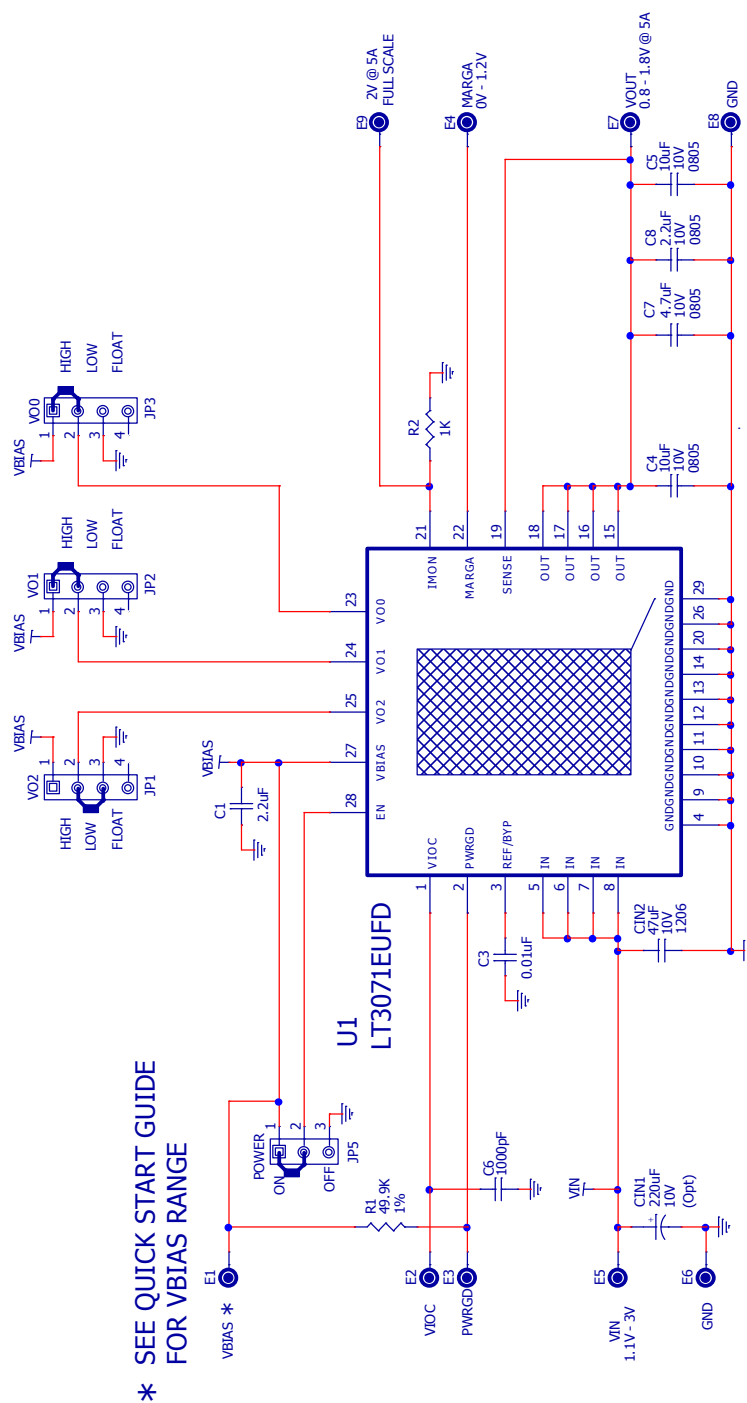


Figure 1. Proper Measurement Equipment Setup



* SEE QUICK START GUIDE
FOR VBIAS RANGE