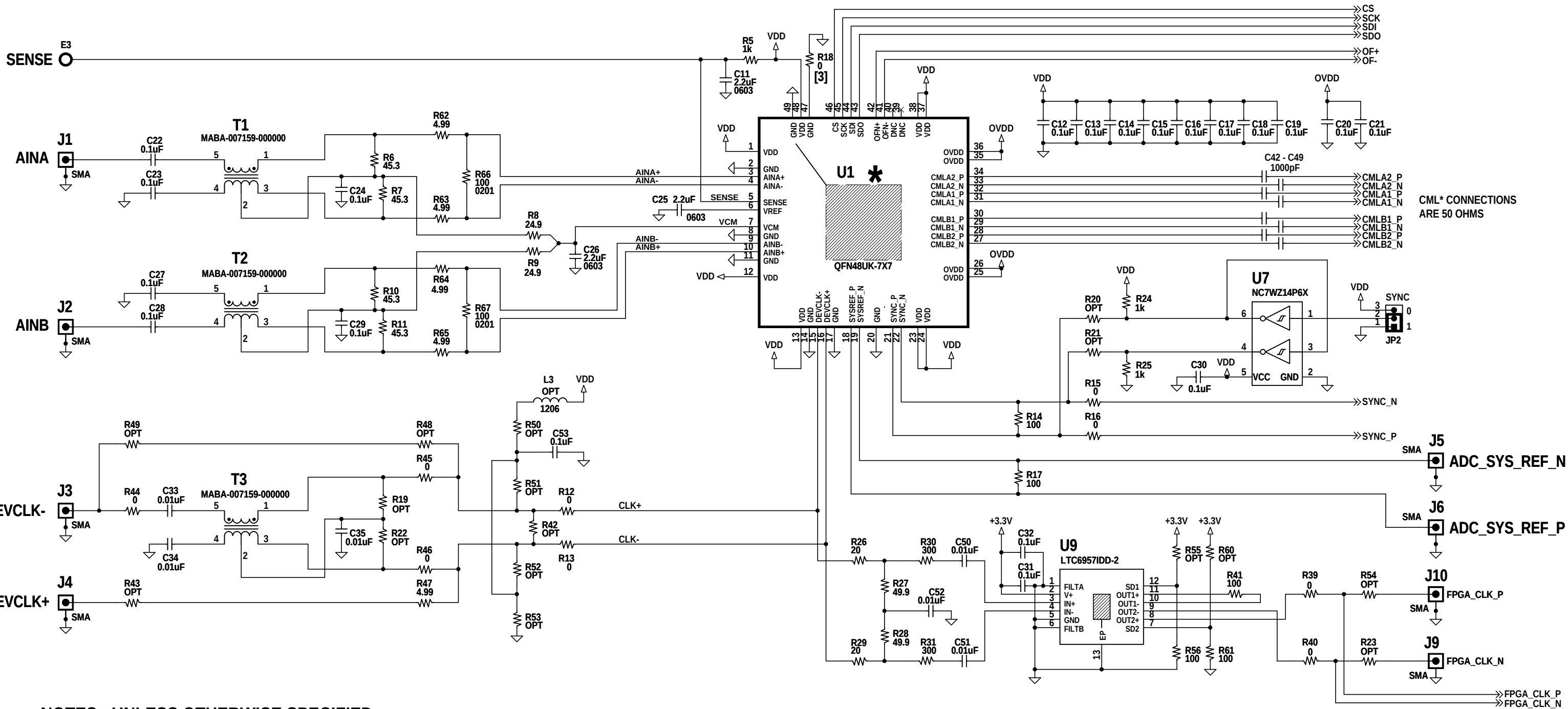


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	3	PRODUCTION	CLARENCE M.	02-25-15



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS AS SHOWN.
3. R18 IS OPTIONAL AT THE FINAL VERSION.

ASSY	U1	-Bit	Mbps
-B	LTC2121IUK-14	14	250
-C	LTC2121IUK-14	14	170

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

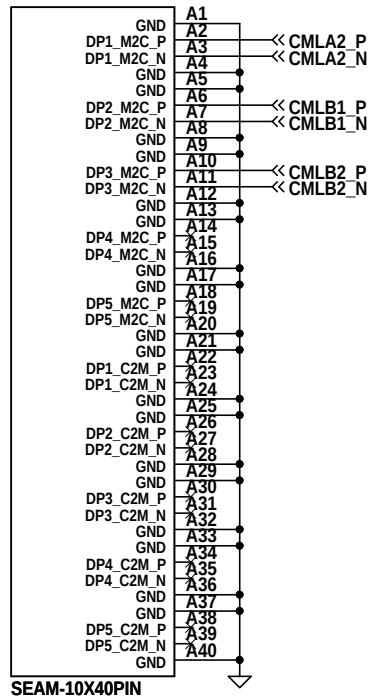
PCB DES.	KIM T.
APP ENG.	CLARENCE M.
SCALE	NONE



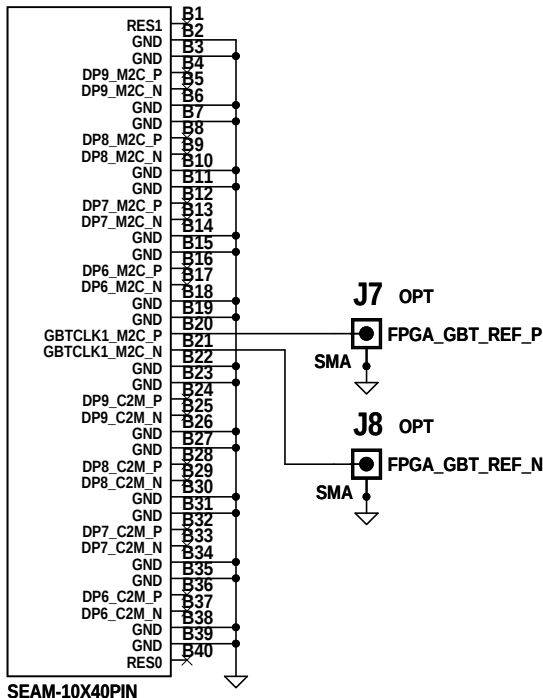
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC DUAL 14-BIT HIGH SPEED ADC WITH JESD204B SERIAL OUTPUTS			
SIZE	IC NO.	LTC2121XIUK FAMILY	
N/A		DEMO CIRCUIT 1974A	
DATE:	Wednesday, February 25, 2015	SHEET 1 OF 2	

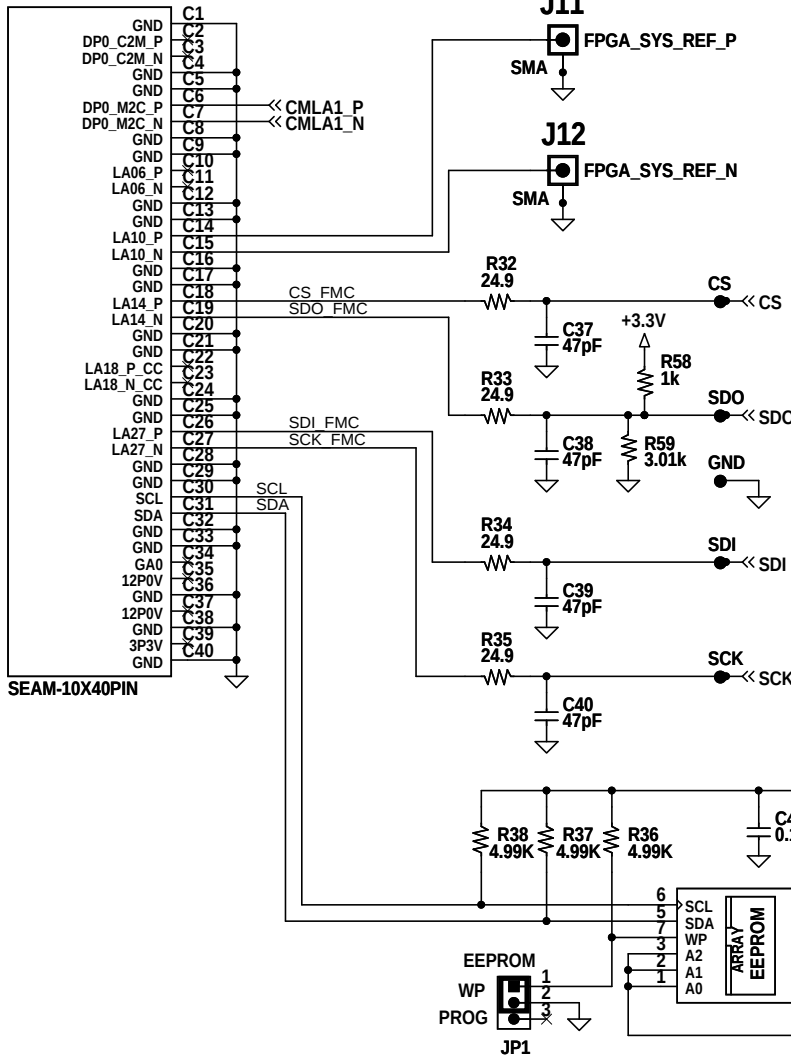
J13A



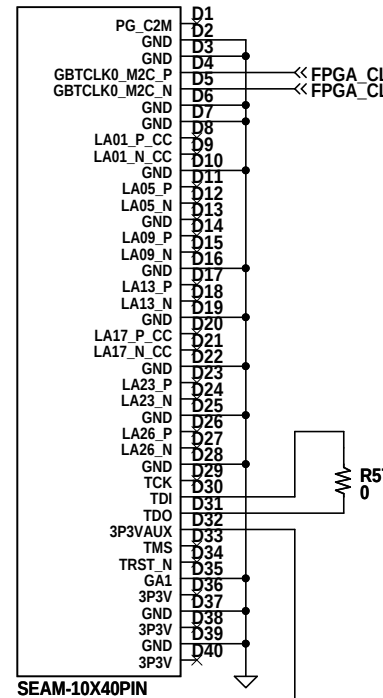
J13B



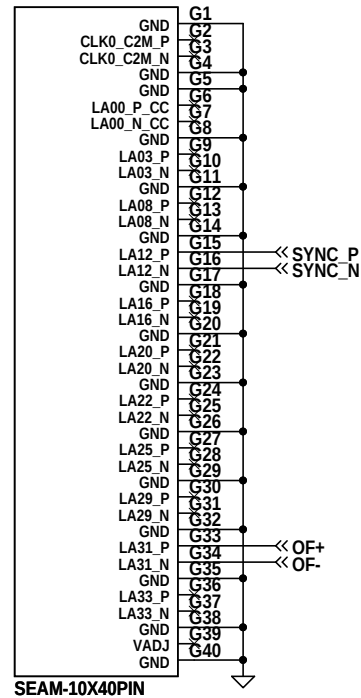
J13C



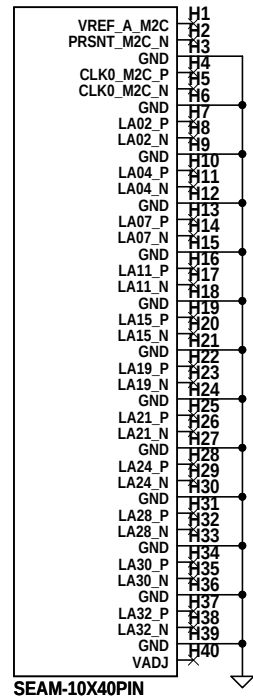
J13D



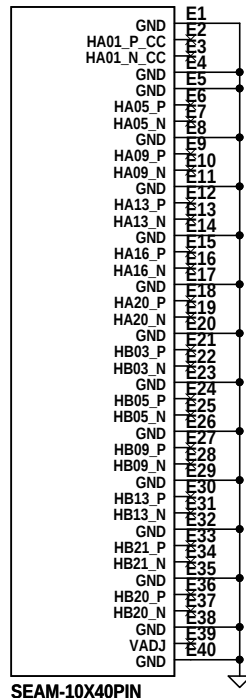
J13G



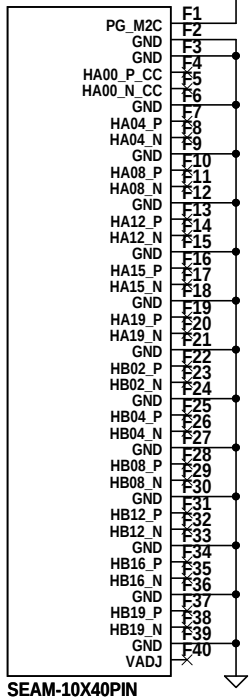
J13H



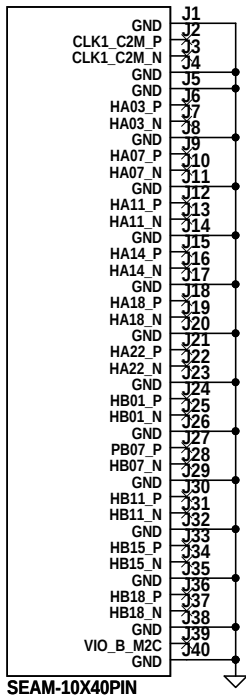
J13E



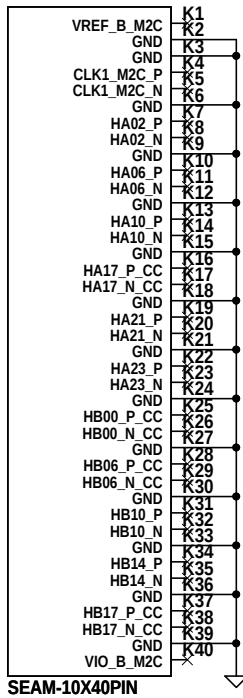
J13F



J13J



J13K



CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES. KIM T.

APP ENG. CLARENCE M.

SCALE = NONE



1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

DUAL 14-BIT HIGH SPEED ADC
WITH JESD204B SERIAL OUTPUTS

SIZE N/A

IC NO.

LTC212XIU FAMILY
DEMO CIRCUIT 1974A

REV.

3

DATE: Wednesday, February 25, 2015

SHEET 2 OF 2