

ISO77xx Triple- and Quad-Digital Isolator Evaluation Module

This user's guide describes the ISO77xx Triple- and Quad-Digital Isolator Evaluation Module (EVM). This EVM allows designers to evaluate device performance for fast development and analysis of isolated systems. The EVM supports evaluation of any of the TI triple- or quad-channel digital isolators in a 16-pin SOIC (DW) package.

CAUTION

This evaluation module is made available for isolator parameter performance evaluation only and is not intended for isolation voltage testing. To prevent damage to the EVM, any voltage applied as a supply or digital input/output must be maintained within the 0 V to 5.5 V recommended operating range.

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1 Introduction

This user's guide describes EVM operation with respect to the ISO77xx triple- and quad-channel digital isolators. However, the EVM may be reconfigured for evaluation of any of TI's triple- or quad-channel digital isolators in a 16-pin SOIC (DW) package. This guide also describes the available channel configurations within the ISO77xx family, the EVM schematic, and typical laboratory setup. A typical input and output waveform is also presented.

2 Overview

The ISO77xx is TI's new digital isolator family capable of galvanic isolations up to 8000 Vpk. The devices are certified to meet reinforced isolation requirements by VDE and CSA. These isolators provide high electromagnetic immunity and low emissions at low power consumption, while isolating CMOS or LVCMOS digital I/Os. The ISO77xx digital isolators have logic input and output buffers separated by a silicon oxide (SiO₂) insulation barrier. Used with isolated power supplies, these devices block high voltages, isolate grounds, and prevent noise currents on a data bus or other circuits from entering the local ground and interfering with, or damaging sensitive circuitry.

3 Pin Configurations of the ISO77xx Triple- and Quad-Channel Digital Isolators

Figure 1 shows the ISO773x triple-channel digital isolator pin configurations.

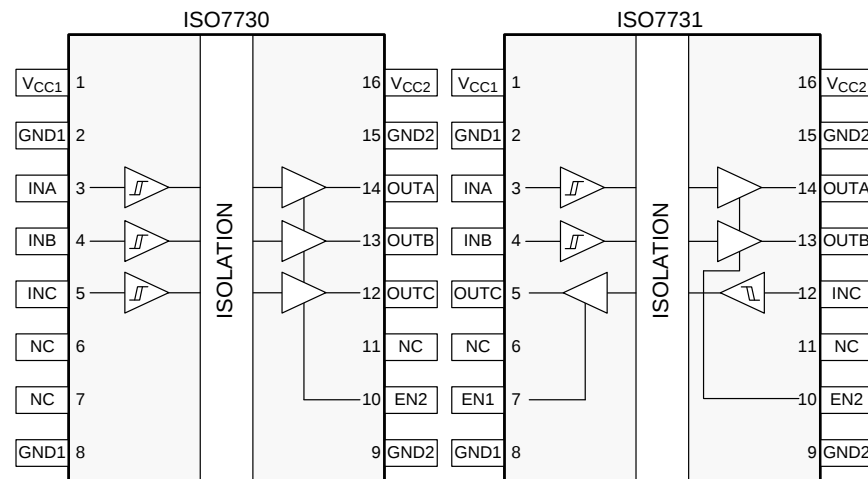


Figure 1. ISO773x Triple-Channel Digital Isolator Pin Configurations

Figure 2 shows the ISO774x quad-channel digital isolator pin configurations.

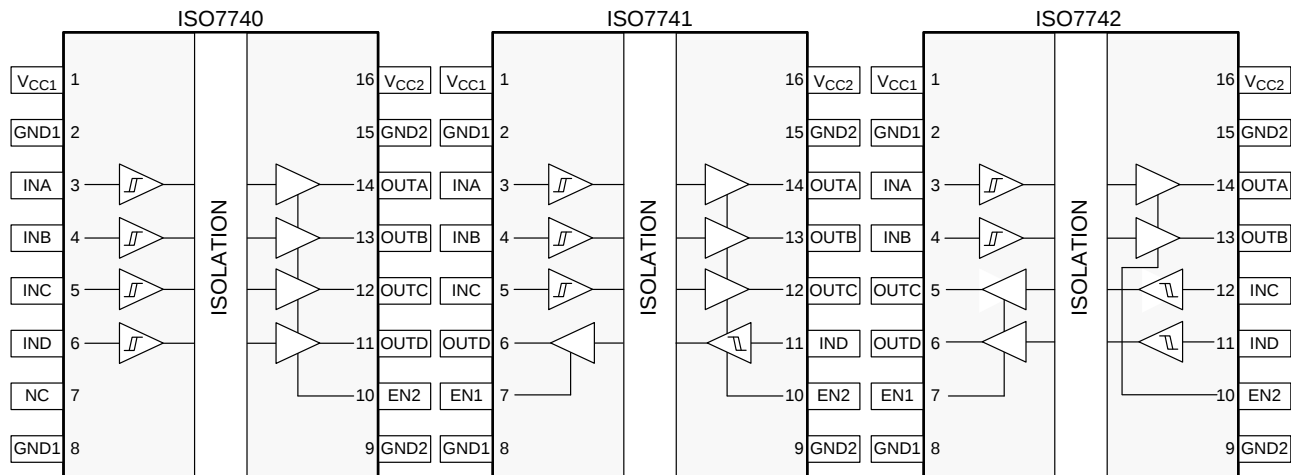


Figure 2. ISO774x Quad-Channel Digital Isolator Pin Configurations

4 ISO7741 – EVM Board Block Diagram and Image

Figure 3 shows the board configuration for evaluation of the ISO7741 quad-channel digital isolator.

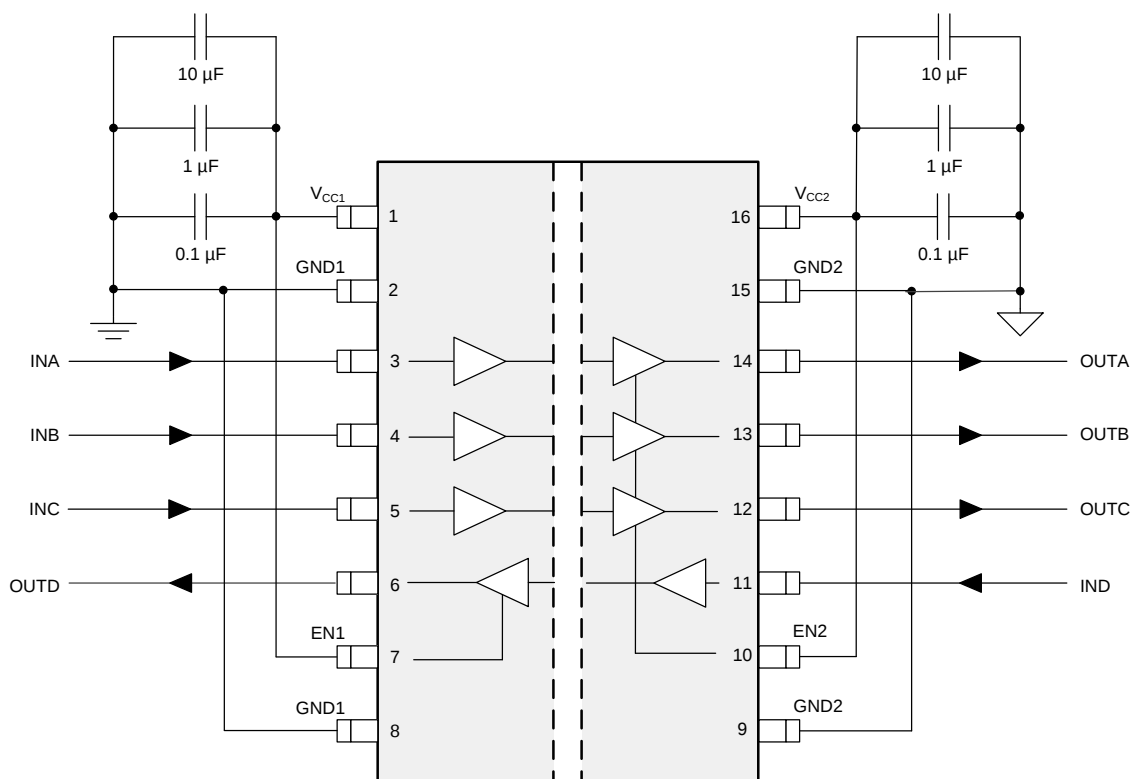


Figure 3. ISO7741 EVM Configuration

Figure 4 shows the photograph of the EVM.

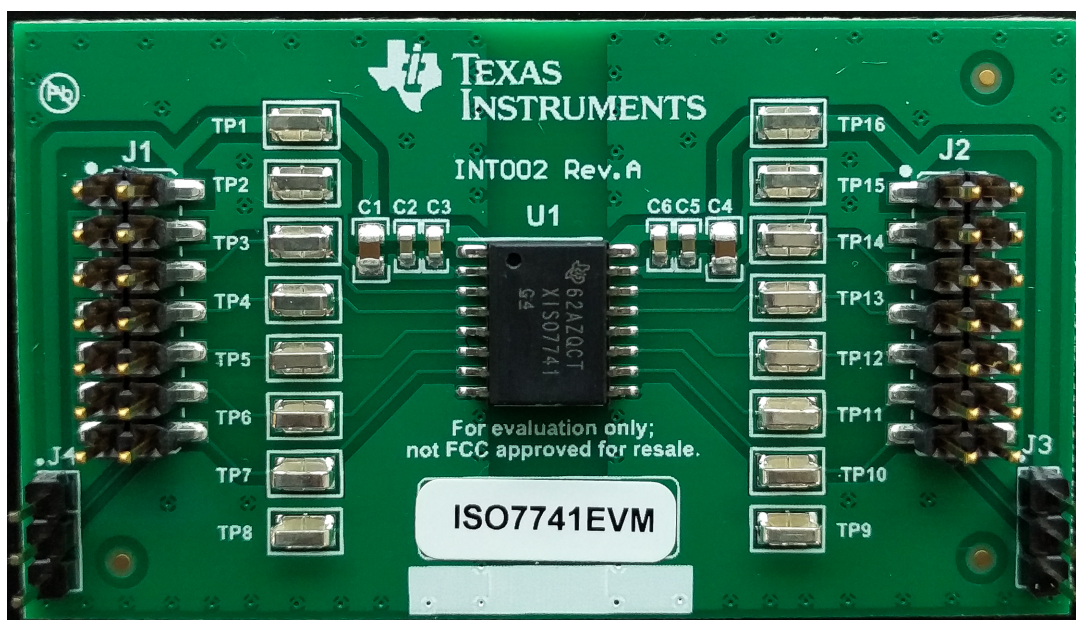


Figure 4. ISO77xx-EVM Photograph

5 EVM Setup and Operation

This section describes the setup and operation of the EVM for parameter performance evaluation. [Figure 5](#) shows the configuration for operating the ISO77xx Triple/Quad Digital Isolator EVM using two power supplies.

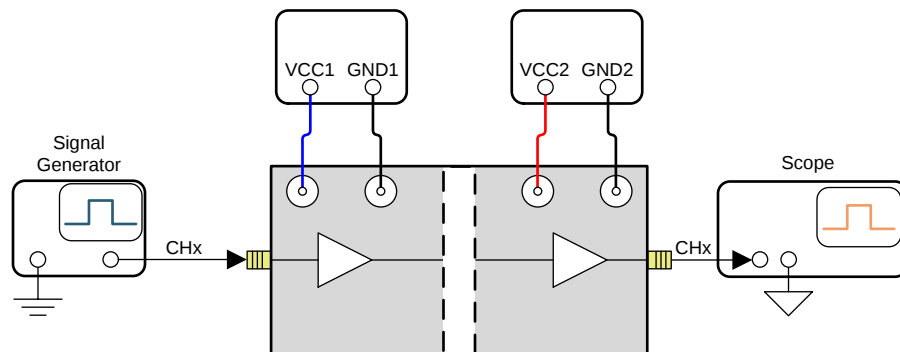


Figure 5. Basic EVM Operation

[Figure 6](#) shows typical input and output waveforms of the EVM for a 1-MHz clock. The input is shown as channel 1, and the output is shown as channel 2.

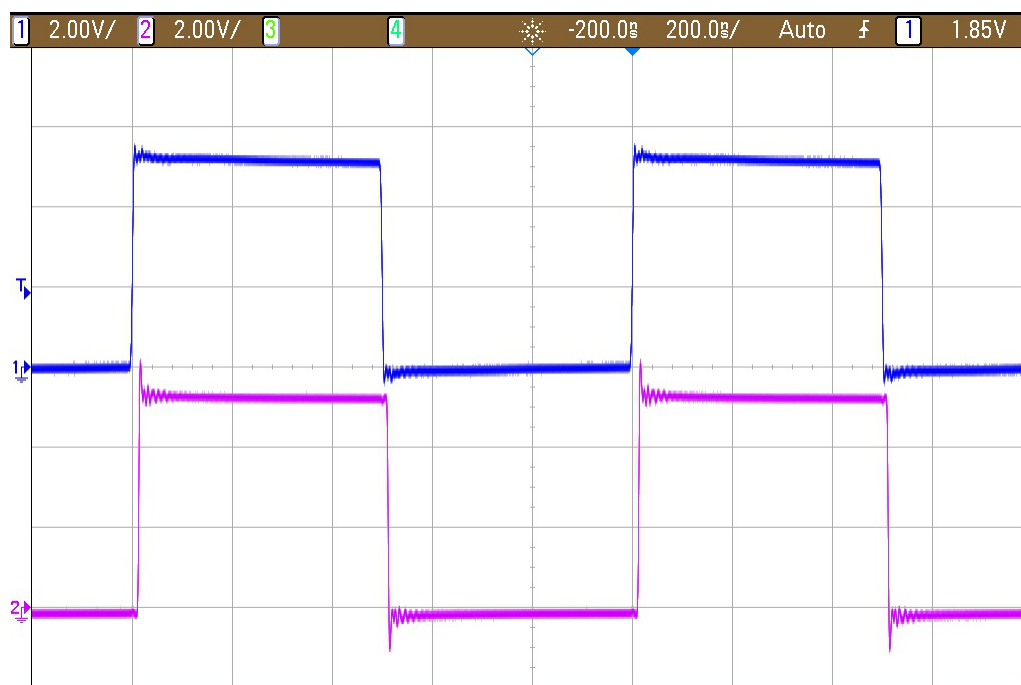


Figure 6. Typical Input and Output Waveforms

6 Bill of Materials

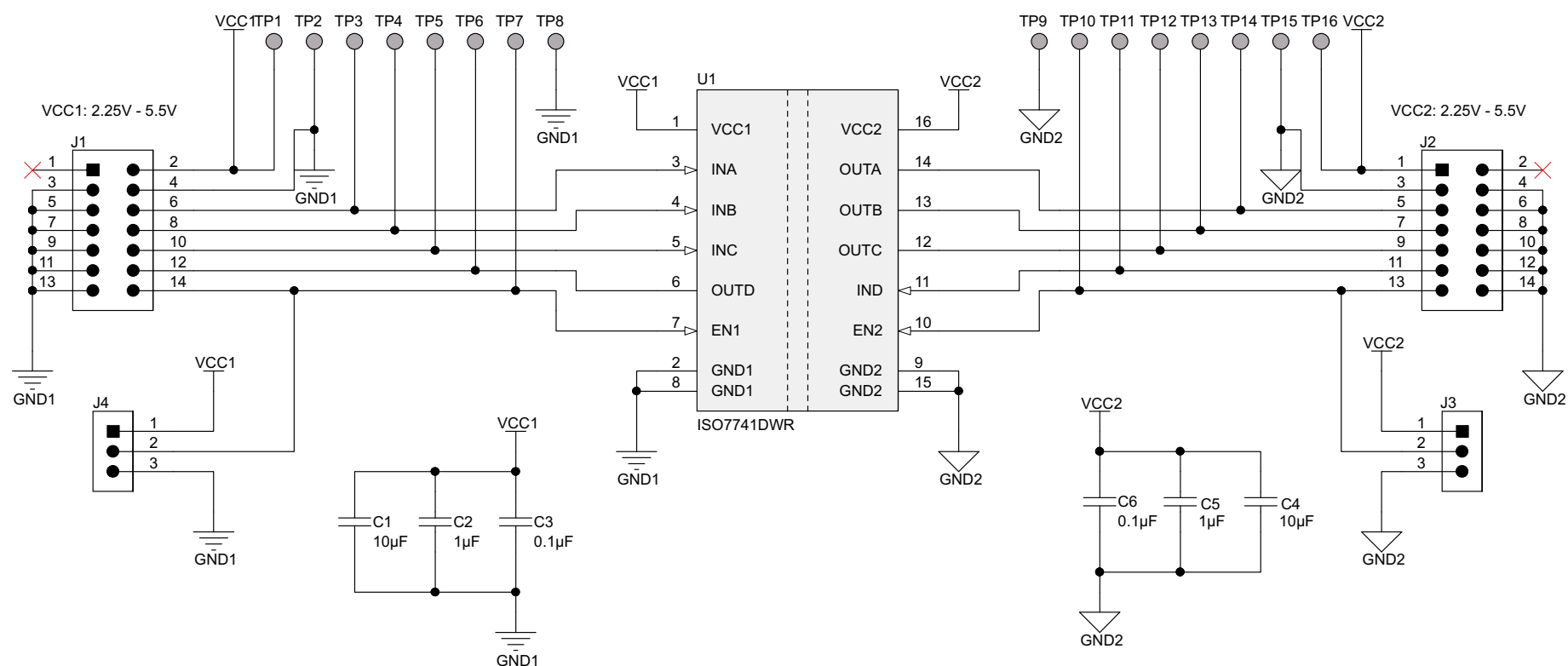
[Table 1](#) shows the bill of materials (BOM) for this EVM.

Table 1. Bill of Materials

Item	Designator	Description	Manufacturer	PartNumber	Quantity
1	J1, J2	Header, 100mil, 7 × 2, SMT	Molex	0015912140	2
2	C2, C5	CAP, CERM, 1 µF, 50 V, ±10%, X5R, 0603	MuRata	GRM188R61H105KA ALD	2
3	C3, C6	CAP, CERM, 0.1 µF, 25 V, ±5%, X7R, 0603	AVX	06033C104JAT2A	2
4	H1, H2, H3, H4	Bumpon, Hemisphere, 0.44 × 0.20, Clear	3M	SJ-5303 (CLEAR)	4
5	J1, J2	Header, 100 mil, 7 × 2, SMT	Molex	0015912140	2
6	J3, J4	Header, 100 mil, 3 × 1, Gold, TH	Samtec	HTSW-103-07-G-S	2
7	TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16	Test Point, Miniature, SMT	Keystone	5019	16
8	U1	High Speed, 5000 VRMS Quad-Channel Digital Isolators, DW0016B	Texas Instruments	ISO7741DWR	1

7 EVM Schematics and Layout

The ISO7741EVM is designed to accommodate any of the ISO77xx triple- and quad-channel devices in a 16-pin DW package. To evaluate any of the ISO77xx triple- and quad-channel devices in a 16-pin DW package, replace ISO7741DW with the device of interest on the ISO7741EVM PCB. No other component requires any modification. [Figure 7](#) shows the ISO77xx EVM schematic and [Figure 8](#) shows the printed-circuit board (PCB) layout.



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Figure 7. ISO77xx EVM Schematic

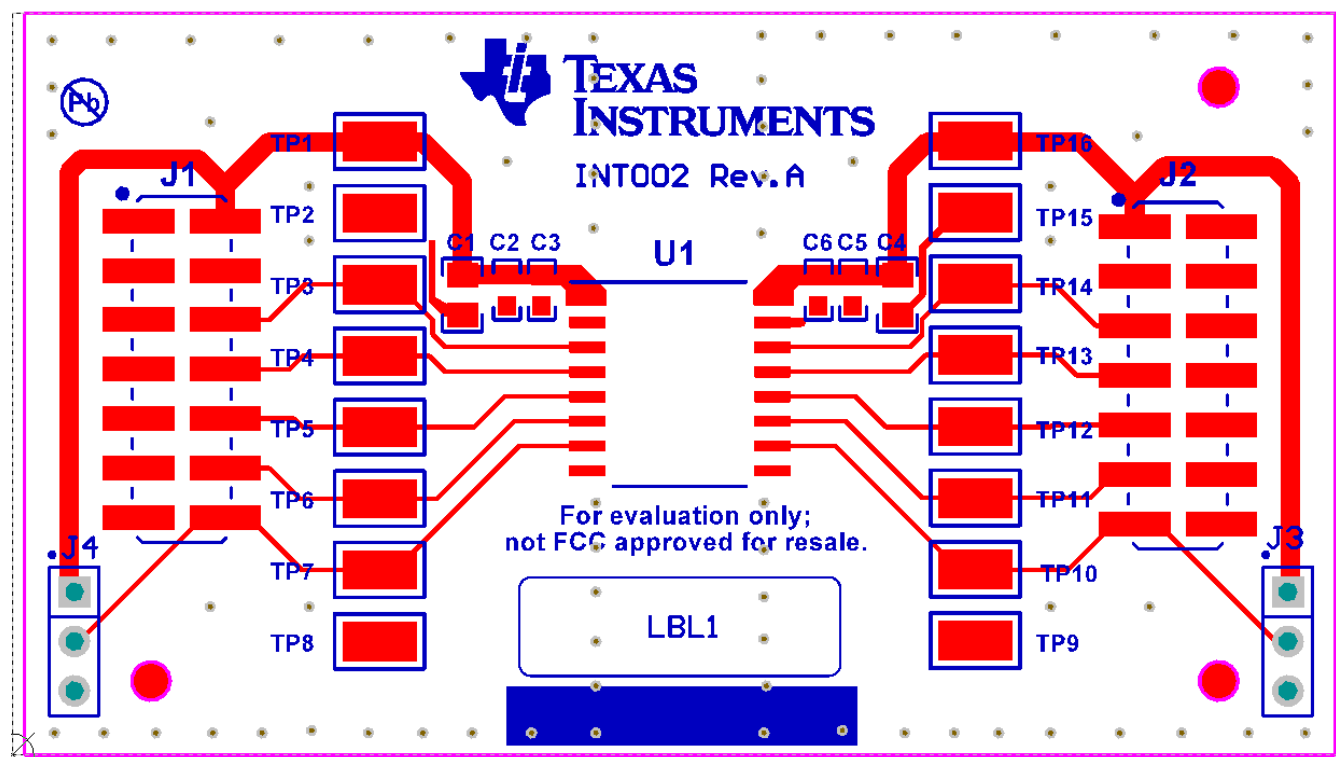


Figure 8. ISO77xx PCB Layout

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CAUTION

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NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

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- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

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Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page

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2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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