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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

NOTES: MATERIALS; MANUFACTURE TO MEET EU RoHS DIRECTIVE, ALL MATERIAL, (INCLUDING SOLDERMASK) REFER TO EU RoHS DIRECTIVE 2002/95/EC.

LAMINATE & RESIN MATERIALS; Tg>170C. Td>310c

ACCEPTABLE MATERIALS; ROGERS 4350, ROGERS 9159, ISOLA IS410, POLYCLAD 370 HR.

CLADDING; EXTERNAL LAYERS 1/2 OZ. COPPER, OVERPLATE TO 1 1/2 OZ. INTERNAL SIGNAL LAYERS 1 OZ. COPPER. INTERNAL PLANE LAYERS 1 OZ. COPPER.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER AND SHALL MEET IPC-SM-840V (LATEST REV.) CLASS 3. COLOR BLUE

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR WHITE

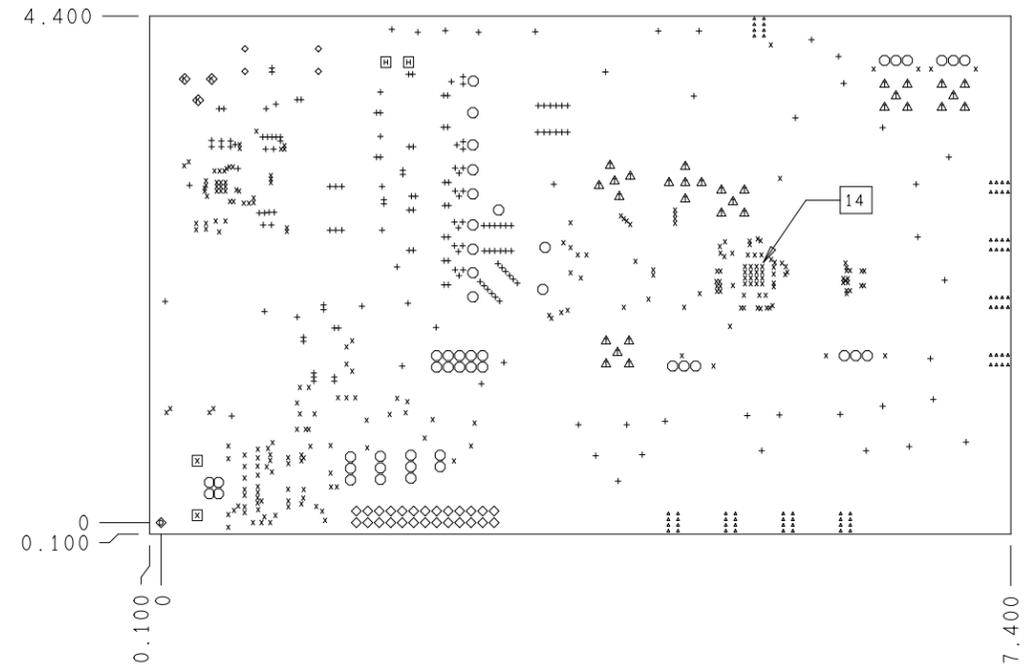
U.L. RATING; 94V0 MINIMUM.

FINISH; ENIG ELECTROLESS Ni (120-200u INCH); IMERSION GOLD (3-8u INCH)

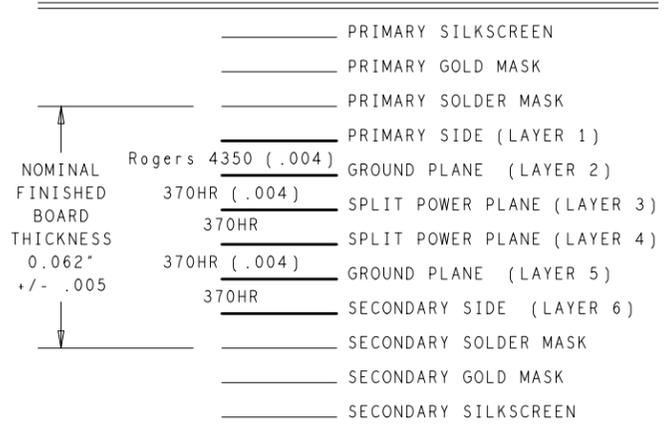
- FABRICATION:
- REFER TO IPC-4101B/99 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
  - UNDIMENSIONED HOLES TO BE LOCATED WITHIN +/- .005 OF THEIR TRUE POSITION WITH RESPECT TO ARTWORK.
  - PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN .001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
  - HOLE DIAMETERS APPLY AFTER PLATING.
  - FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL, INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
  - MINIMUM DESIGN LINE WIDTH IS .0045 INCH.
  - MINIMUM DESIGN SPACING IS .0055 INCH.
  - BOARD/PANEL MUST MEET IPC-A-600 (LATEST REV.) CLASS 2 FOR FLATNESS.
  - MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
    - A. U.L. CODE
    - B. DATE CODE (STAMP)
    - C. FLAMMABILITY RATING
    - D. MFGR. LOGO - DESIGNATED AREA
    - E. SUCCESSFUL ELECTRICAL BOARD TEST
    - F. LOT NUMBER - DESIGNATED AREA
  - NON-FUNCTIONAL PADS MAY BE REMOVED FROM INNER SIGNAL LAYERS AT MFGR. DISCRETION.
  - IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY TEAR DROP PADS TO MAINTAIN ANNULAR RING AT PAD TO CIRCUIT INTERFACE ONLY AND MUST INSURE ELECTRICAL INTEGRITY. REPAIRS PER IPC-R-700 ARE ALLOWED.
  - MODIFICATIONS TO THE ARTWORK, OTHER THAN THOSE DESCRIBED ON THE FABRICATION DRAWING, ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
  - VIAS IN THIS AREA ARE TO BE FILLED USING A NON-CONDUCTIVE FILLER.

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FIDUCIALS; TOP & BOTTOM ARE MIRRORED COPIES, (.100, .275), & (7.400, 4.400).



### 6 LAYER STACKUP



CHARACTERISTIC IMPEDANCE = 50 OHMS +/- 10%  
 ARTWORK LINE WIDTH FOR IMPEDANCE CONTROLLED LINES = 0.0675\"/>

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
x	8.0	PLATED	241
+	13.0	PLATED	187
.	16.0	PLATED	72
◇	20.0	PLATED	1
◊	35.0	PLATED	4
○	40.0	PLATED	49
◇	45.0	PLATED	26
△	63.0	PLATED	30
□	65.0	PLATED	2
◇	75.0	PLATED	3
□	90.0	PLATED	2

### PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX .010 +-1/32 +-2 .XXX .005 .XXX .0050	APPROVAL	DATE	 WMM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER X	ddMMyy				
	HARDWARE SERVICES X	ddMMyy				
	HARDWARE SYSTEMS X	ddMMyy				
MATERIAL	TEST ENGINEER X	ddMMyy	TITLE FABRICATION AD9524 EVALUATION BD.			
COMPONENT ENGINEER X	ddMMyy					
TEST PROCESS X	ddMMyy					
HARDWARE RELEASE X	ddMMyy					
FINISH	DESIGNER RLO	07/22/09	SIZE	FSCM NO	DRAWING NUMBER	REV
	PTD ENGINEER X	ddMMyy	D	24355	9524EE01	C
	CHECKER X	ddMMyy	SCALE	1/1		SHEET 1 OF 1

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