IXYS

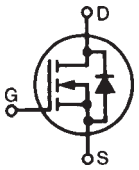
Advance Technical Information

# X-Class HiPerFET™ Power MOSFET

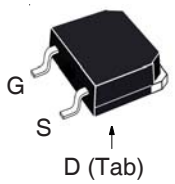
# IXFT40N85XHV
# IXFH40N85X

$V_{DSS}$ = 850V
$I_{D25}$ = 40A
$R_{DS(on)}$ ≤ 145mΩ

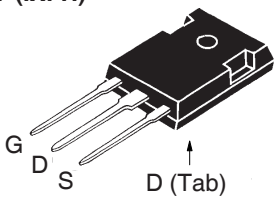
N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode




**TO-268HV (IXFT)**




**TO-247 (IXFH)**




G = Gate D = Drain
S = Source Tab = Drain

| Symbol | Test Conditions | Maximum Ratings | |
|---|---|---|---|
| $V_{DSS}$ | $T_J$ = 25°C to 150°C | 850 | V |
| $V_{DGR}$ | $T_J$ = 25°C to 150°C, $R_{GS}$ = 1MΩ | 850 | V |
| $V_{GSS}$ | Continuous | ±30 | V |
| $V_{GSM}$ | Transient | ±40 | V |
| $I_{D25}$ | $T_C$ = 25°C | 40 | A |
| $I_{DM}$ | $T_C$ = 25°C, Pulse Width Limited by $T_{JM}$ | 80 | A |
| $I_A$ | $T_C$ = 25°C | 20 | A |
| $E_{AS}$ | $T_C$ = 25°C | 1.5 | J |
| dv/dt | $I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$ | 50 | V/ns |
| $P_D$ | $T_C$ = 25°C | 860 | W |
| $T_J$ | | -55 ... +150 | °C |
| $T_{JM}$ | | 150 | °C |
| $T_{stg}$ | | -55 ... +150 | °C |
| $T_L$ | Maximum Lead Temperature for Soldering | 300 | °C |
| $T_{SOLD}$ | 1.6 mm (0.062in.) from Case for 10s | 260 | °C |
| $M_d$ | Mounting Torque (TO-247) | 1.13 / 10 | Nm/lb.in |
| Weight | TO-268HV | 4 | g |
| | TO-247 | 6 | g |

| Symbol | Test Conditions ($T_J$ = 25°C, Unless Otherwise Specified) | Characteristic Values Min. | Typ. | Max. | |
|---|---|---|---|---|---|
| $BV_{DSS}$ | $V_{GS}$ = 0V, $I_D$ = 1mA | 850 | | | V |
| $V_{GS(th)}$ | $V_{DS} = V_{GS}$, $I_D$ = 4mA | 3.5 | | 5.5 | V |
| $I_{GSS}$ | $V_{GS}$ = ±30V, $V_{DS}$ = 0V | | | ±100 | nA |
| $I_{DSS}$ | $V_{DS} = V_{DSS}$, $V_{GS}$ = 0V | | | 25 | μA |
| | $T_J$ = 125°C | | | 3 | mA |
| $R_{DS(on)}$ | $V_{GS}$ = 10V, $I_D = 0.5 \bullet I_{D25}$, Note 1 | | | 145 | mΩ |

**Features**

- International Standard Packages
- High Voltage Package
- Low $R_{DS(ON)}$ and $Q_G$
- Avalanche Rated
- Low Package Inductance

**Advantages**

- High Power Density
- Easy to Mount
- Space Savings

**Applications**

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

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| Symbol | Test Conditions ($T_J$ = 25°C, Unless Otherwise Specified) | Characteristic Values Min. | Typ. | Max | |
|---|---|---|---|---|---|
| $g_{fs}$ | $V_{DS}$ = 10V, $I_D$ = 0.5 • $I_{D25}$, Note 1 | 13 | 21 | | S |
| $R_{Gi}$ | Gate Input Resistance | | 0.7 | | Ω |
| $C_{iss}$ | $V_{GS}$ = 0V, $V_{DS}$ = 25V, f = 1MHz | | 3700 | | pF |
| $C_{oss}$ | | | 3690 | | pF |
| $C_{rss}$ | | | 57 | | pF |
| | **Effective Output Capacitance** | | | | |
| $C_{o(er)}$ | Energy related; $V_{GS}$ = 0V, $V_{DS}$ = 0.8 • $V_{DSS}$ | | 104 | | pF |
| $C_{o(tr)}$ | Time related; $V_{GS}$ = 0V, $V_{DS}$ = 0.8 • $V_{DSS}$ | | 520 | | pF |
| $t_{d(on)}$ | **Resistive Switching Times** | | 27 | | ns |
| $t_r$ | $V_{GS}$ = 10V, $V_{DS}$ = 0.5 • $V_{DSS}$, $I_D$ = 0.5 • $I_{D25}$ | | 11 | | ns |
| $t_{d(off)}$ | $R_G$ = 3Ω (External) | | 63 | | ns |
| $t_f$ | | | 10 | | ns |
| $Q_{g(on)}$ | $V_{GS}$ = 10V, $V_{DS}$ = 0.5 • $V_{DSS}$, $I_D$ = 0.5 • $I_{D25}$ | | 98 | | nC |
| $Q_{gs}$ | | | 23 | | nC |
| $Q_{gd}$ | | | 55 | | nC |
| $R_{thJC}$ | | | | 0.145 | °C/W |
| $R_{thCS}$ | TO-247 | | 0.21 | | °C/W |

**Source-Drain Diode**

| Symbol | Test Conditions ($T_J$ = 25°C, Unless Otherwise Specified) | Characteristic Values Min. | Typ. | Max | |
|---|---|---|---|---|---|
| $I_S$ | $V_{GS}$ = 0V | | | 40 | A |
| $I_{SM}$ | Repetitive, pulse Width Limited by $T_{JM}$ | | | 160 | A |
| $V_{SD}$ | $I_F$ = $I_S$, $V_{GS}$ = 0V, Note 1 | | | 1.4 | V |
| $t_{rr}$ | $I_F$ = 20A, -di/dt = 100A/μs | | 200 | | ns |
| $Q_{RM}$ | $V_R$ = 100V | | 1.6 | | μC |
| $I_{RM}$ | | | 16.0 | | A |

Note 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

**ADVANCE TECHNICAL INFORMATION**

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,860,072 4,881,106 4,931,844 5,017,508 5,034,796 5,049,961 5,063,307 5,187,117 5,237,481 5,381,025 5,486,715 6,162,665 6,259,123B1 6,306,728B1 6,404,065B1 6,534,343 6,583,505 6,683,344 6,710,405B2 6,710,463 6,727,585 6,759,692 6,771,478B2 7,005,734B2 7,063,975B2 7,071,537 7,157,338B2

**Fig. 1. Output Characteristics @ $T_J$ = 25ºC**

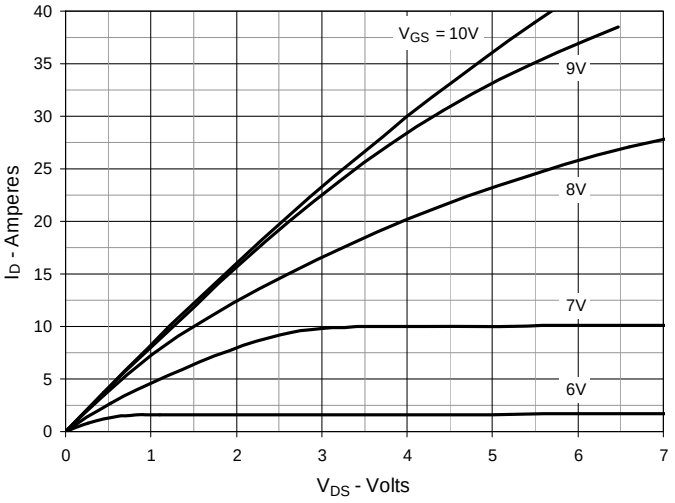



**Fig. 2. Extended Output Characteristics @ $T_J$ = 25ºC**

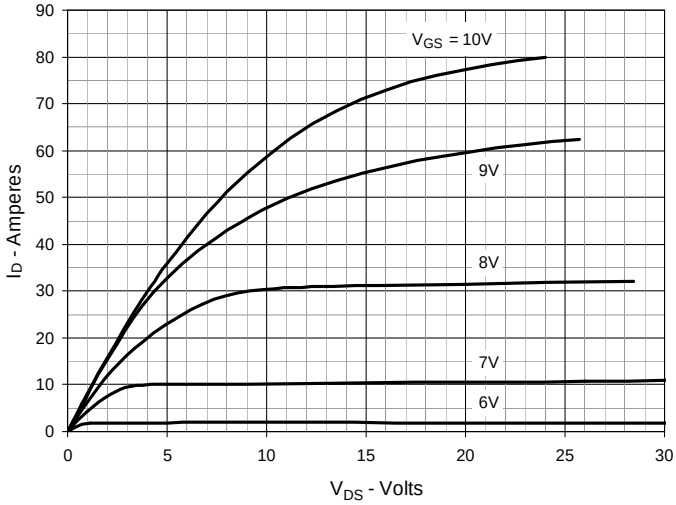



**Fig. 3. Output Characteristics @ $T_J$ = 125ºC**

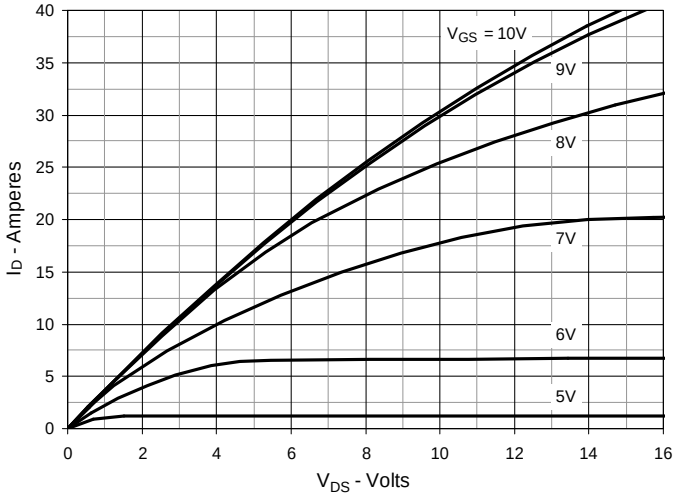



**Fig. 4. $R_{DS(on)}$ Normalized to $I_D$ = 20A Value vs. Junction Temperature**

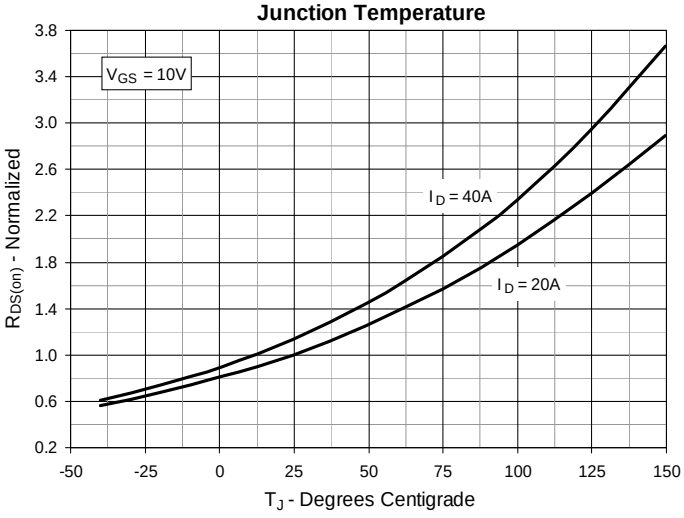



**Fig. 5. $R_{DS(on)}$ Normalized to $I_D$ = 20A Value vs. Drain Current**

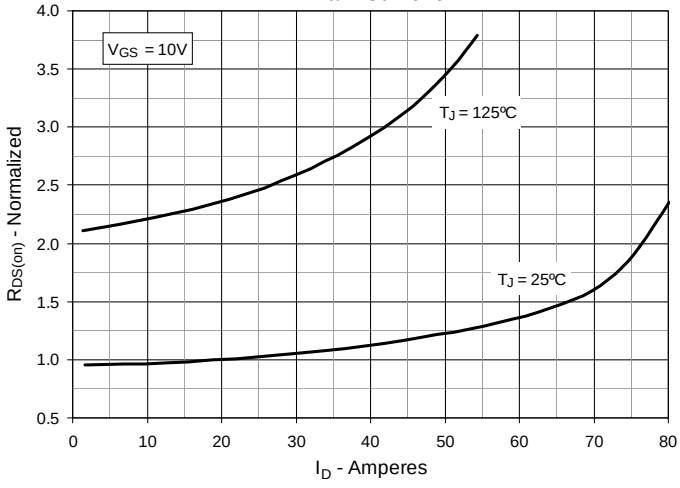
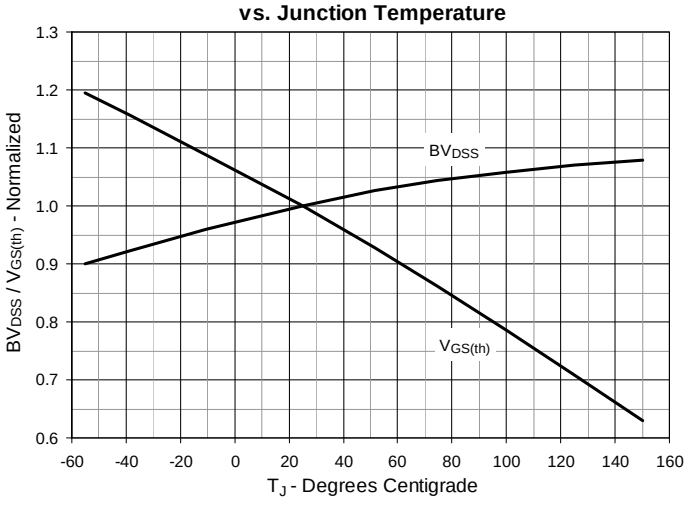



**Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature**




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**Fig. 7. Maximum Drain Current vs. Case Temperature**

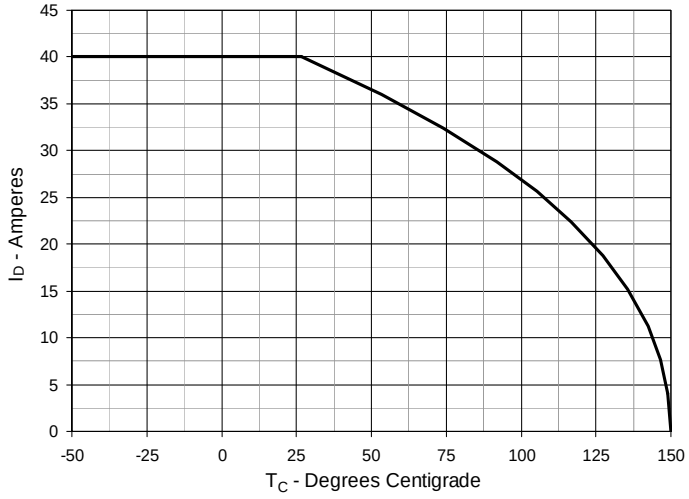



**Fig. 8. Input Admittance**

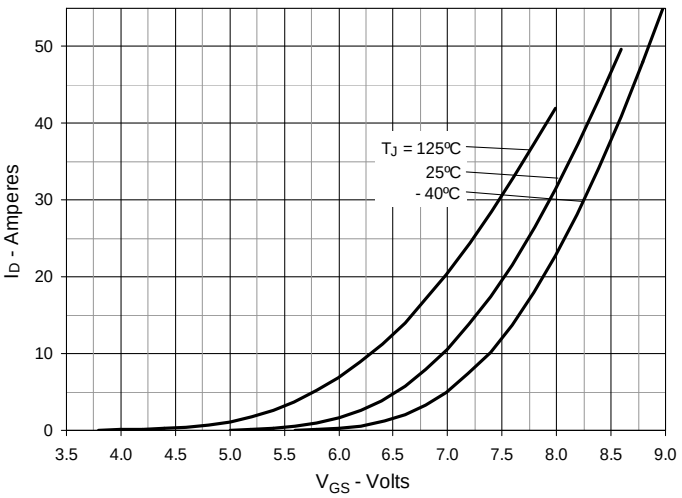



**Fig. 9. Transconductance**

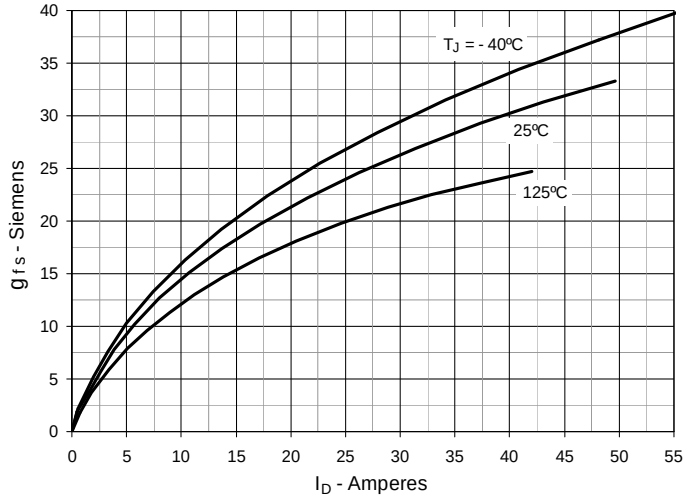



**Fig. 10. Forward Voltage Drop of Intrinsic Diode**

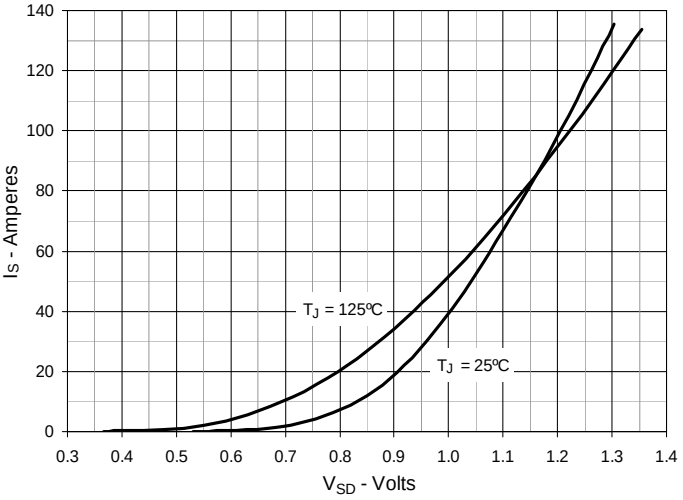



**Fig. 11. Gate Charge**

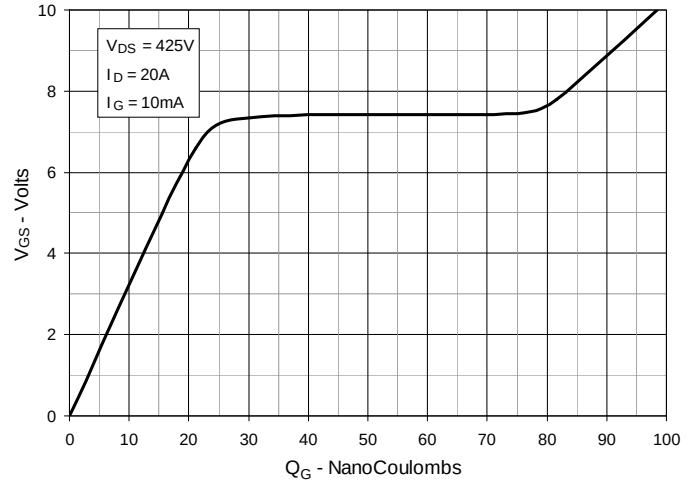
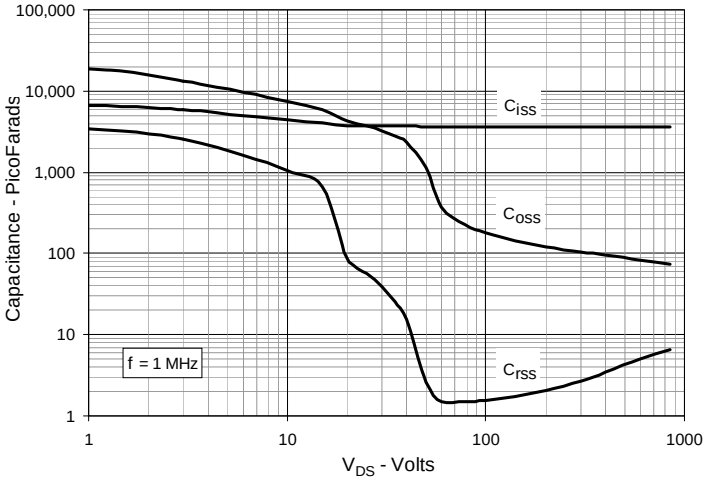



**Fig. 12. Capacitance**




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**Fig. 13. Output Capacitance Stored Energy**

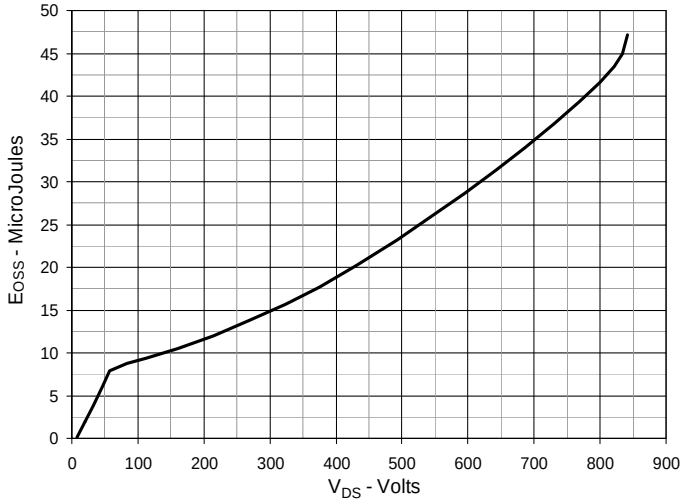



**Fig. 14. Forward-Bias Safe Operating Area**

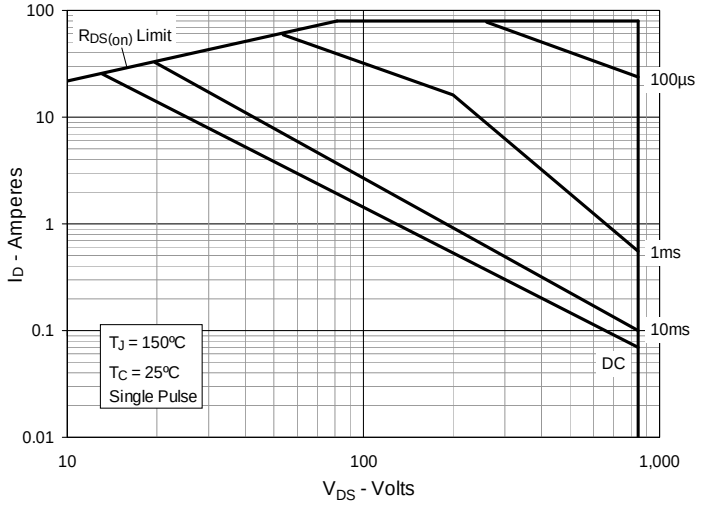
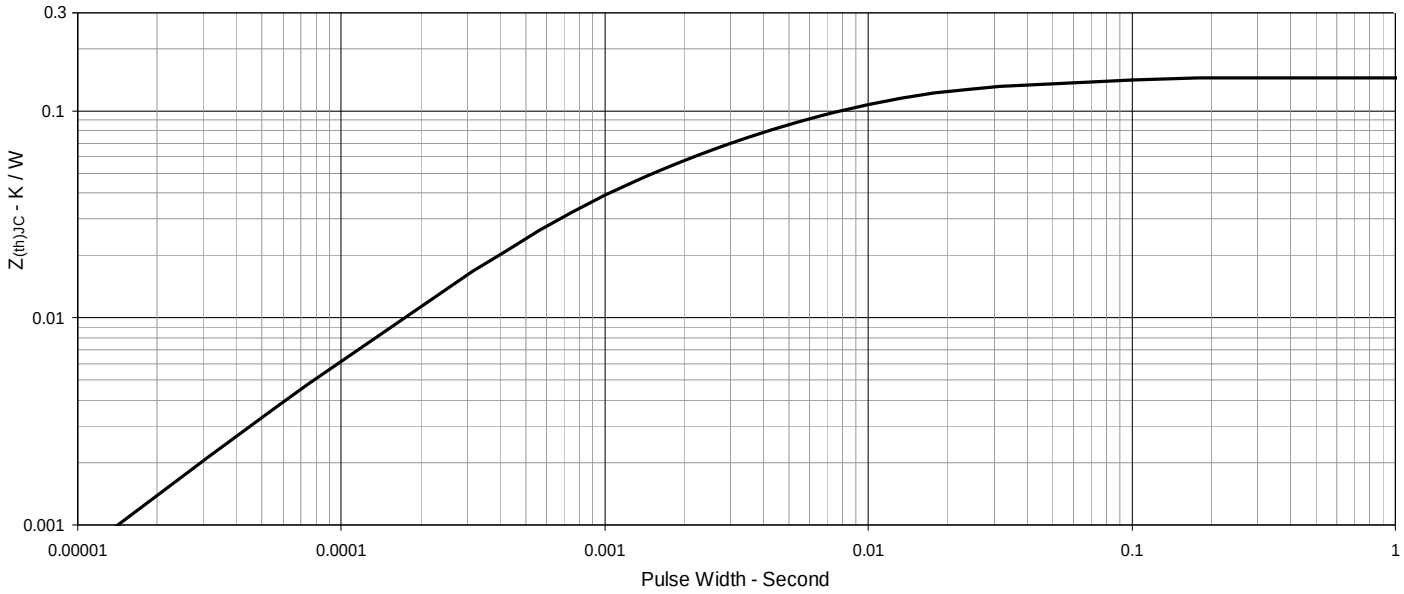
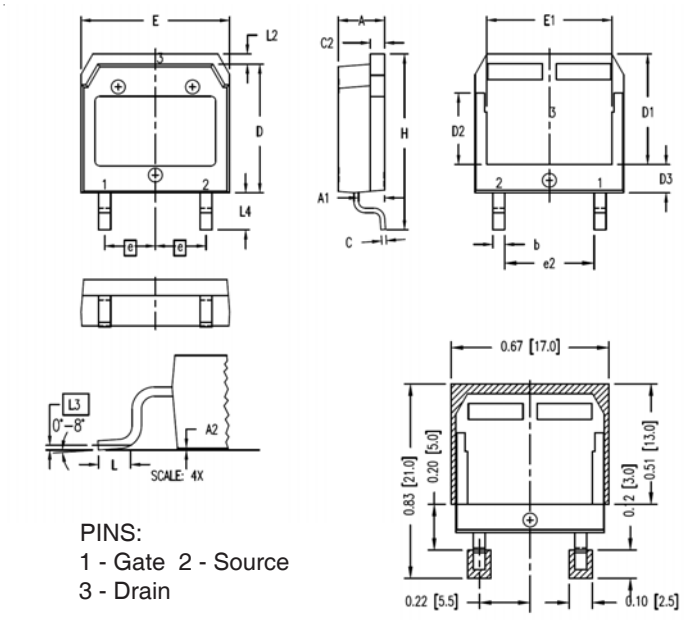



**Fig. 15. Maximum Transient Thermal Impedance**




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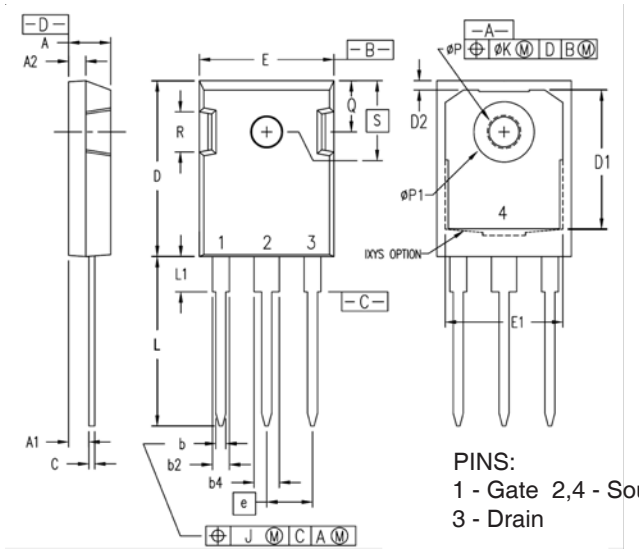
## TO-268HV Outline




PINS:
1 - Gate 2 - Source
3 - Drain

| SYM | INCHES | | MILLIMETER | |
|---|---|---|---|---|
| | MIN | MAX | MIN | MAX |
| A | .193 | .201 | 4.90 | 5.10 |
| A1 | .106 | .114 | 2.70 | 2.90 |
| A2 | .001 | .010 | 0.02 | 0.25 |
| b | .045 | .057 | 1.15 | 1.45 |
| C | .016 | .026 | 0.40 | 0.65 |
| C2 | .057 | .063 | 1.45 | 1.60 |
| D | .543 | .551 | 13.80 | 14.00 |
| D1 | .465 | .476 | 11.80 | 12.10 |
| D2 | .295 | .307 | 7.50 | 7.80 |
| D3 | .114 | .126 | 2.90 | 3.20 |
| E | .624 | .632 | 15.85 | 16.05 |
| E1 | .524 | .535 | 13.30 | 13.60 |
| e | .215 BSC | | 5.45 BSC | |
| (e2) | .374 | .386 | 9.50 | 9.80 |
| H | .736 | .752 | 18.70 | 19.10 |
| L | .067 | .079 | 1.70 | 2.00 |
| L2 | .039 | .045 | 1.00 | 1.15 |
| L3 | .010 BSC | | 0.25 BSC | |
| L4 | .150 | .161 | 3.80 | 4.10 |

## TO-247 Outline




PINS:
1 - Gate 2,4 - Source
3 - Drain

| SYM | INCHES | | MILLIMETERS | |
|---|---|---|---|---|
| | MIN | MAX | MIN | MAX |
| A | .190 | .205 | 4.83 | 5.21 |
| A1 | .090 | .100 | 2.29 | 2.54 |
| A2 | .075 | .085 | 1.91 | 2.16 |
| b | .045 | .055 | 1.14 | 1.40 |
| b2 | .075 | .087 | 1.91 | 2.20 |
| b4 | .115 | .126 | 2.92 | 3.20 |
| C | .024 | .031 | 0.61 | 0.80 |
| D | .819 | .840 | 20.80 | 21.34 |
| D1 | .650 | .690 | 16.51 | 17.53 |
| D2 | .035 | .050 | 0.89 | 1.27 |
| E | .620 | .635 | 15.75 | 16.13 |
| E1 | .545 | .565 | 13.84 | 14.35 |
| e | .215 BSC | | 5.45 BSC | |
| J | -- | .010 | -- | 0.25 |
| K | -- | .025 | -- | 0.64 |
| L | .780 | .810 | 19.81 | 20.57 |
| L1 | .150 | .170 | 3.81 | 4.32 |
| øP | .140 | .144 | 3.55 | 3.65 |
| øP1 | .275 | .290 | 6.99 | 7.37 |
| Q | .220 | .244 | 5.59 | 6.20 |
| R | .170 | .190 | 4.32 | 4.83 |
| S | .242 BSC | | 6.15 BSC | |

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