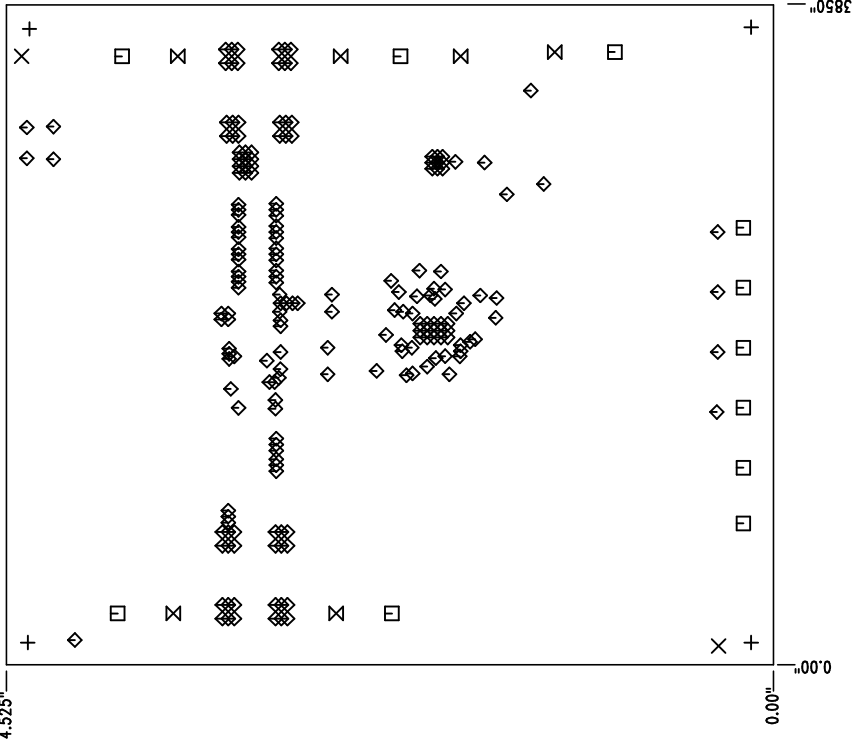
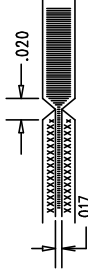


A cross-sectional diagram of a four-layer printed circuit board (PCB). The layers are labeled on the right side with leader lines pointing to their respective layers in the stack. From top to bottom, the layers are: L1 - TOP SIDE, L2 - GND PLANE, L3 - GND PLANE/TRACE, and L4 - BOTTOM SIDE. The diagram shows the physical structure of the board, including the core, prepreg, and copper layers.






NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE: PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: -SMDsC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
- FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
- 8 DO NOT ALTER SOLDER MASK MAINTAIN .0018" OVERSIZE
ON SMT PADS. A .005" WEBBING IS REQUIRED BETWEEN SMD PADS.
9. SCORING FOR PANELIZED PCB: "PRODUCTION FAB ONLY"



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.095	11	□	YES	+/-0.003
0.01	195	◇	YES	+/-0.003
0.204	6	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON DECIMALS: 0.1X = ±0.015" 0.01X = ±0.005" INTERPRET DIM AND TOL. PER ASME Y14.5M-1994	APPROVALS	 8330 UCARTRY BLVD MULTIPLETS, CA 96035 PH: (408)432-9000 www.linear.com	8330 UCARTRY BLVD MULTIPLETS, CA 96035 PH: (408)432-9000 www.linear.com
PCB DES. HZ	PCB DES. HZ	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
APP ENG. Charlie Z.	APP ENG. Charlie Z.	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
THIRD ANGLE PROJECTION	THIRD ANGLE PROJECTION	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
		LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
DO NOT SCALE DRAWING	DO NOT SCALE DRAWING	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
SCALE: NONE	SCALE: NONE	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
FILENAME: DC2044A-2.PCB	FILENAME: DC2044A-2.PCB	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY
SHT 1 of 1	SHT 1 of 1	LINEAR TECHNOLOGY	LINEAR TECHNOLOGY