



ON Semiconductor®

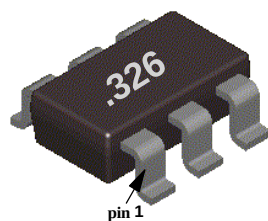
## FDC6326L Integrated Load Switch

### General Description

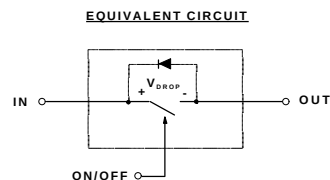
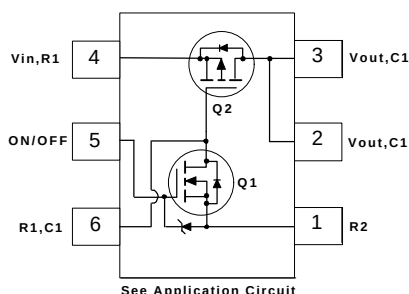
This device is particularly suited for compact power management in portable electronic equipment where 3V to 20V input and 1.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT™-6 package.

### Features

- $V_{\text{DROP}}=0.20\text{V}$  @  $V_{\text{IN}}=12\text{V}$ ,  $I_{\text{L}}=1.5\text{A}$ ,  $R_{\text{DS(ON)}} = 0.125\ \Omega$   
 $V_{\text{DROP}}=0.20\text{V}$  @  $V_{\text{IN}}=5\text{V}$ ,  $I_{\text{L}}=1\text{A}$ ,  $R_{\text{DS(ON)}} = 0.20\ \Omega$ .
- SuperSOT™-6 package design using copper lead frame for superior thermal and electrical capabilities.



SuperSOT™-6



### Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

| Symbol                         | Parameter                                                                    | FDC6326L   | Units            |
|--------------------------------|------------------------------------------------------------------------------|------------|------------------|
| $V_{\text{IN}}$                | Input Voltage Range                                                          | 3 - 20     | V                |
| $V_{\text{ON/OFF}}$            | On/Off Voltage Range                                                         | 2.5 - 8    | V                |
| $I_{\text{L}}$                 | Load Current - Continuous (Note 1)                                           | 1.8        | A                |
|                                | - Pulsed (Note 1 & 3)                                                        | 5          |                  |
| $P_{\text{D}}$                 | Maximum Power Dissipation (Note 2)                                           | 0.7        | W                |
| $T_{\text{J}}, T_{\text{STG}}$ | Operating and Storage Temperature Range                                      | -55 to 150 | $^\circ\text{C}$ |
| ESD                            | Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm) | 6          | kV               |

### THERMAL CHARACTERISTICS

|                       |                                                  |     |                    |
|-----------------------|--------------------------------------------------|-----|--------------------|
| $R_{\theta\text{JA}}$ | Thermal Resistance, Junction-to-Ambient (Note 2) | 180 | $^\circ\text{C/W}$ |
| $R_{\theta\text{JC}}$ | Thermal Resistance, Junction-to-Case (Note 2)    | 60  | $^\circ\text{C/W}$ |

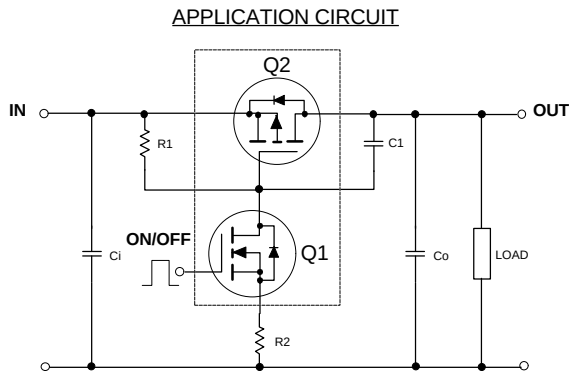
## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

| Symbol                             | Parameter                             | Conditions                                                                       | Min | Typ   | Max   | Units |
|------------------------------------|---------------------------------------|----------------------------------------------------------------------------------|-----|-------|-------|-------|
| <b>OFF CHARACTERISTICS</b>         |                                       |                                                                                  |     |       |       |       |
| I <sub>FL</sub>                    | Forward Leakage Current               | V <sub>IN</sub> = 20 V, V <sub>ON/OFF</sub> = 0 V                                |     |       | 1     | μA    |
| <b>ON CHARACTERISTICS</b> (Note 3) |                                       |                                                                                  |     |       |       |       |
| V <sub>DROP</sub>                  | Conduction Voltage Drop               | V <sub>IN</sub> = 12 V, V <sub>ON/OFF</sub> = 3.3 V, I <sub>L</sub> = 1.5 A      |     | 0.15  | 0.2   | V     |
|                                    |                                       | V <sub>IN</sub> = 5 V, V <sub>ON/OFF</sub> = 3.3 V, I <sub>L</sub> = 1 A         |     | 0.14  | 0.2   |       |
| R <sub>DS(ON)</sub>                | Q <sub>2</sub> - Static On-Resistance | V <sub>GS</sub> = -12 V, I <sub>D</sub> = -1.9 A                                 |     | 0.095 | 0.125 | Ω     |
|                                    |                                       | V <sub>GS</sub> = -5 V, I <sub>D</sub> = -1.5 A                                  |     | 0.14  | 0.2   |       |
| I <sub>L</sub>                     | Load Current                          | V <sub>DROP</sub> = 0.125 V, V <sub>IN</sub> = 12 V, V <sub>ON/OFF</sub> = 3.3 V | 1   |       |       | A     |
|                                    |                                       | V <sub>DROP</sub> = 0.20 V, V <sub>IN</sub> = 5 V, V <sub>ON/OFF</sub> = 3.3 V   | 1   |       |       |       |

Notes:

1. V<sub>IN</sub> = 20V, V<sub>ON/OFF</sub> = 8V, T<sub>J</sub> = 25°C
2. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>θJC</sub> is guaranteed by design while R<sub>θCA</sub> is determined by the user's board design.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%.

## FDC6326L Load Switch Application



### External Component Recommendation

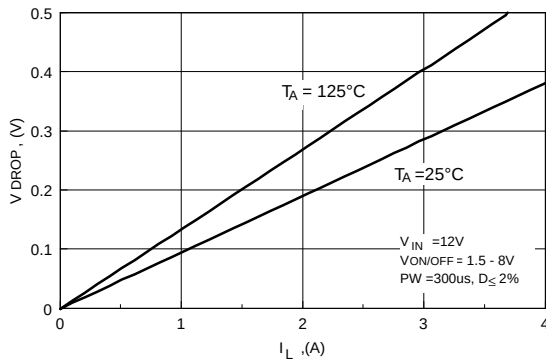
First select R2, 100 - 1kΩ, for Slew Rate control.

C1 ≤ 1000pF can be added in addition to R2 for further In-rush current control.

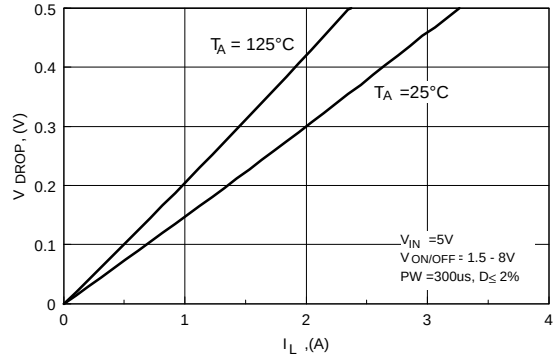
Then select R1 such that R1/R2 ratio maintains between 10 - 100. R1 is required to turn Q2 off.

For SPICE simulation, users can download a "FDC6326L.MOD" Spice model from ON Semiconductor Web Site at [www.onsemi.com](http://www.onsemi.com)

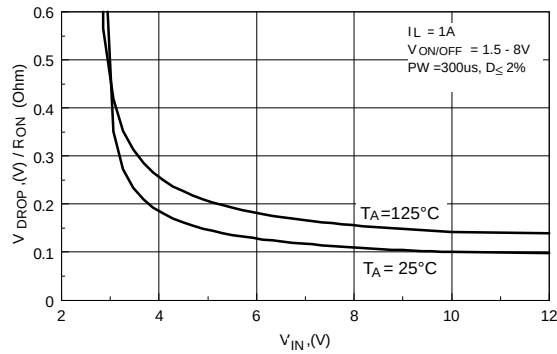
# **Typical Electrical Characteristics** ( $T_A = 25^\circ\text{C}$ unless otherwise noted )



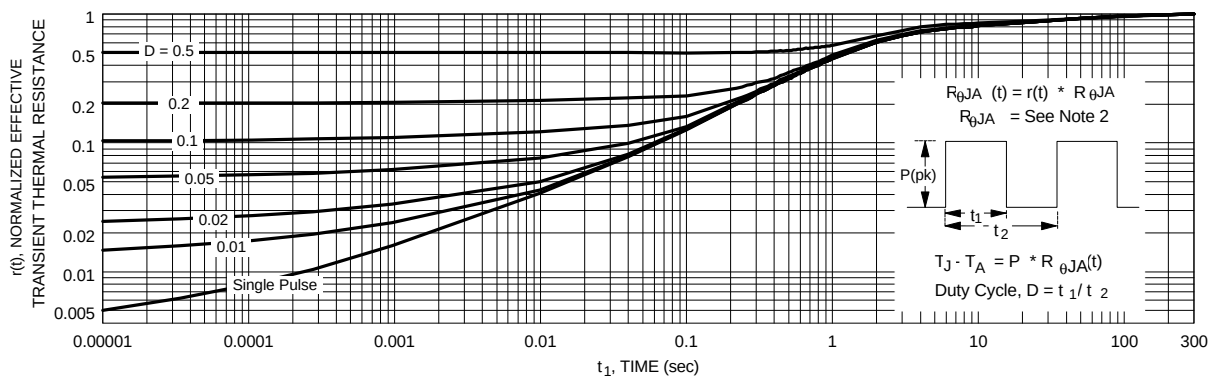
**Figure 1. Conduction Voltage Drop Variation with Load Current.**



**Figure 2. Conduction Voltage Drop Variation with Load Current.**



**Figure 3. On-Resistance Variation with Input Voltage.**



**Figure 4. Transient Thermal Response Curve.**

Thermal characterization performed on the conditions described in Note 2.

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada

**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910

**Japan Customer Focus Center**  
Phone: 81-3-5817-1050

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)

**Order Literature:** <http://www.onsemi.com/orderlit>

For additional information, please contact your local  
Sales Representative