

# NLAS4501

## Single SPST Analog Switch

The NLAS4501 is an analog switch manufactured in sub-micron silicon-gate CMOS technology. It achieves very low  $R_{ON}$  while maintaining extremely low power dissipation. The device is a bilateral switch suitable for switching either analog or digital signals, which may vary from zero to full supply voltage.

The NLAS4501 is pin-for-pin compatible with the MAX4501. The NLAS4501 can be used as a direct replacement for the MAX4501 in all 2.0 V to 5.5 V applications where a  $R_{ON}$  performance improvement is required.

The Enable pin is compatible with standard CMOS outputs when supply voltage is nominal 5.0 Volts. It is also over-voltage tolerant, making it a very useful logic level translator.

- Guaranteed  $R_{ON}$  of 32  $\Omega$  at 5.5 V
- Low Power Dissipation:  $I_{CC} = 2 \mu A$
- Provides Voltage translation for many different voltage levels  
3.3 to 5.0 V, Enable pin may go as high as +5.5 Volts  
1.8 to 3.3 V  
1.8 to 2.5 V
- Improved version of MAX4501 (at any voltage between 2 and 5.5 Volts)
- Chip Complexity: FETs 11
- Pb-Free Packages are Available

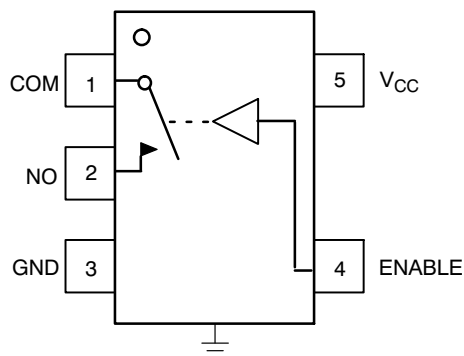


Figure 1. Pinout (Top View)



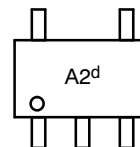
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<http://onsemi.com>

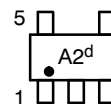
### MARKING DIAGRAMS



SC70-5/SC-88A/SOT-353  
DF SUFFIX  
CASE 419A



SOT23-5/TSOP-5/SC59-5  
DT SUFFIX  
CASE 483



d = Date Code

### PIN ASSIGNMENT

| PIN ASSIGNMENT |          |
|----------------|----------|
| 1              | COM      |
| 2              | NO       |
| 3              | GND      |
| 4              | ENABLE   |
| 5              | $V_{CC}$ |

### FUNCTION TABLE

| On/Off Enable Input | State of Analog Switch |
|---------------------|------------------------|
| L                   | Off                    |
| H                   | On                     |

### ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

## MAXIMUM RATINGS

| Symbol                | Parameter                                                                                                     | Value                          | Unit |
|-----------------------|---------------------------------------------------------------------------------------------------------------|--------------------------------|------|
| V <sub>CC</sub>       | Positive DC Supply Voltage                                                                                    | − 0.5 to + 7.0                 | V    |
| V <sub>IN</sub>       | Digital Input Voltage (Enable)                                                                                | − 0.5 to + 7.0                 | V    |
| V <sub>IS</sub>       | Analog Output Voltage (V <sub>NO</sub> or V <sub>COM</sub> )                                                  | − 0.5 to V <sub>CC</sub> + 0.5 | V    |
| I <sub>IK</sub>       | DC Current, Into or Out of Any Pin                                                                            | ± 20                           | mA   |
| T <sub>STG</sub>      | Storage Temperature Range                                                                                     | − 65 to + 150                  | °C   |
| T <sub>L</sub>        | Lead Temperature, 1 mm from Case for 10 Seconds                                                               | 260                            | °C   |
| T <sub>J</sub>        | Junction Temperature under Bias                                                                               | + 150                          | °C   |
| θ <sub>JA</sub>       | Thermal Resistance<br>SC70-5/SC-88A (Note 1)<br>TSOP-5                                                        | 350<br>230                     | °C/W |
| P <sub>D</sub>        | Power Dissipation in Still Air at 85°C<br>SC70-5/SC-88A<br>TSOP-5                                             | 150<br>200                     | mW   |
| MSL                   | Moisture Sensitivity                                                                                          | Level 1                        |      |
| F <sub>R</sub>        | Flammability Rating<br>Oxygen Index: 30% – 35%                                                                | UL-94-VO (0.125 in)            |      |
| V <sub>ESD</sub>      | ESD Withstand Voltage<br>Human Body Model (Note 2)<br>Machine Model (Note 3)<br>Charged Device Model (Note 4) | > 2000<br>> 100<br>N/A         | V    |
| I <sub>Latch-Up</sub> | Latch-Up Performance Above V <sub>CC</sub> and Below GND at 85°C (Note 5)                                     | ± 300                          | mA   |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.
2. Tested to EIA/JESD22-A114-A.
3. Tested to EIA/JESD22-A115-A.
4. Tested to JESD22-C101-A.
5. Tested to EIA/JESD78.

## RECOMMENDED OPERATING CONDITIONS

| Symbol                          | Characteristics                                | Min                                                                | Max             | Unit |
|---------------------------------|------------------------------------------------|--------------------------------------------------------------------|-----------------|------|
| V <sub>CC</sub>                 | Positive DC Supply Voltage                     | 2.0                                                                | 5.5             | V    |
| V <sub>IN</sub>                 | Digital Input Voltage (Enable)                 | GND                                                                | 5.5             | V    |
| V <sub>IO</sub>                 | Static or Dynamic Voltage Across an Off Switch | GND                                                                | V <sub>CC</sub> | V    |
| V <sub>IS</sub>                 | Analog Input Voltage (NO, COM)                 | GND                                                                | V <sub>CC</sub> | V    |
| T <sub>A</sub>                  | Operating Temperature Range, All Package Types | −55                                                                | +125            | °C   |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise or Fall Time,<br>(Enable Input)     | V <sub>CC</sub> = 3.3 V ± 0.3 V<br>V <sub>CC</sub> = 5.0 V ± 0.5 V | 0<br>100<br>20  | ns/V |

## DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES

| Junction Temperature °C | Time, Hours | Time, Years |
|-------------------------|-------------|-------------|
| 80                      | 1,032,200   | 117.8       |
| 90                      | 419,300     | 47.9        |
| 100                     | 178,700     | 20.4        |
| 110                     | 79,600      | 9.4         |
| 120                     | 37,000      | 4.2         |
| 130                     | 17,800      | 2.0         |
| 140                     | 8,900       | 1.0         |

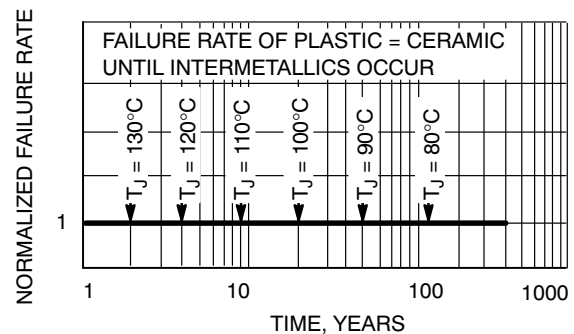


Figure 2. Failure Rate vs. Time Junction Temperature

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## DC CHARACTERISTICS – Digital Section (Voltages Referenced to GND)

| Symbol          | Parameter                                       | Condition                                           | V <sub>CC</sub>          | –40°C to +85°C             |                  |                            | Unit |
|-----------------|-------------------------------------------------|-----------------------------------------------------|--------------------------|----------------------------|------------------|----------------------------|------|
|                 |                                                 |                                                     |                          | Min                        | Typ              | Max                        |      |
| V <sub>IH</sub> | Minimum High-Level Input Voltage, Enable Inputs |                                                     | 2.0<br>3.0<br>4.5<br>5.5 | 1.5<br>2.1<br>3.15<br>3.85 | –<br>–<br>–<br>– | –<br>–<br>–<br>–           | V    |
| V <sub>IL</sub> | Maximum Low-Level Input Voltage, Enable Inputs  |                                                     | 2.0<br>3.0<br>4.5<br>5.5 | –<br>–<br>–<br>–           | –<br>–<br>–<br>– | 0.5<br>0.9<br>1.35<br>1.65 | V    |
| I <sub>IN</sub> | Maximum Input Leakage Current, Enable Inputs    | V <sub>IN</sub> = 5.5 V or GND                      | 0 V to 5.5 V             | –                          | ±0.1             | ±1.0                       | μA   |
| I <sub>CC</sub> | Maximum Quiescent Supply Current (per package)  | Enable and V <sub>IS</sub> = V <sub>CC</sub> or GND | 5.5                      | –                          | –                | 1.0                        | μA   |

## DC ELECTRICAL CHARACTERISTICS – Analog Section

| Symbol                | Parameter                              | Condition                                                                                                                                       | V <sub>CC</sub>   | –40°C to +85°C |                |                | Unit |
|-----------------------|----------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|----------------|----------------|----------------|------|
|                       |                                        |                                                                                                                                                 |                   | Min            | Typ            | Max            |      |
| R <sub>ON</sub>       | Maximum ON Resistance (Figures 8 – 12) | V <sub>IN</sub> = V <sub>IH</sub><br>V <sub>IS</sub> = V <sub>CC</sub> to GND<br>I <sub>IS</sub> = ≤10.0mA                                      | 3.0<br>4.5<br>5.5 | –<br>–<br>–    | 45<br>30<br>25 | 50<br>35<br>25 | Ω    |
| R <sub>FLAT(ON)</sub> | ON Resistance Flatness                 | V <sub>IN</sub> = V <sub>IH</sub><br>I <sub>IS</sub> = ≤10.0mA<br>V <sub>IS</sub> = 1V, 2V, 3.5V                                                | 4.5               | –              | 4.0            | 4.0            | Ω    |
| I <sub>NO(OFF)</sub>  | Off Leakage Current, Pin 2 (Figure 3)  | V <sub>IN</sub> = V <sub>IL</sub><br>V <sub>NO</sub> = 1.0 V, V <sub>COM</sub> = 4.5 V or<br>V <sub>COM</sub> = 1.0 V and V <sub>NO</sub> 4.5 V | 5.5               | –              | 1.0            | 100            | nA   |
| I <sub>COM(OFF)</sub> | Off Leakage Current, Pin 1 (Figure 3)  | V <sub>IN</sub> = V <sub>IL</sub><br>V <sub>NO</sub> = 4.5 V or 1.0 V<br>V <sub>COM</sub> = 1.0 V or 4.5 V                                      | 5.5               | –              | 1.0            | 100            | nA   |

## AC ELECTRICAL CHARACTERISTICS (Input t<sub>r</sub> = t<sub>f</sub> = 3.0 ns)

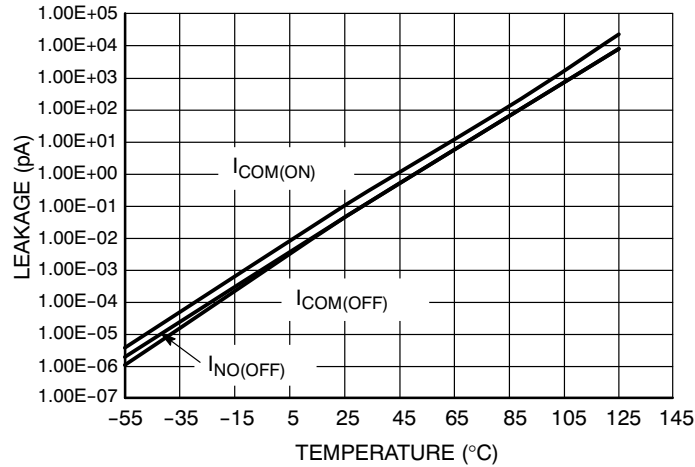
| Symbol           | Parameter     | Test Conditions                                                          | V <sub>CC</sub><br>(V)   | Guaranteed Max Limit |                           |                      |       |     |                      |        |     |                      | Unit |
|------------------|---------------|--------------------------------------------------------------------------|--------------------------|----------------------|---------------------------|----------------------|-------|-----|----------------------|--------|-----|----------------------|------|
|                  |               |                                                                          |                          | –55 to 25°C          |                           |                      | <85°C |     |                      | <125°C |     |                      |      |
|                  |               |                                                                          |                          | Min                  | Typ                       | Max                  | Min   | Typ | Max                  | Min    | Typ | Max                  |      |
| t <sub>ON</sub>  | Turn-On Time  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF<br>(Figures 4, 5, and 13) | 2.0<br>3.0<br>4.5<br>5.5 |                      | 7.0<br>5.0<br>4.5<br>4.5  | 14<br>10<br>9<br>9   |       |     | 16<br>12<br>11<br>11 |        |     | 16<br>12<br>11<br>11 | ns   |
| t <sub>OFF</sub> | Turn-Off Time | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF<br>(Figures 4, 5, and 13) | 2.0<br>3.0<br>4.5<br>5.5 |                      | 11.0<br>7.0<br>5.0<br>5.0 | 22<br>14<br>10<br>10 |       |     | 24<br>16<br>12<br>12 |        |     | 24<br>16<br>12<br>12 | ns   |

|                                    |                                         | Typical @ 25, V <sub>CC</sub> = 5.0 V |    |
|------------------------------------|-----------------------------------------|---------------------------------------|----|
| C <sub>IN</sub>                    | Maximum Input Capacitance, Select Input | 8                                     | pF |
| C <sub>NO</sub> or C <sub>NC</sub> | Analog I/O (switch off)                 | 10                                    |    |
| C <sub>COM(OFF)</sub>              | Common I/O (switch off)                 | 10                                    |    |
| C <sub>COM(ON)</sub>               | Feedthrough (switch on)                 | 20                                    |    |

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## ADDITIONAL APPLICATION CHARACTERISTICS (Voltages Referenced to GND Unless Noted)

| Symbol           | Parameter                                                       | Condition                                                                                                                                                                                                                       | V <sub>CC</sub><br>V | Limit             | Unit |
|------------------|-----------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------|-------------------|------|
|                  |                                                                 |                                                                                                                                                                                                                                 |                      | 25°C              |      |
| BW               | Maximum On-Channel -3dB Bandwidth or Minimum Frequency Response | V <sub>IS</sub> = 0 dBm<br>V <sub>IS</sub> centered between V <sub>CC</sub> and GND<br>(Figures 6 and 14)                                                                                                                       | 3.0<br>4.5<br>5.5    | 190<br>200<br>220 | MHz  |
| V <sub>ONL</sub> | Maximum Feedthrough On Loss                                     | V <sub>IS</sub> = 0 dBm @ 10 kHz<br>V <sub>IS</sub> centered between V <sub>CC</sub> and GND<br>(Figure 6)                                                                                                                      | 3.0<br>4.5<br>5.5    | -2<br>-2<br>-2    | dB   |
| V <sub>ISO</sub> | Off-Channel Isolation                                           | f = 100 kHz; V <sub>IS</sub> = 1 V RMS<br>V <sub>IS</sub> centered between V <sub>CC</sub> and GND<br>(Figures 6 and 15)                                                                                                        | 3.0<br>4.5<br>5.5    | -93               | dB   |
| Q                | Charge Injection<br>Enable Input to Common I/O                  | V <sub>IS</sub> = V <sub>CC</sub> to GND, F <sub>IS</sub> = 20 kHz<br>t <sub>r</sub> = t <sub>f</sub> = 3 ns<br>R <sub>IS</sub> = 0 Ω, C <sub>L</sub> = 1000 pF<br>Q = C <sub>L</sub> * ΔV <sub>OUT</sub><br>(Figures 7 and 16) | 3.0<br>5.5           | 1.5<br>3.0        | pC   |
| THD              | Total Harmonic Distortion<br>THD + Noise                        | F <sub>IS</sub> = 20 Hz to 1 MHz, R <sub>L</sub> = R <sub>gen</sub> = 600 Ω, C <sub>L</sub> = 50 pF<br>V <sub>IS</sub> = 3.0 V <sub>PP</sub> sine wave<br>V <sub>IS</sub> = 5.0 V <sub>PP</sub> sine wave<br>(Figure 17)        | 3.3<br>5.5           | 0.3<br>0.15       | %    |



**Figure 3. Switch Leakage vs. Temperature**

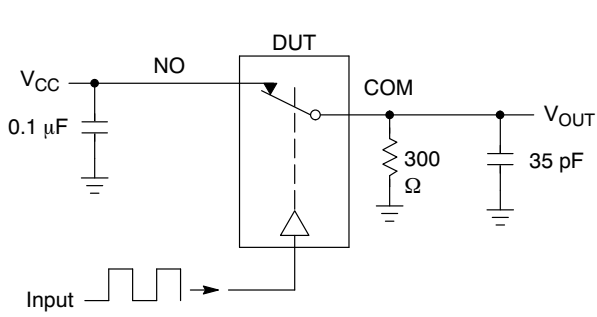


Figure 4.  $t_{ON}/t_{OFF}$

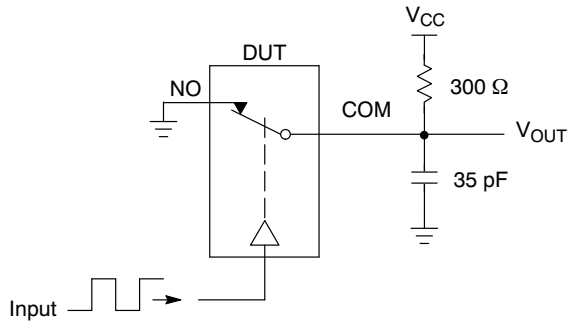
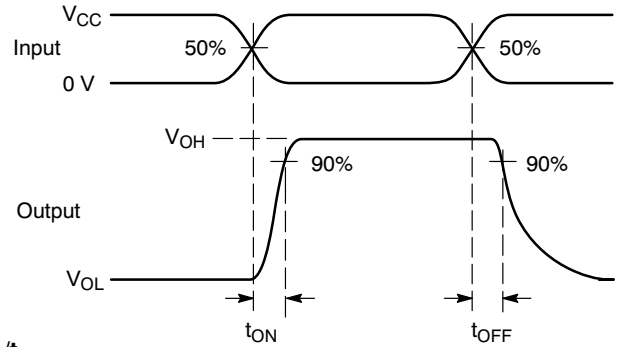
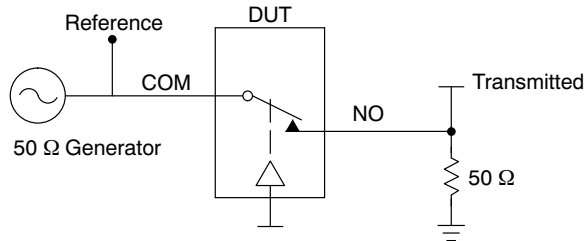
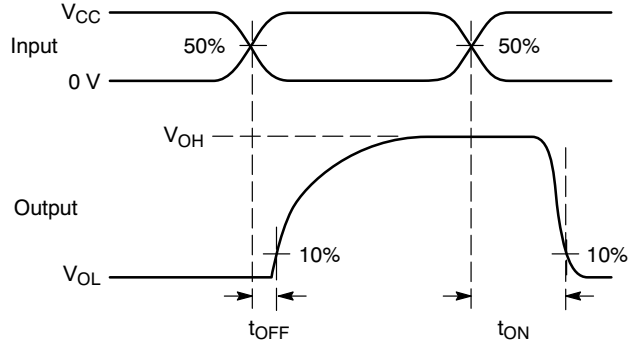


Figure 5.  $t_{ON}/t_{OFF}$



Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch.  $V_{ISO}$ , Bandwidth and  $V_{ONL}$  are independent of the input signal direction.  
 $V_{ISO}$  = Off Channel Isolation =  $20 \text{ Log } \left( \frac{V_{OUT}}{V_{IN}} \right)$  for  $V_{IN}$  at 100 kHz  
 $V_{ONL}$  = On Channel Loss =  $20 \text{ Log } \left( \frac{V_{OUT}}{V_{IN}} \right)$  for  $V_{IN}$  at 100 kHz to 50 MHz  
 Bandwidth (BW) = the frequency 3 dB below  $V_{ONL}$

Figure 6. Off Channel Isolation/On Channel Loss (BW)/Crosstalk  
(On Channel to Off Channel)/ $V_{ONL}$

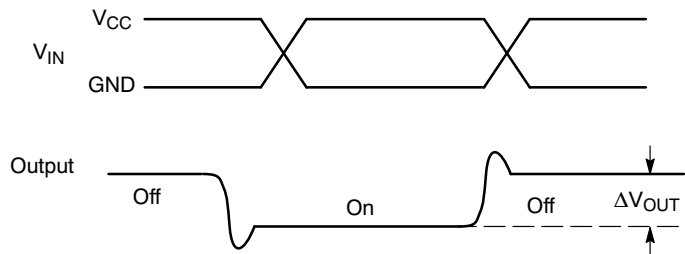
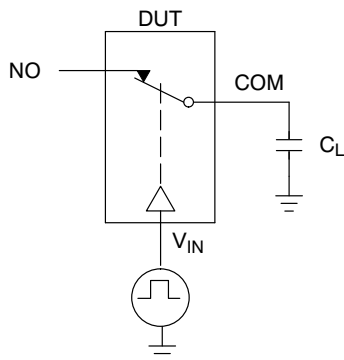


Figure 7. Charge Injection: (Q)

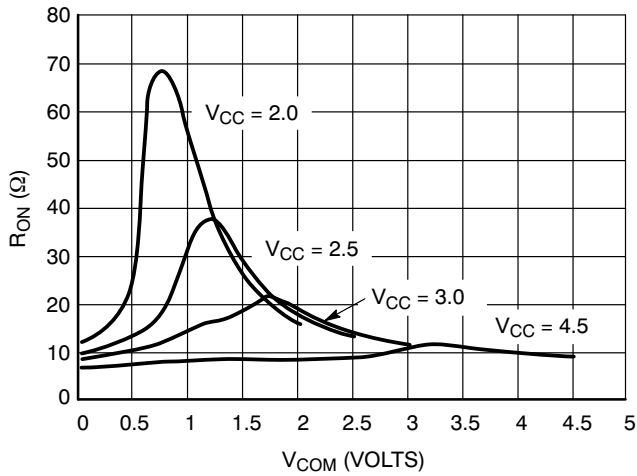


Figure 8.  $R_{ON}$  vs.  $V_{COM}$  and  $V_{CC}$  (@25°C)

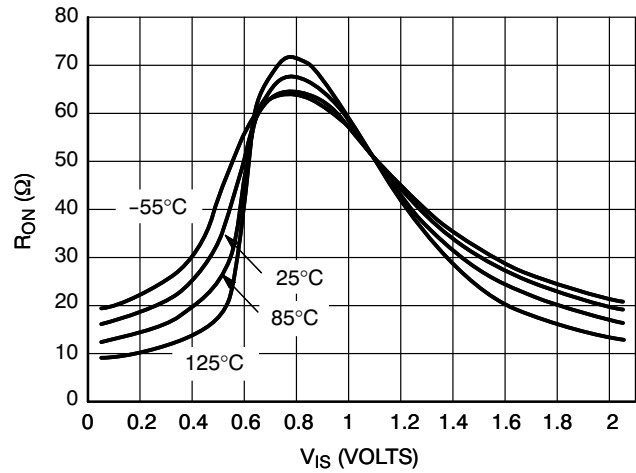


Figure 9.  $R_{ON}$  vs.  $V_{COM}$  and Temperature,  $V_{CC} = 2.0$  V

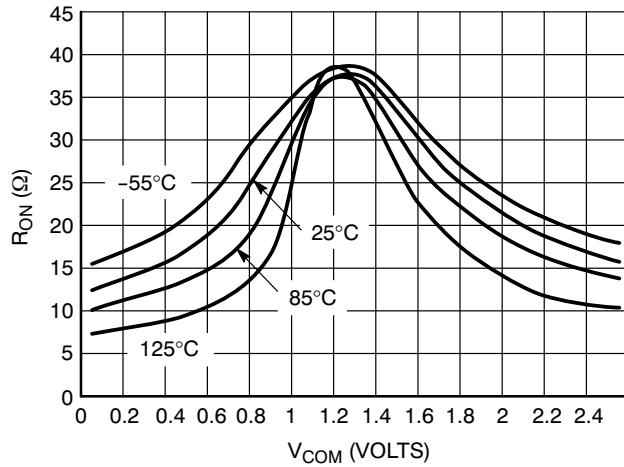


Figure 10.  $R_{ON}$  vs.  $V_{COM}$  and Temperature,  $V_{CC} = 2.5$  V

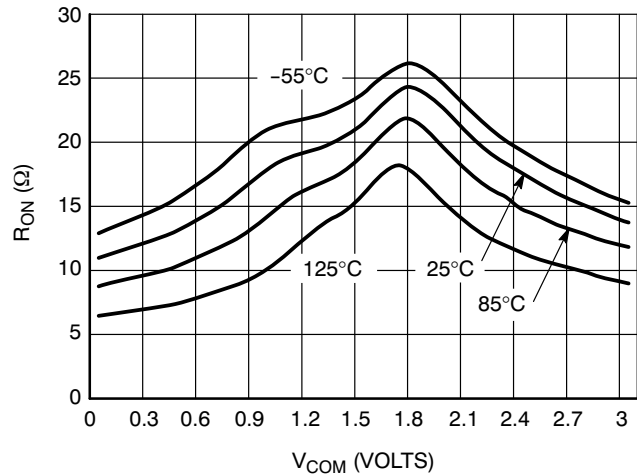


Figure 11.  $R_{ON}$  vs.  $V_{COM}$  and Temperature,  $V_{CC} = 3.0$  V

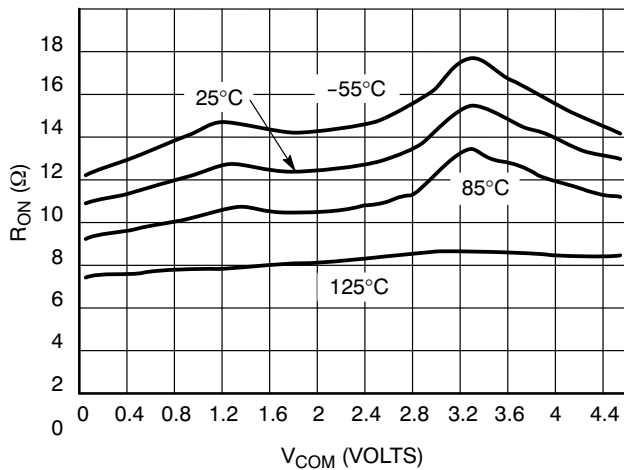


Figure 12.  $R_{ON}$  vs.  $V_{COM}$  and Temperature,  $V_{CC} = 4.5$  V

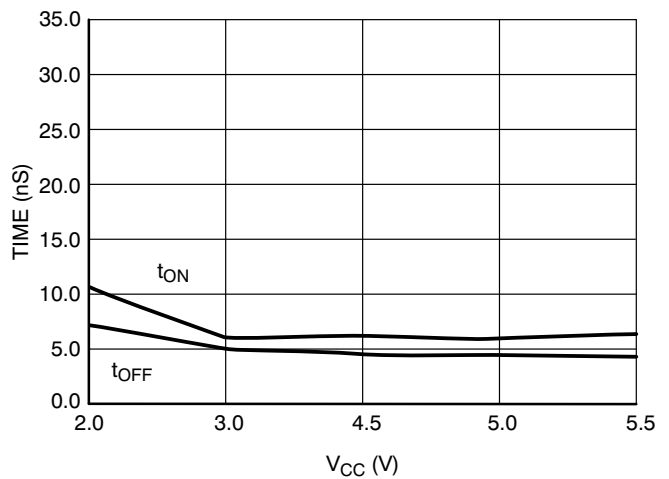
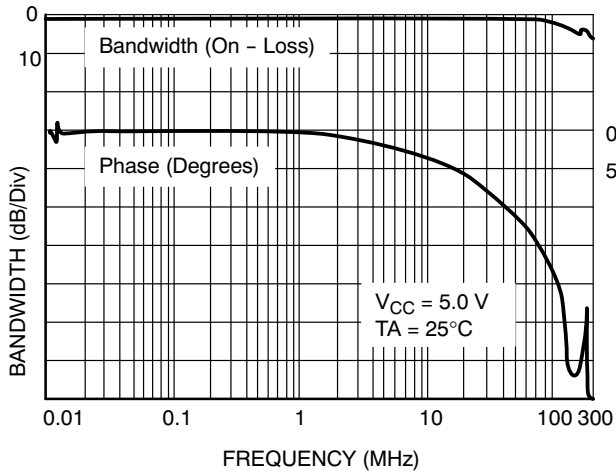
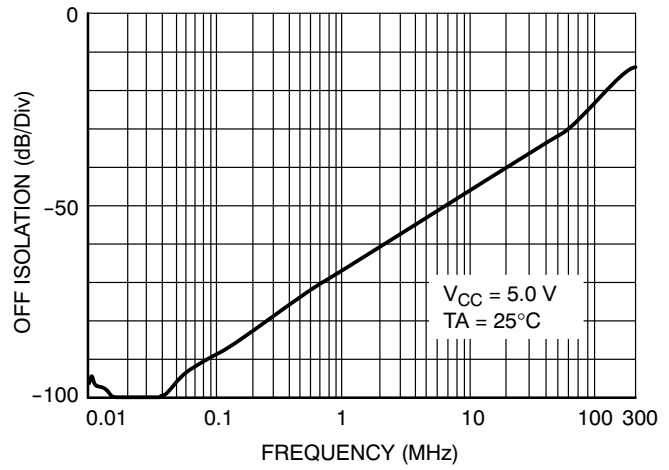


Figure 13. Switching Time vs. Supply Voltage,  $T = 25^\circ\text{C}$

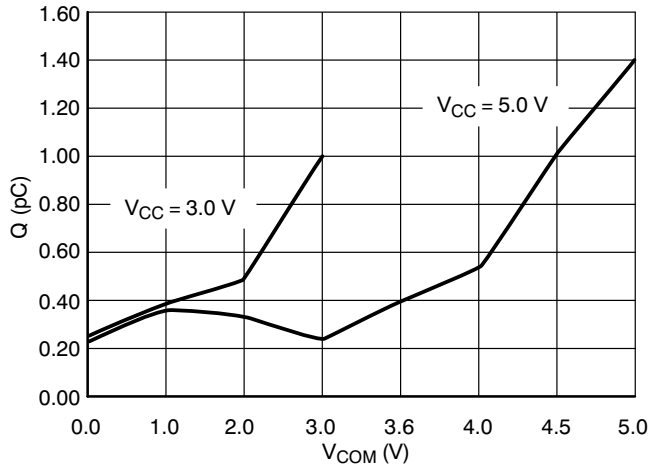
# NLAS4501



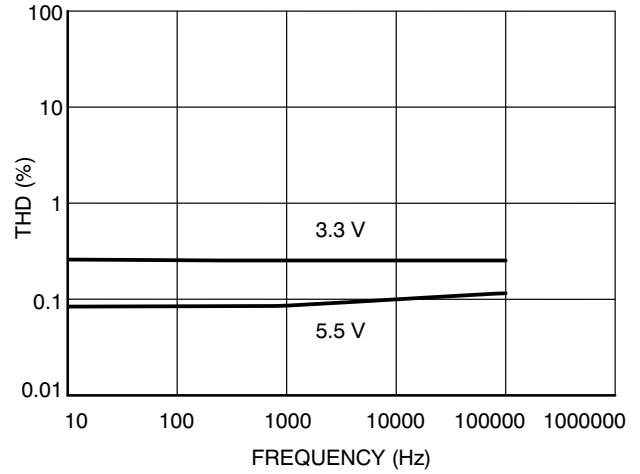
**Figure 14. ON Channel Bandwidth and Phase Shift Over Frequency**



**Figure 15. Off Channel Isolation**



**Figure 16. Charge Injection vs.  $V_{COM}$**



**Figure 17. THD vs. Frequency**

## DEVICE ORDERING INFORMATION

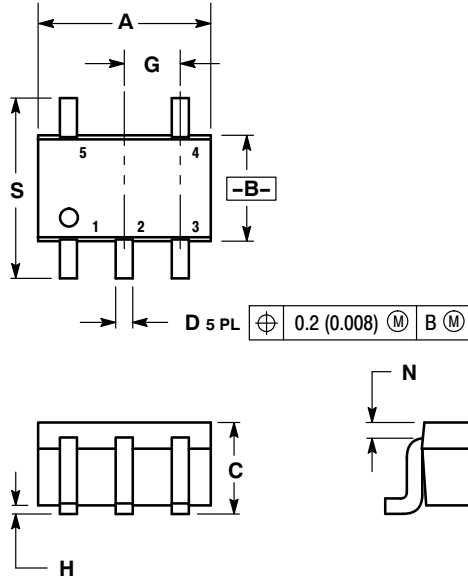
| Device Order Number | Device Nomenclature |            |                 |                |                    | Package Type               | Shipping†                             |
|---------------------|---------------------|------------|-----------------|----------------|--------------------|----------------------------|---------------------------------------|
|                     | Circuit Indicator   | Technology | Device Function | Package Suffix | Tape & Reel Suffix |                            |                                       |
| NLAS4501DFT2        | NL                  | AS         | 4501            | DF             | T2                 | SC-88A                     | 178 mm (7 )<br>3000 / Tape & Reel     |
| NLAS4501DFT2G       | NL                  | AS         | 4501            | DF             | T2                 | SC-88A<br>(Pb-Free)        | 178 mm (7 )<br>3000 / Tape & Reel     |
| NLAS4501DTT1        | NL                  | AS         | 4501            | DT             | T1                 | SOT-23/TSOP-5              | 178 mm (7 inch)<br>3000 / Tape & Reel |
| NLAS4501DTT1G       | NL                  | AS         | 4501            | DT             | T1                 | SOT-23/TSOP-5<br>(Pb-Free) | 178 mm (7 inch)<br>3000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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## PACKAGE DIMENSIONS

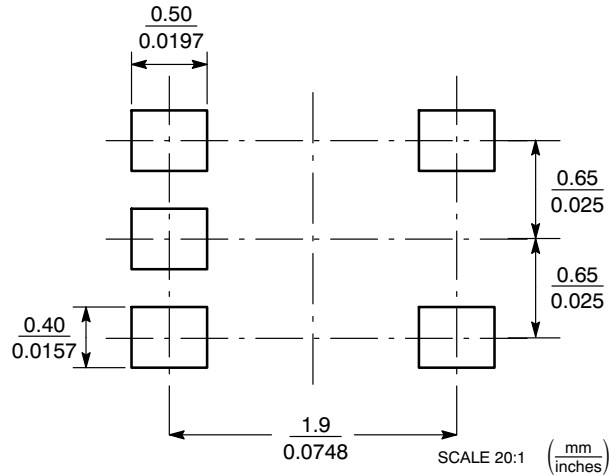
SC-88A, SOT-353, SC-70  
DF SUFFIX  
CASE 419A-02  
ISSUE J



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.
  4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

| DIM | INCHES    |       | MILLIMETERS |      |
|-----|-----------|-------|-------------|------|
|     | MIN       | MAX   | MIN         | MAX  |
| A   | 0.071     | 0.087 | 1.80        | 2.20 |
| B   | 0.045     | 0.053 | 1.15        | 1.35 |
| C   | 0.031     | 0.043 | 0.80        | 1.10 |
| D   | 0.004     | 0.012 | 0.10        | 0.30 |
| G   | 0.026 BSC |       | 0.65 BSC    |      |
| H   | ---       | 0.004 | ---         | 0.10 |
| J   | 0.004     | 0.010 | 0.10        | 0.25 |
| K   | 0.004     | 0.012 | 0.10        | 0.30 |
| N   | 0.008 REF |       | 0.20 REF    |      |
| S   | 0.079     | 0.087 | 2.00        | 2.20 |

## SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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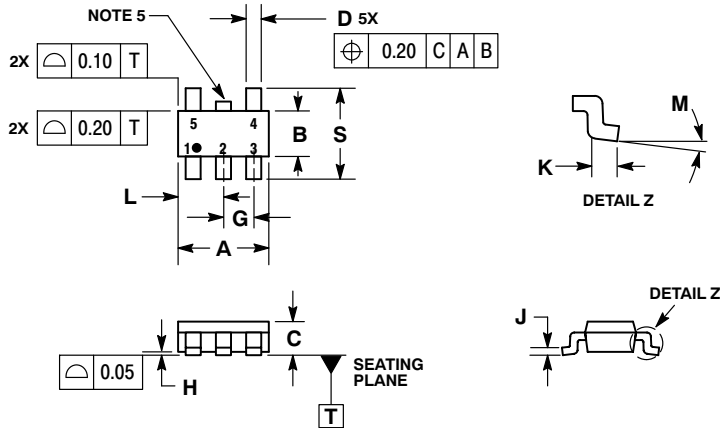
## PACKAGE DIMENSIONS

SOT23-5/TSOP-5/SC59-5

DT SUFFIX

CASE 483-02

ISSUE H

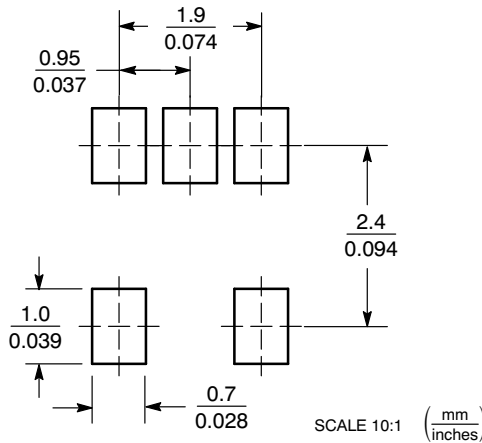


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
5. OPTIONAL CONSTRUCTION: AN ADDITIONAL TRIMMED LEAD IS ALLOWED IN THIS LOCATION. TRIMMED LEAD NOT TO EXTEND MORE THAN 0.2 FROM BODY.

| DIM | MILLIMETERS |      |
|-----|-------------|------|
|     | MIN         | MAX  |
| A   | 3.00 BSC    |      |
| B   | 1.50 BSC    |      |
| C   | 0.90        | 1.10 |
| D   | 0.25        | 0.50 |
| G   | 0.95 BSC    |      |
| H   | 0.01        | 0.10 |
| J   | 0.10        | 0.26 |
| K   | 0.20        | 0.60 |
| L   | 1.25        | 1.55 |
| M   | 0°          | 10°  |
| S   | 2.50        | 3.00 |

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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