Si5380
Data Sheet Errata for Product Revision D

This document contains information on the errata of product revision D of Si5380.

The device data sheet explains how to identify product revision, either from package marking or electronically.

Errata effective date: 29 July 2016.

Note: This document applies to Ordering Part Numbers (OPNs) which refer to product revision **D**. For example: Si5345A-**D**-GM or Si5345-**D**xxxxx-GM, where xxxxx is the custom OPN ID, and D refers to the product revision.

ERRATA DEFINITIONS

Impact Definition: Each erratum is marked with an impact, as defined below:

- Minor—Workaround(s) exists.
- Major—Errata that do not conform to the data sheet or standard.
- Information—The device behavior is not ideal but acceptable. Typically, the data sheet and/or ClockBuilder Pro may be changed to match or address the device behavior.

Table .1. Errata Status Summary

Erratum	Title/Problem	Impact	Workarounds	Resolution
1	INTRb pin activity while RSTb pin held low	Information	Yes	Will be fixed in a future silicon revision.

1. INTRb_pin_activity_while_RSTb_pin_held_low

Description

The INTRb pin voltage varies at approximately 5 MHz while the RSTb reset input pin is held low (active).

Impact

Activity on the INTRb pin while RSTb is held low may appear as an interrupt to the system.

Note: The INTRb pin is driven low (active) when exiting the reset state.

Workaround

The INTRb signal can be ignored or masked by the system until RSTb is released or driven high.

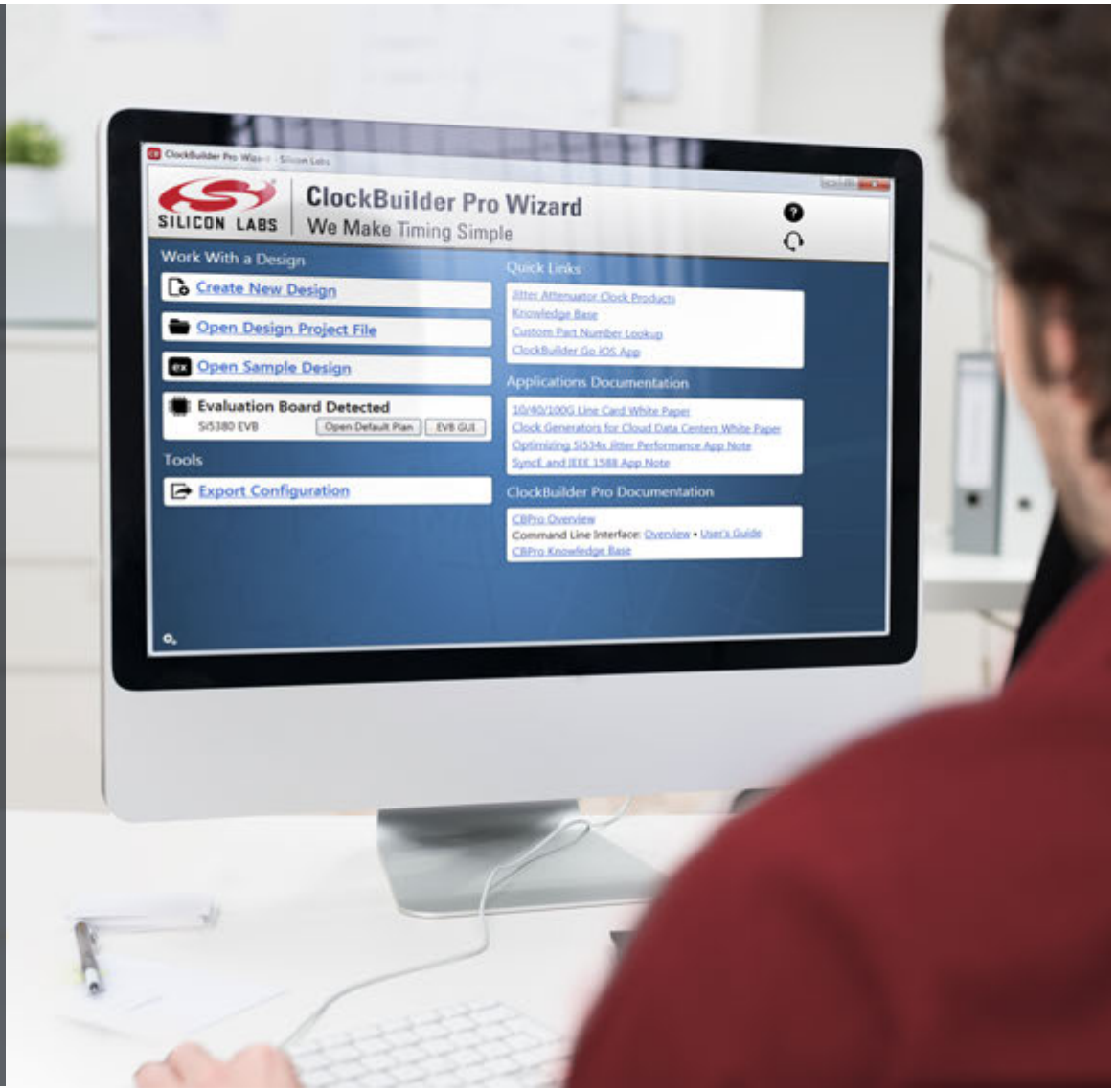
Resolution

This erratum will be fixed in the next silicon revision.

2. Document History

v1.0: July 2016

- Initial release of this document for Si5380-D errata, documented separately from other Si534x family members.



ClockBuilder Pro

One-click access to Timing tools, documentation, software, source code libraries & more. Available for Windows and iOS (CBGo only).

www.silabs.com/CBPro

Timing Portfolio
www.silabs.com/timing

SW/HW
www.silabs.com/CBPro

Quality
www.silabs.com/quality

Support and Community
community.silabs.com

Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701
USA

http://www.silabs.com