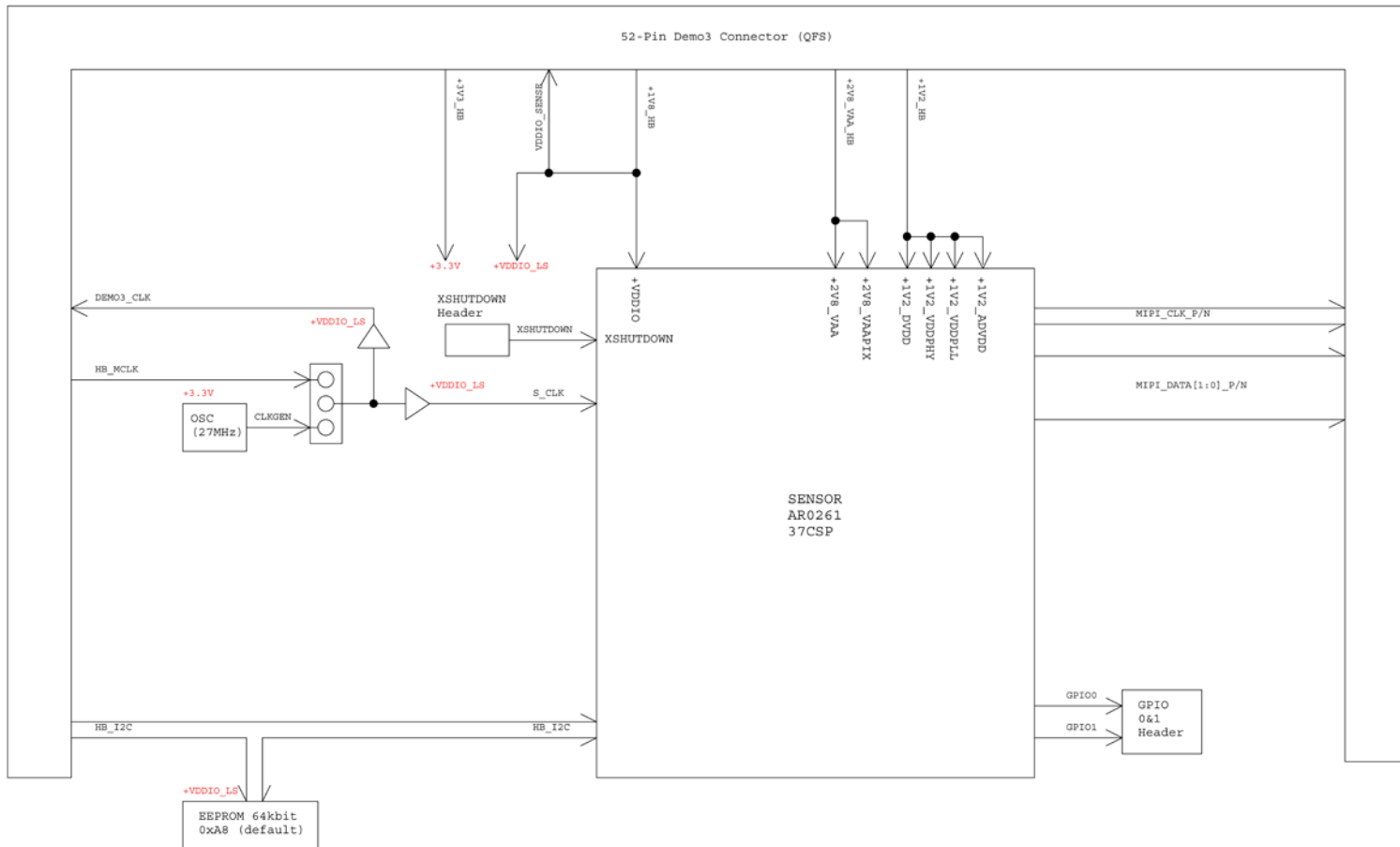


## Schematic for the AR0261CSSC30SMKAH3-GEVB Evaluation Board

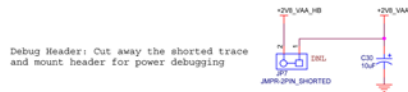
### Block Diagram





# Power

## ANALOG VAA 2.8V SUPPLY



## ANALOG VAAPIX 2.8V SUPPLY



## ADVDD 1.2V SUPPLY



## DVDD 1.2V SUPPLY



## VDDPHY 1.2V SUPPLY

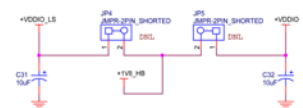


## VDDPLL 1.2V SUPPLY



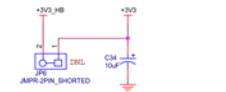
## VDDIO & VDDIO\_LS 1.8V / 2.8V SUPPLY

Debug Header: Cut away the shorted trace and mount header for power debugging



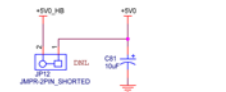
## PERIPHERAL 3.3V SUPPLY

Debug Header: Cut away the shorted trace and mount header for power debugging

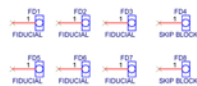
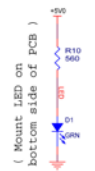


## PERIPHERAL 5V SUPPLY

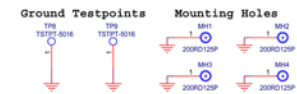
Debug Header: Cut away the shorted trace and mount header for power debugging



## 5V LED



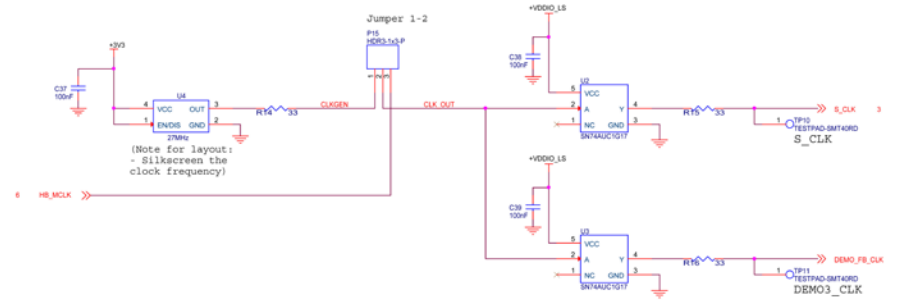
6	+5V_HB	+5V_HB
6	+2V_HB	+2V_HB
6	+2V_VAA_HB	+2V_VAA_HB
6	+1V_HB	+1V_HB
6	+1V2_HB	+1V2_HB
3	+2V_VAA	+2V_VAA
3	+2V_VAPIX	+2V_VAPIX
3	+1V2_ADVDD	+1V2_ADVDD
3	+1V2_DVDD	+1V2_DVDD
3	+1V2_VDDPHY	+1V2_VDDPHY
3	+1V2_VDDPLL	+1V2_VDDPLL
3	+VDDIO	+VDDIO
3	+5V	+5V
3,6	+3V	+3V
3,6	+VDDIO_LS	+VDDIO_LS





# Clock and Reset

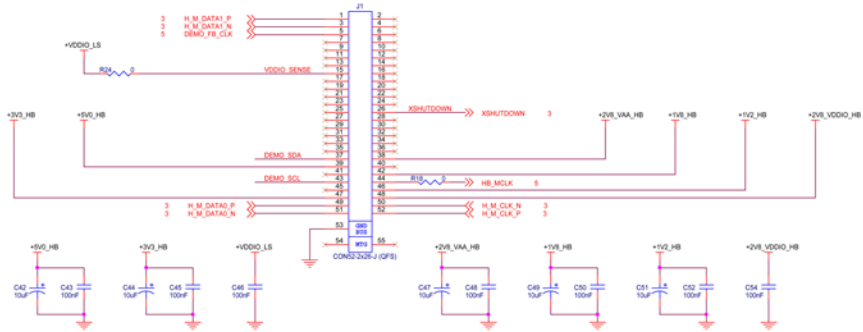
CLOCK CIRCUIT



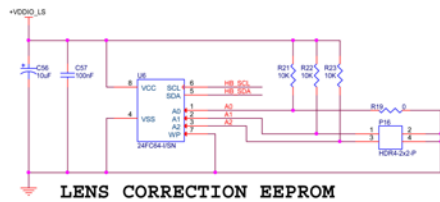
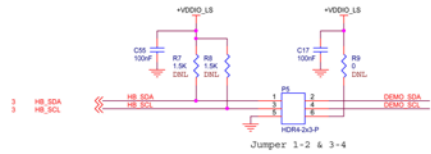
4,8	+V0_HB		+V0_HB
4,8	+3V0_HB		+3V0_HB
4,8	+2V8_VAA_HB		+2V8_VAA_HB
4,8	+1V8_HB		+1V8_HB
4,8	+1V2_HB		+1V2_HB
3,4	+2V8_VAA		+2V8_VAA
3,4	+2V8_VAARX		+2V8_VAARX
3,4	+1V2_ADVDD		+1V2_ADVDD
3,4	+1V2_DVDD		+1V2_DVDD
3,4	+1V2_VDDPHY		+1V2_VDDPHY
3,4	+1V2_VDDPLL		+1V2_VDDPLL
3,4	+VDD0		+VDD0
3,4	+V0		+V0
3,4	+3V0		+3V0
3,4,8	+VDD0_L15		+VDD0_L15

# External Interface

DEMO3 BASEBOARD I/F



I2C DEBUG



**EEPROM Address Switch Settings:**  
 A2 = HIGH, A1 = LOW, A0 = LOW; Address => 0x88 (default)  
 A2 = HIGH, A1 = HIGH, A0 = LOW; Address => 0xA4  
 A2 = LOW, A1 = HIGH, A0 = LOW; Address => 0xA4  
 A2 = LOW, A1 = LOW, A0 = LOW; Address => 0xA0

